

FIG. 1

10

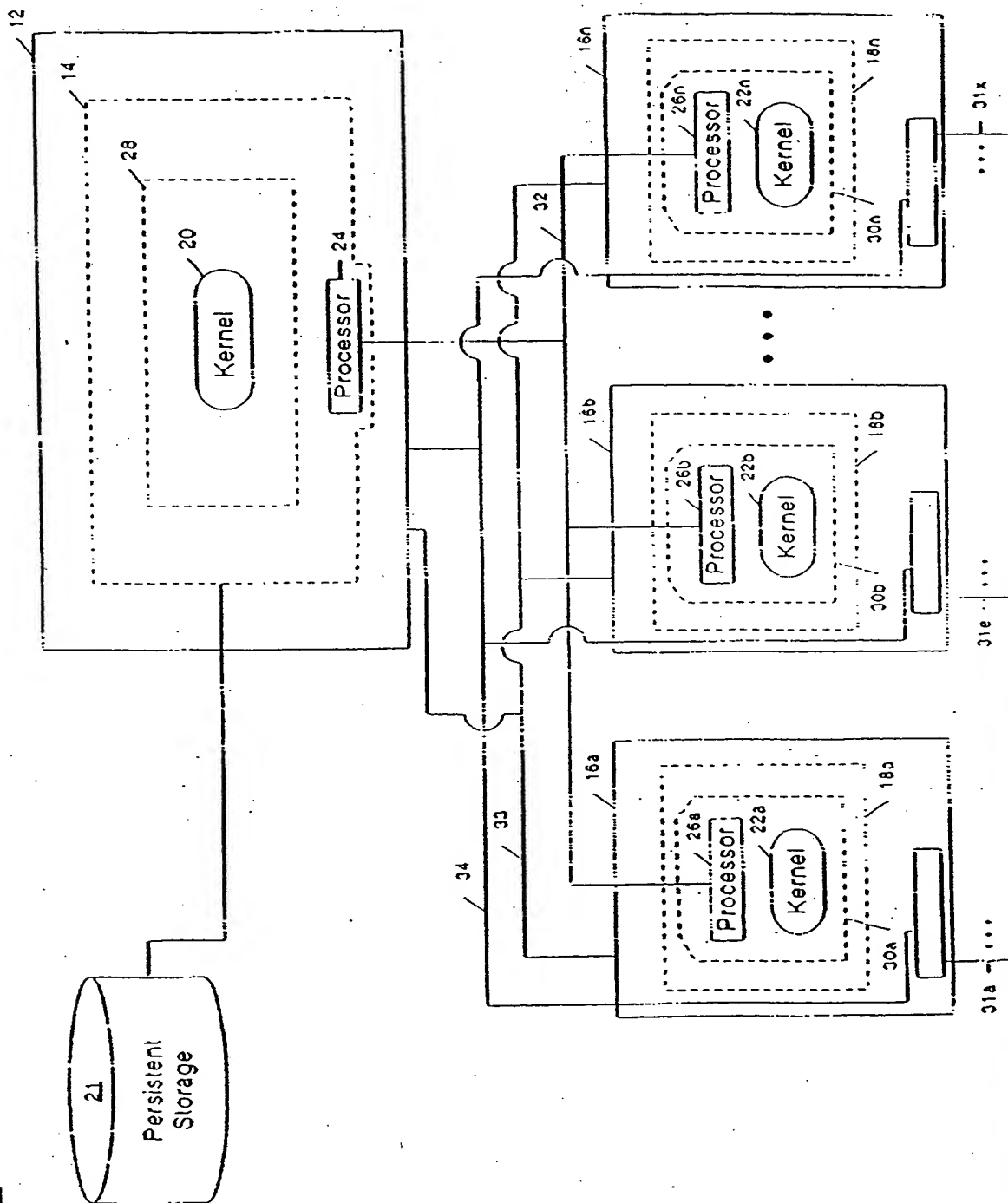


Fig. 2a

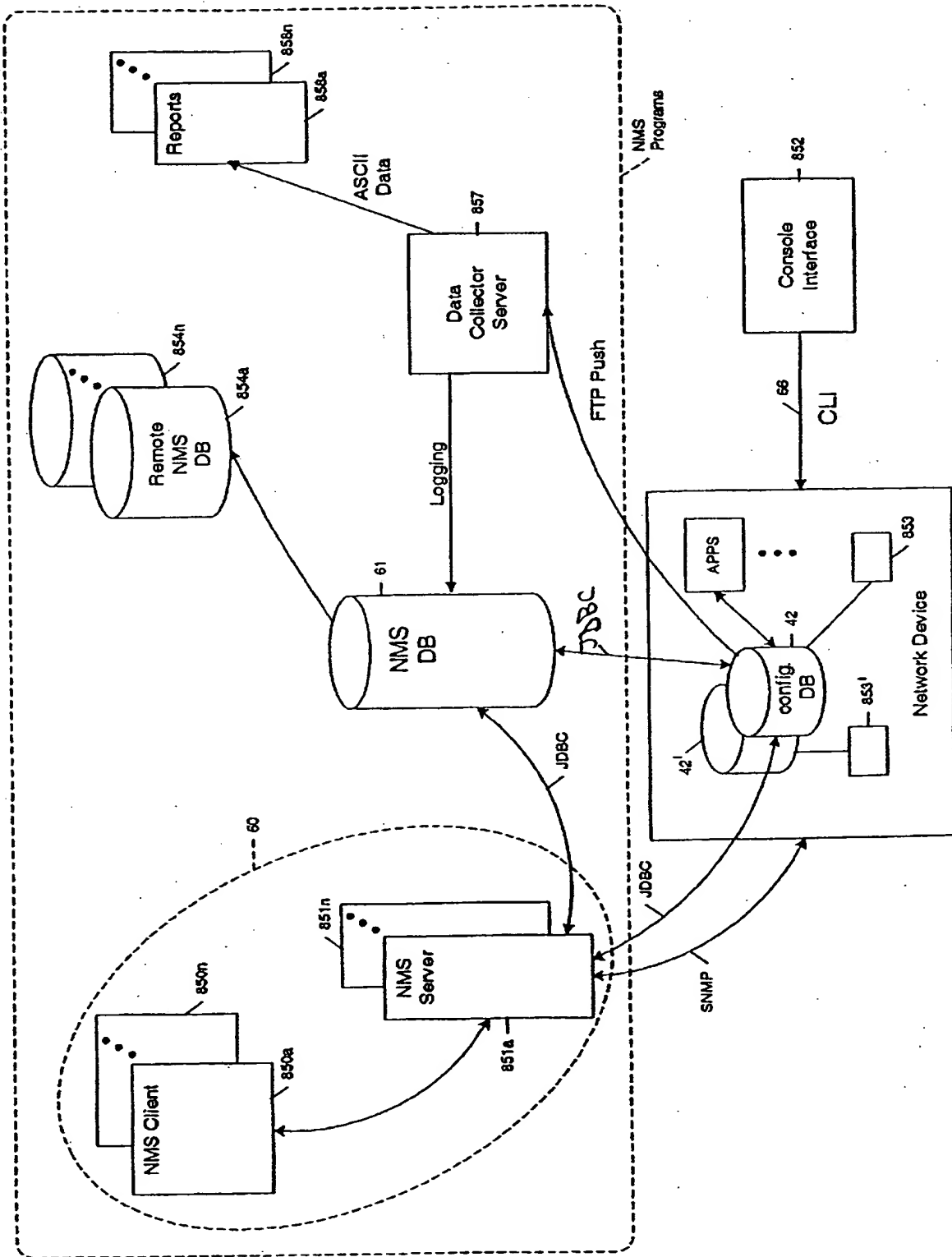


Fig. 2b

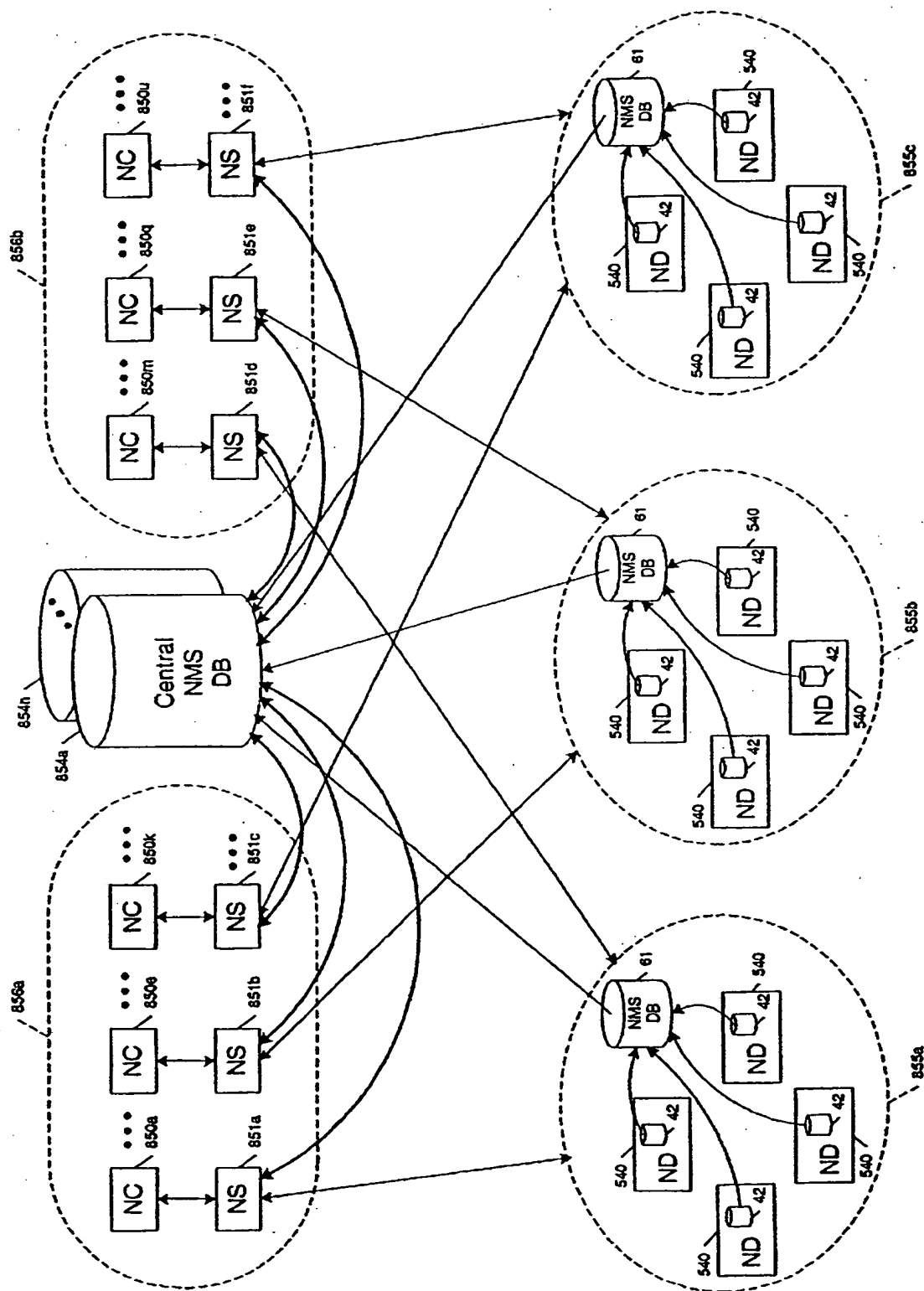


FIG. 3a

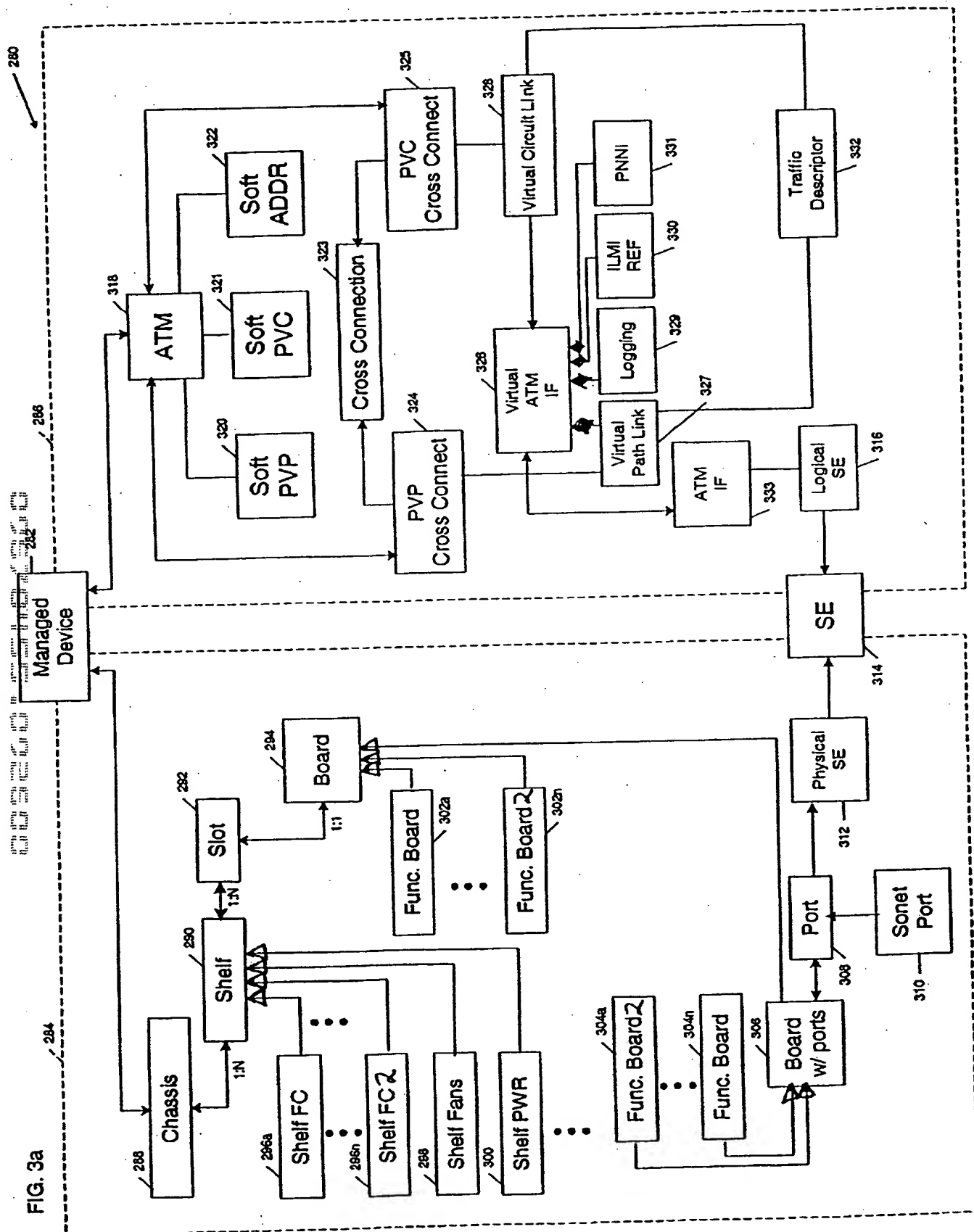
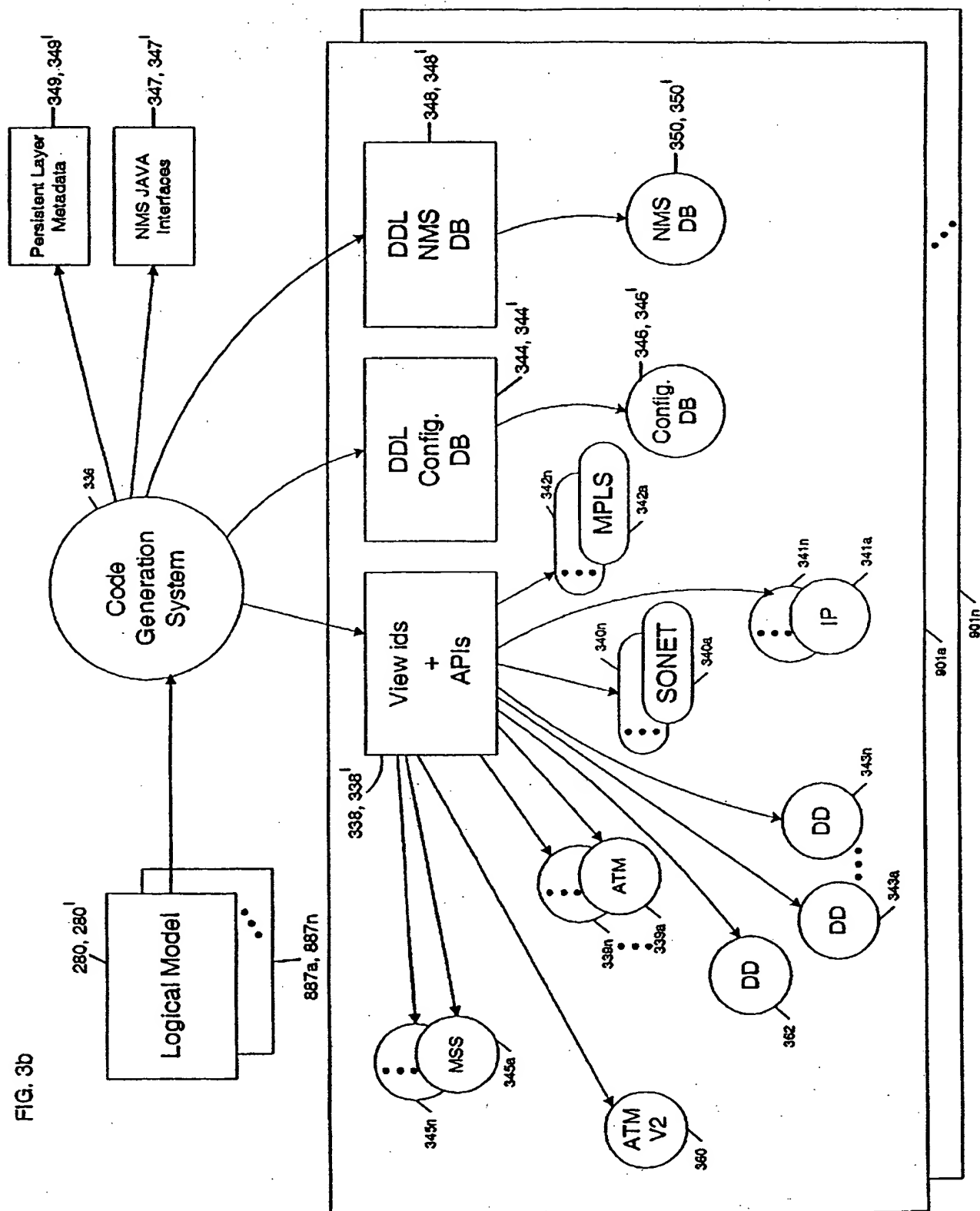




FIG. 3b



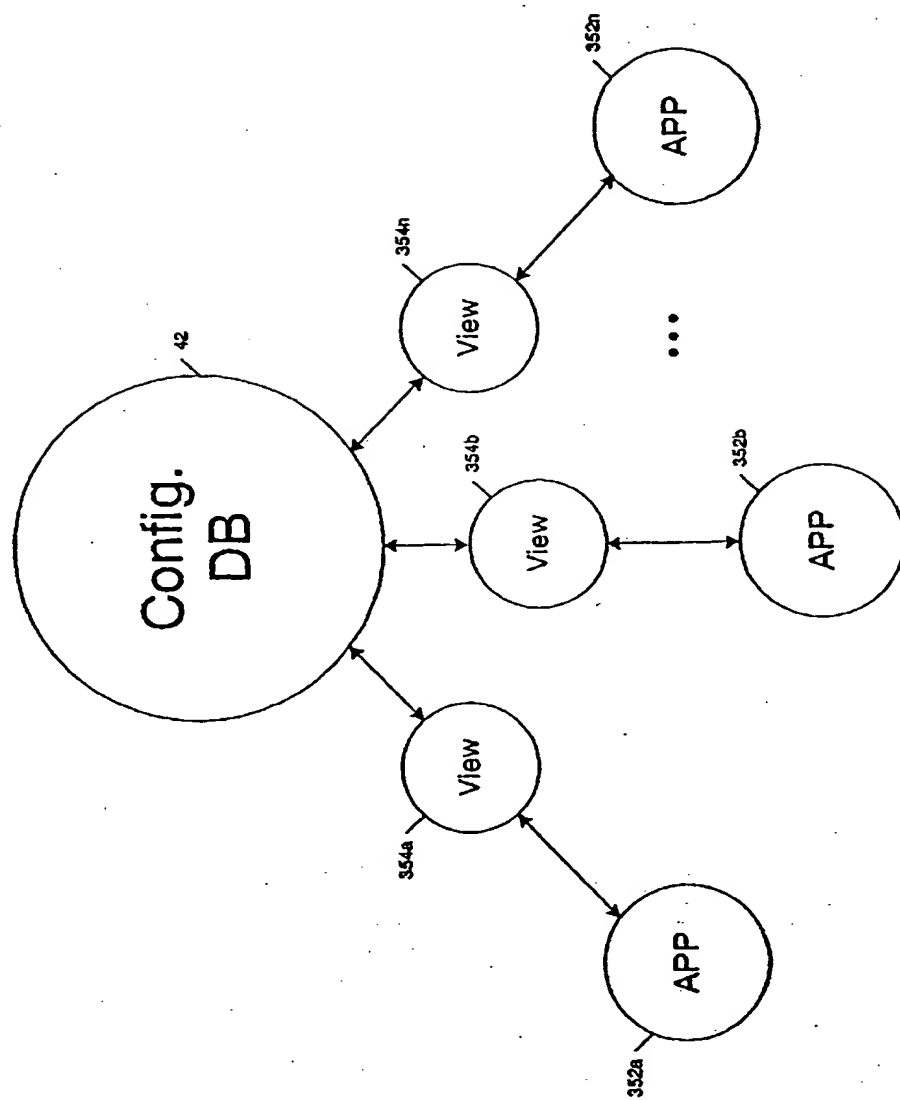
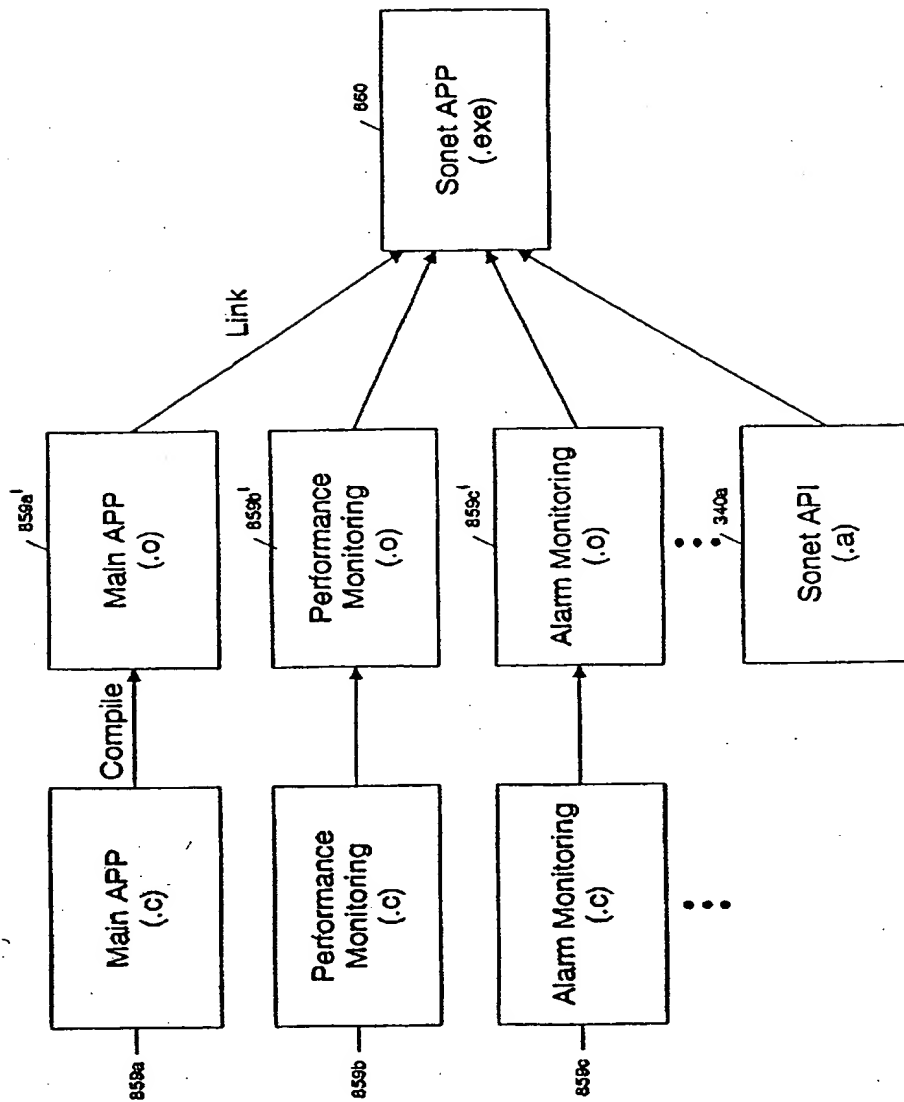


FIG. 3c

Fig. 3d

Build SONET Application



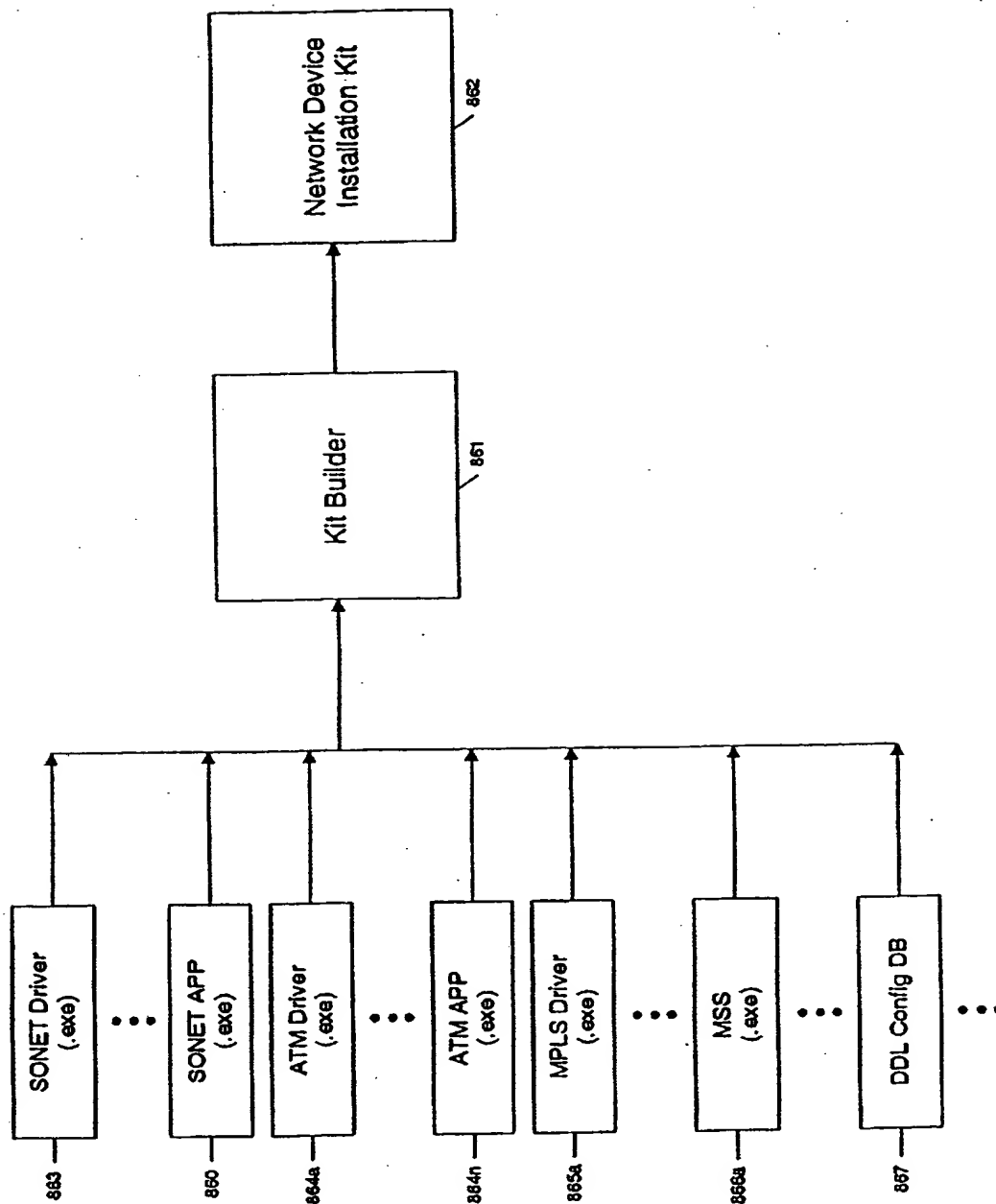


Fig. 3e

Fig. 3R

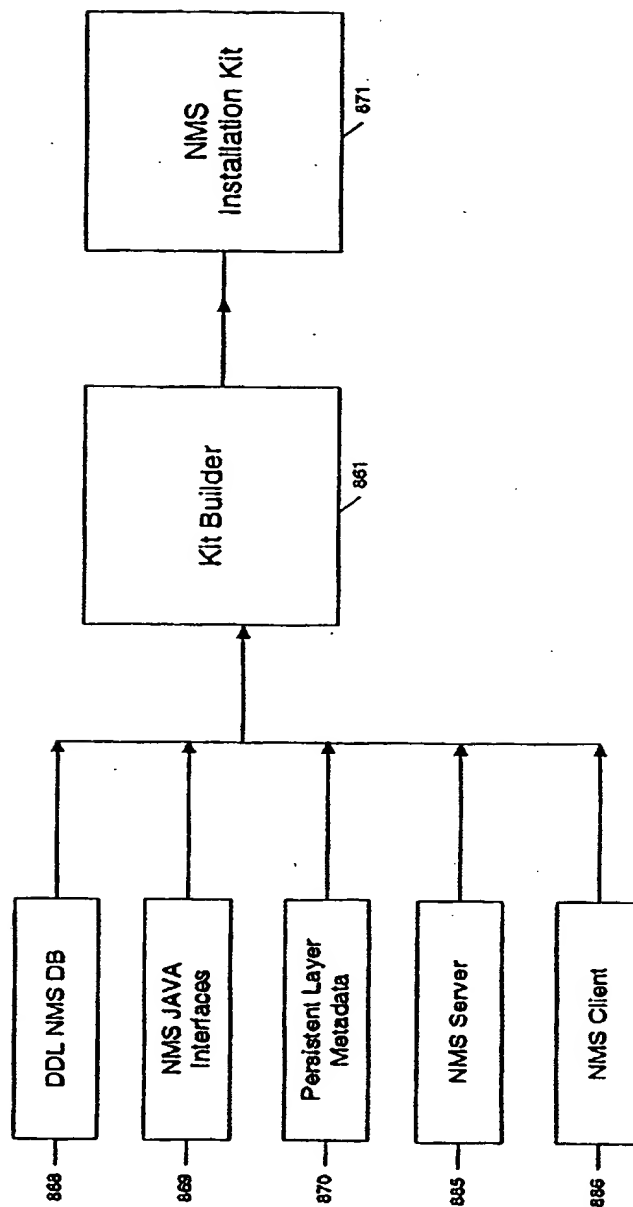


Fig. 3g

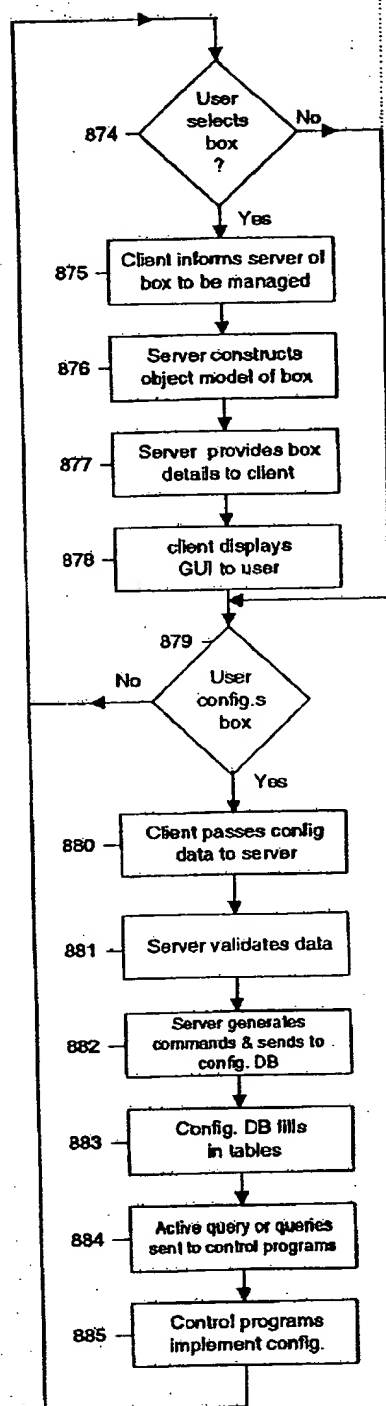
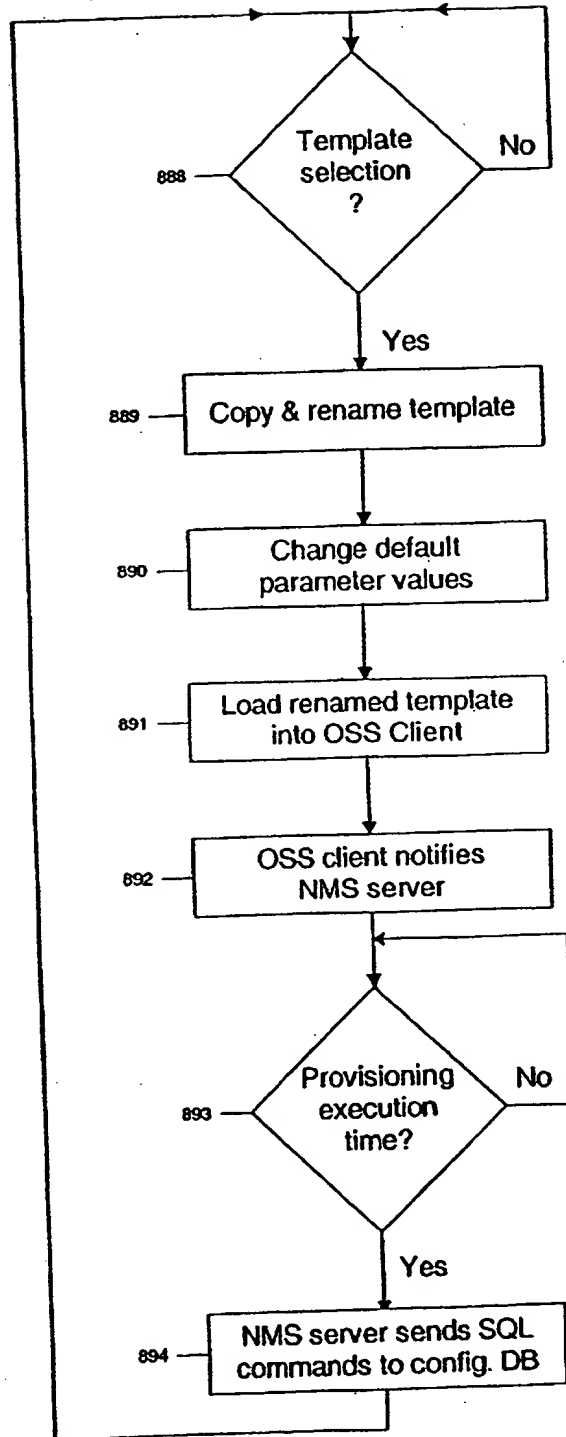


Fig. 3h



912

```
Command Prompt (2) - enetcli
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli> help
Commands are:
bye
close
execute
help
load
manage
open
quit
showCurrent
showTemplate
set
status
writeCurrent
writeTemplate
Enetcli>
Enetcli>
914 Enetcli> showCurrent SPATH
      AIMifName=AIMif11/1/1
      Concatenated=false
      Name=Path11/1/1
      Operant=SPATH
      Operator=Create
      PortID=1
      Position=1
      Service=AIM
      ShelfID=11
      SlotID=1
      Type=Terminated
      Version=U1_1_0_0
      Width=STS3
Enetcli>
Enetcli>
Enetcli>
916 Enetcli> showTemplate SPATH
      AIMifName=<String>[TerminatedOnly]
      Concatenated=<true:false>
      Name=<String>
      Operant=SPATH
      Operator=<Create|Replace|Update|Delete>
      PortID=<Integer><1-16>
      Position=<Integer>
      Service=<None|AIM>
      ShelfID=<11[top]13[bottom]>
      SlotID=<Integer><1-8>
      Type=<Switched|Terminated>
      Version=U1_1_0_0
      Width=<STS1|STS3|STS12|STS48>
Enetcli>
Enetcli>
918 Enetcli> status
919 Not currently connected to server
Supporting templates: CONTROL, PUC, SPATH, SPUC, ID, and UAIF
920 Enetcli>
```

Fig. 3i



Fig. 3j

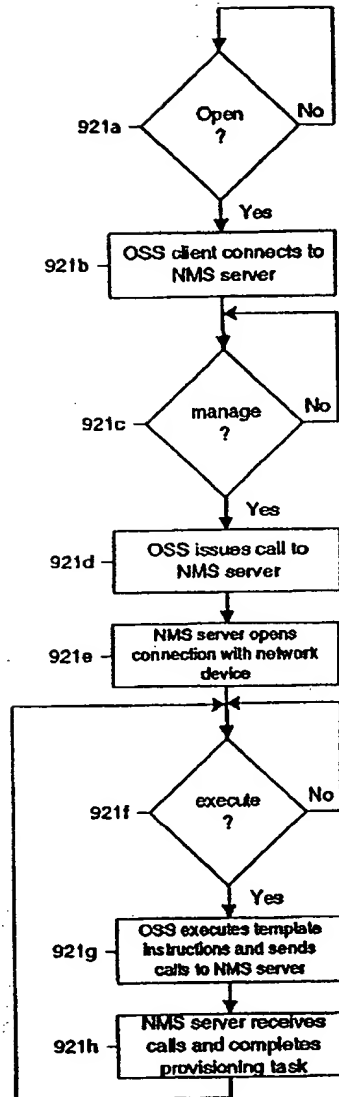


Fig. 3K

```

922- Command Prompt (2) - enetcli
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli>
Enetcli> showCurrent CONTROL
Input=Q:\nns\com\equipecon\nns\utils\enetcli
Interactive=false
Operant=CONTROL
Operator=Manage
923d- Output=Q:\nns\com\equipecon\nns\utils\enetcli
923f- Password=None
923c- System=192.168.9.202
923e- User=None
923b- Version=U1_1_0_0
923g- Server=localhost
923a- Enetcli>

```

Fig. 3L

← 924 BATCH

Operant=BATCH

Operator=Execute

Version=V1\_1\_0\_0

924a — Task1=execute-SPATH

924b — Task2=execute-PVC

924c — Task3=execute-SPVC

924d — Task4=load-SPVC-spvc1

924e — Task5=execute-SPVC

924f — Task6=load-SPVC-spvc2

924g — Task7=execute-SPVC

924h — Task50=set-SPATH-PortID-3

924i — Task51=execute-SPATH

924j — Task52=set-SPATH-SlotID-2

924k — Task53=execute-SPATH

Fig. 301

← 925

Operant=BATCH

Operator=Execute

Version=V1\_1\_0\_0

925a—Task1=execute-CONTROL

925b—Task2=execute-SPATH

925c—Task3=set-SPATH-PortID-3

925d—Task4=execute-SPATH

925e—Task61=set-CONTROL-System-192.168.9.201

925f—Task62=execute-CONTROL

925g—Task63=execute-SPATH

925h—Task108=close

925i—Task109=set-CONTROL-Server-Server1

925j—Task110=set-CONTROL-System-192.168.8.200

925k—Task111=execute-CONTROL

925l—Task112=execute-SPATH

895

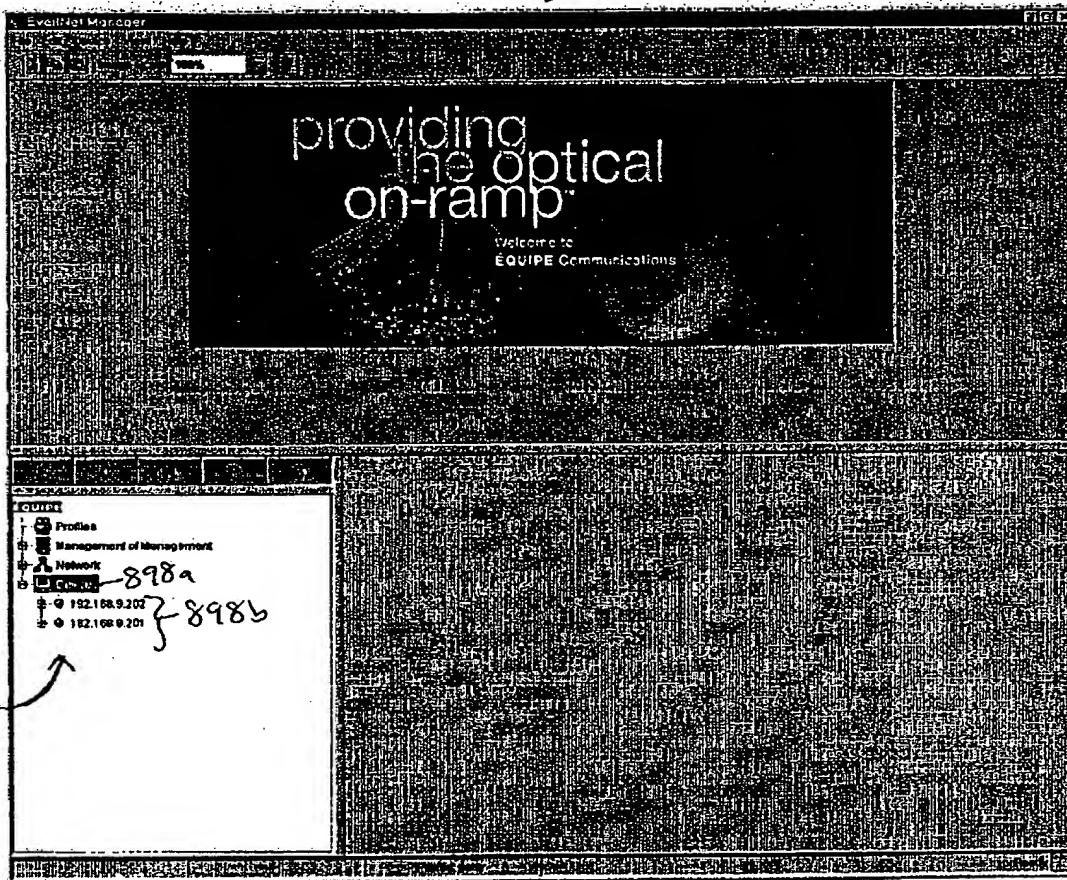


Fig. 4a

895

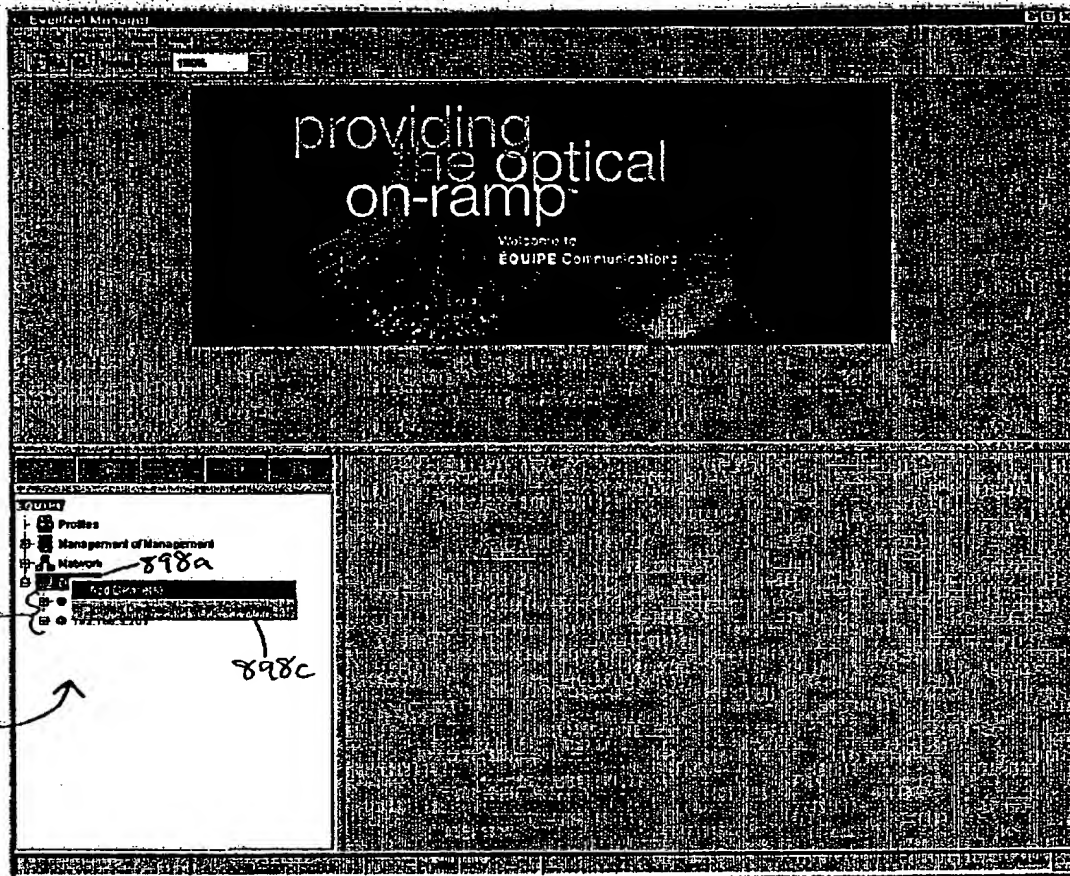


Fig. 4b

Fig. 4c

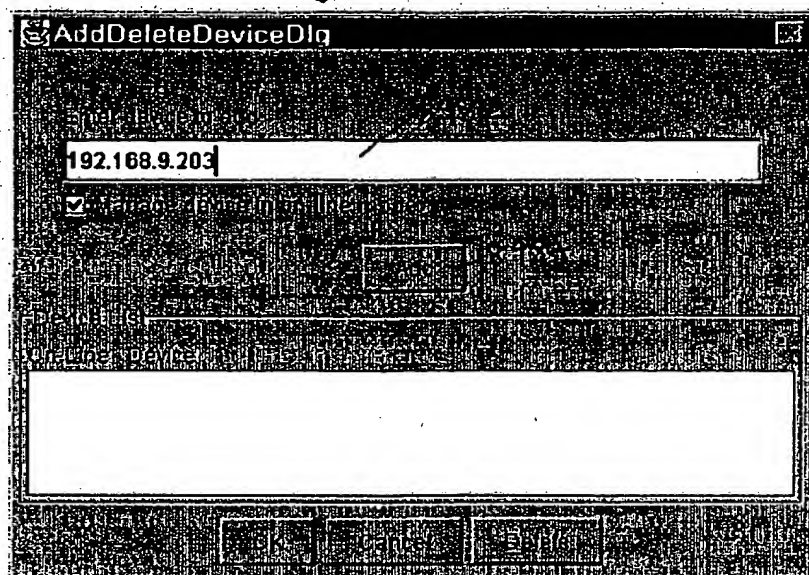
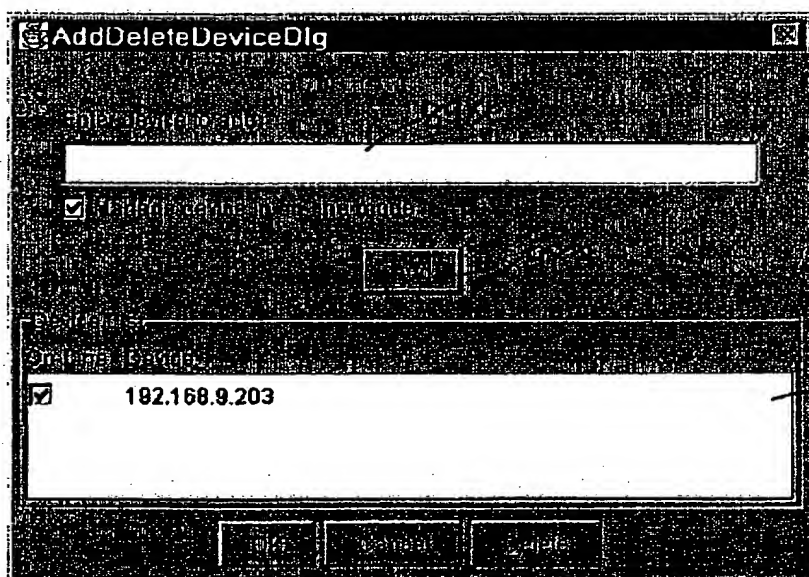


Fig. 4d



895

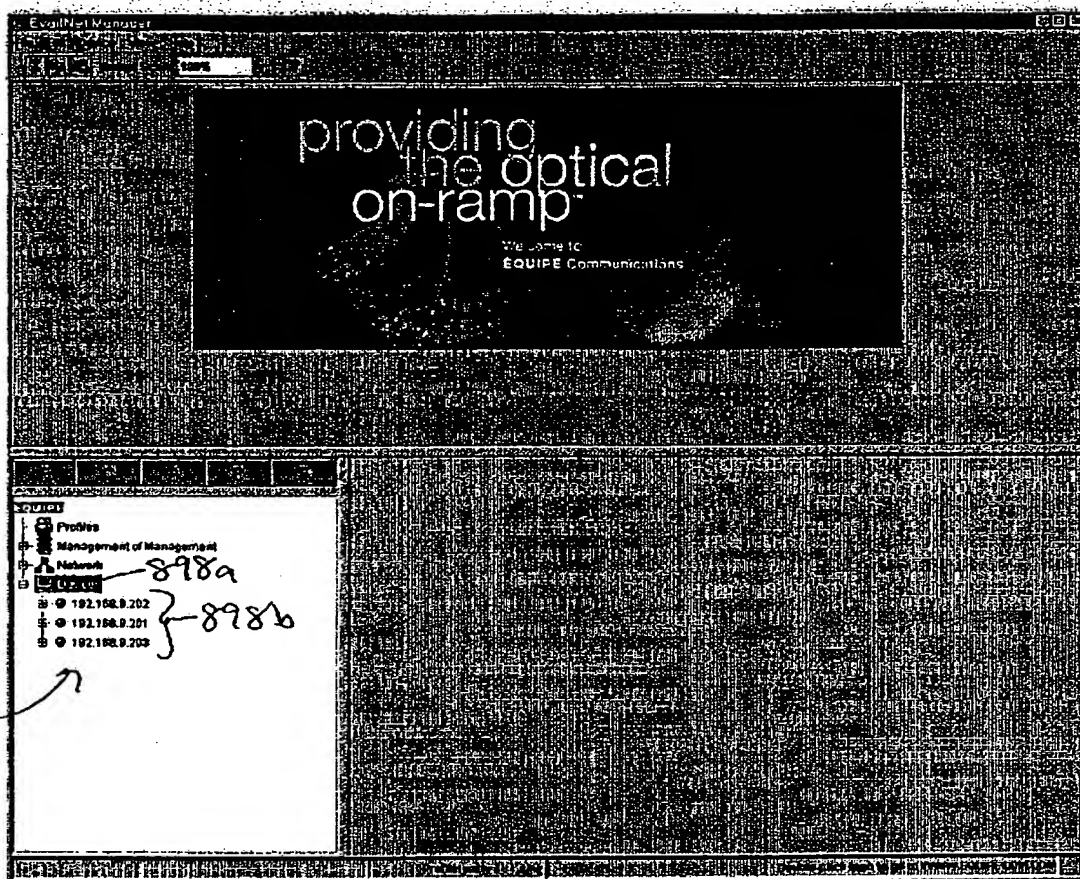


Fig. 4e



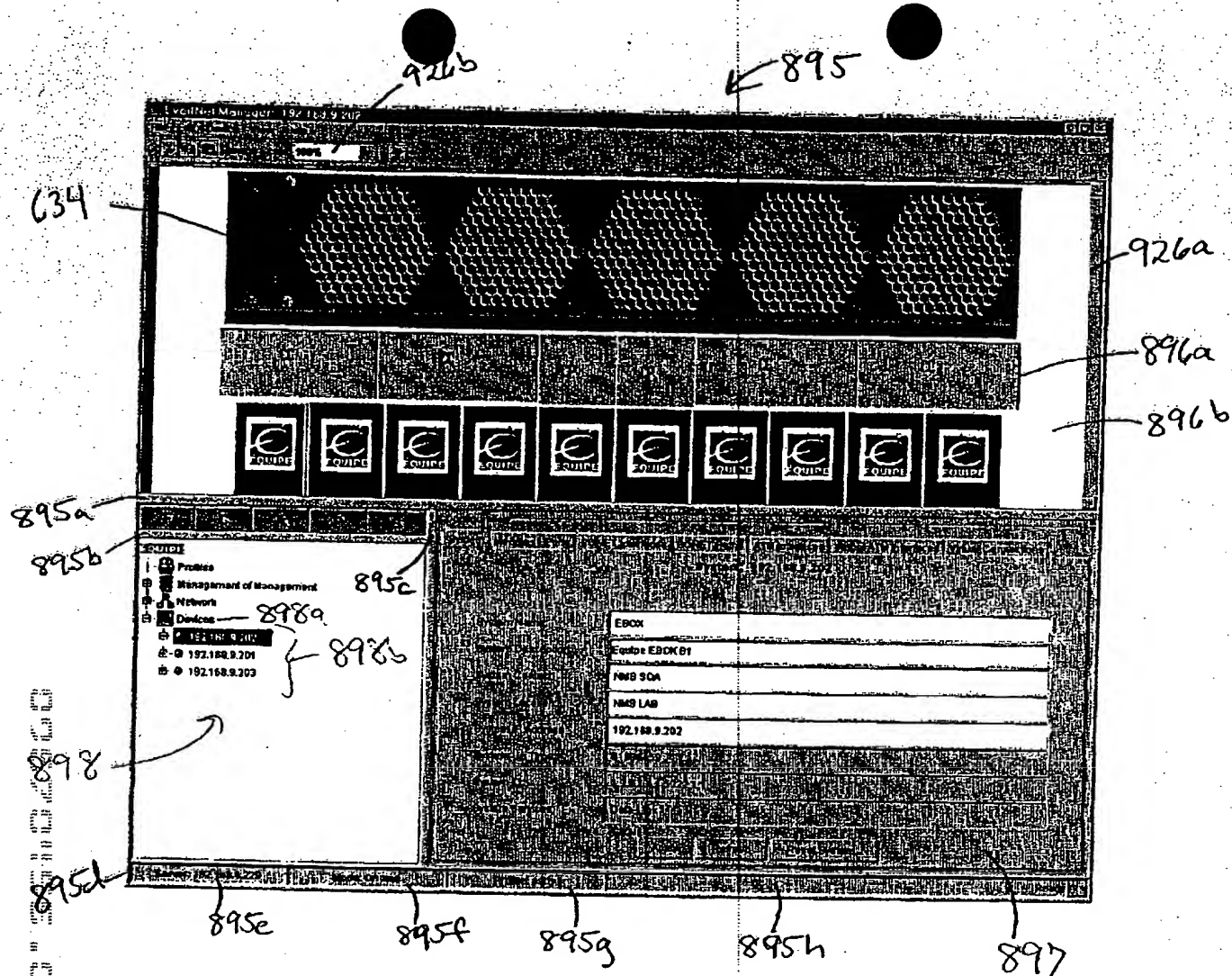


Fig. 4f

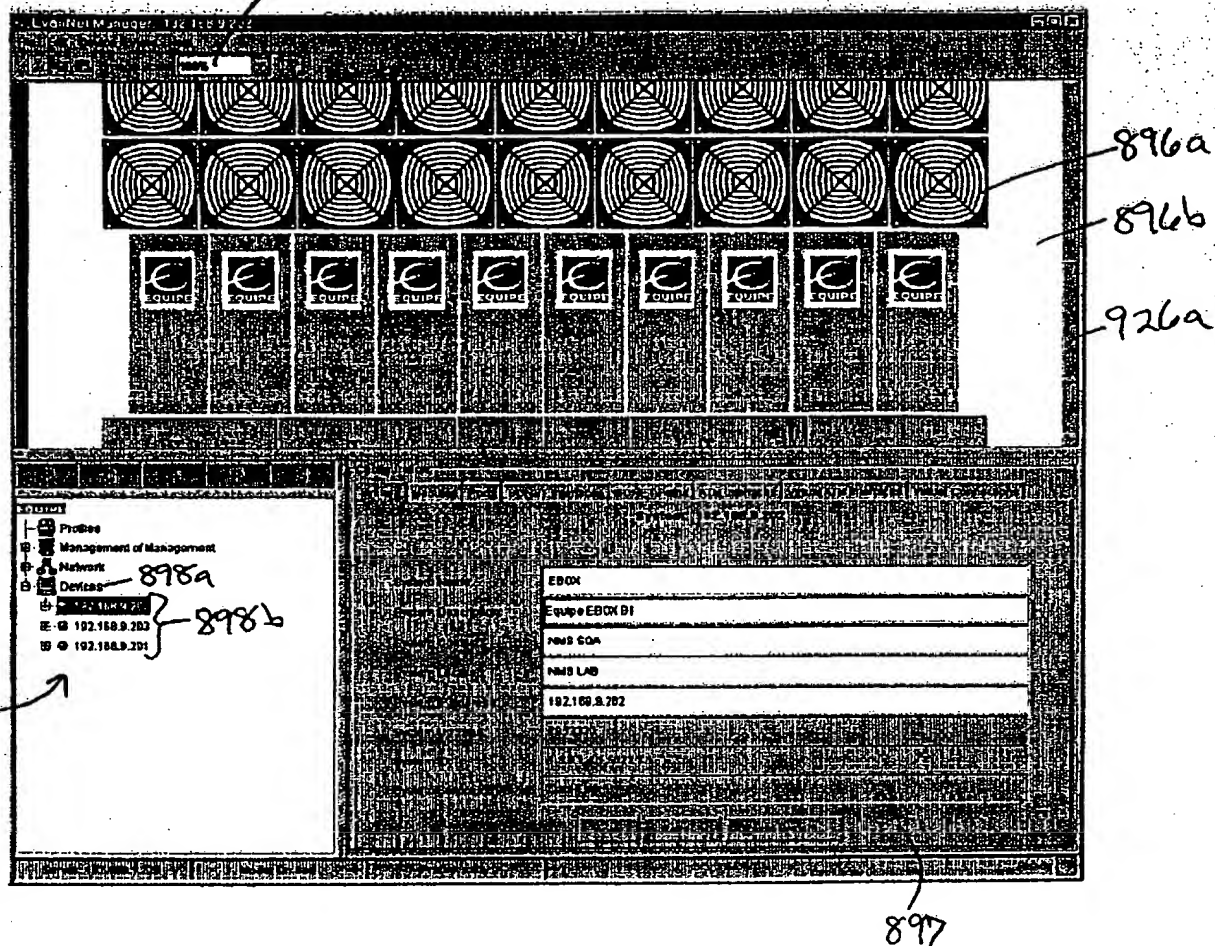


Fig. 4g

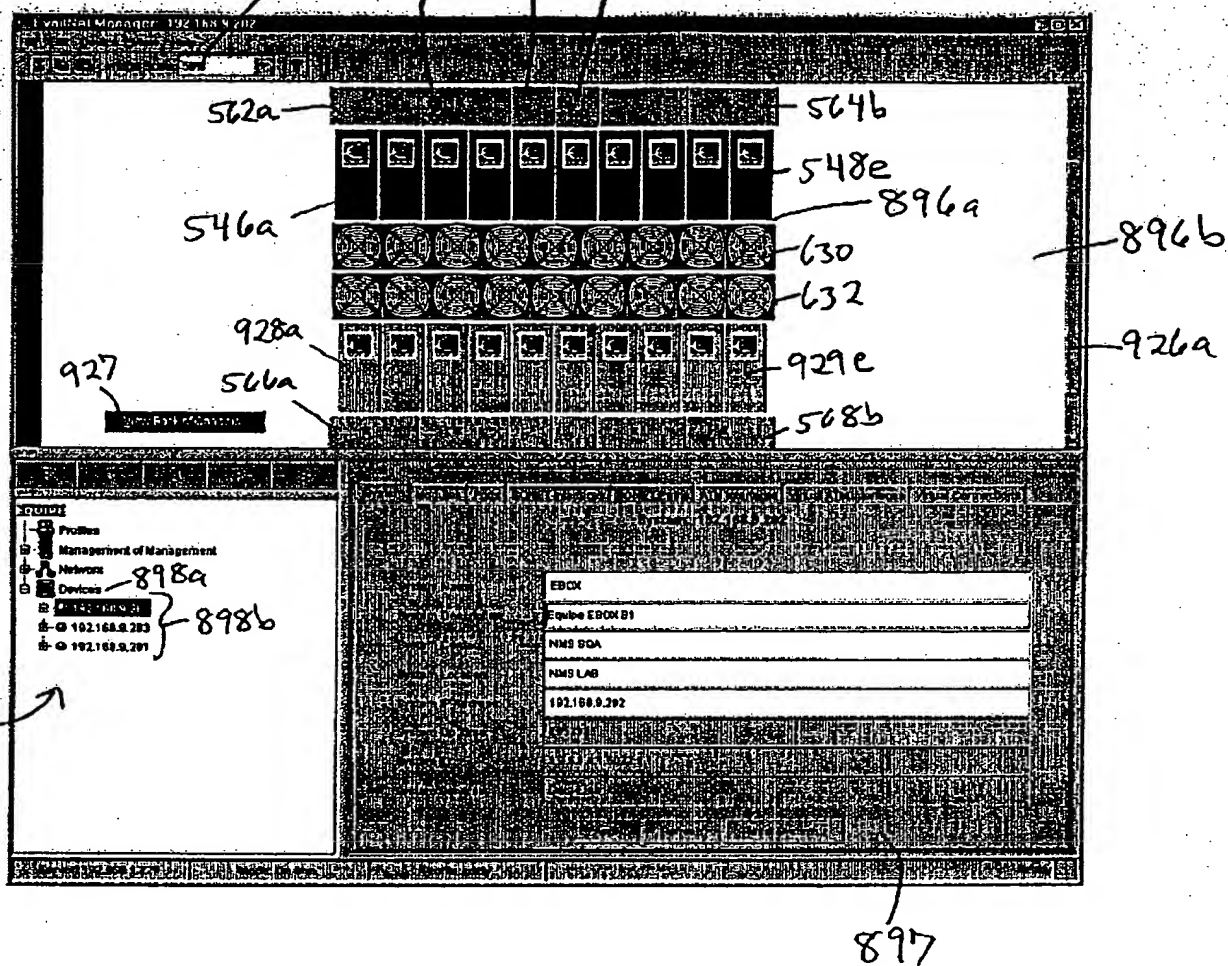


Fig. 4h

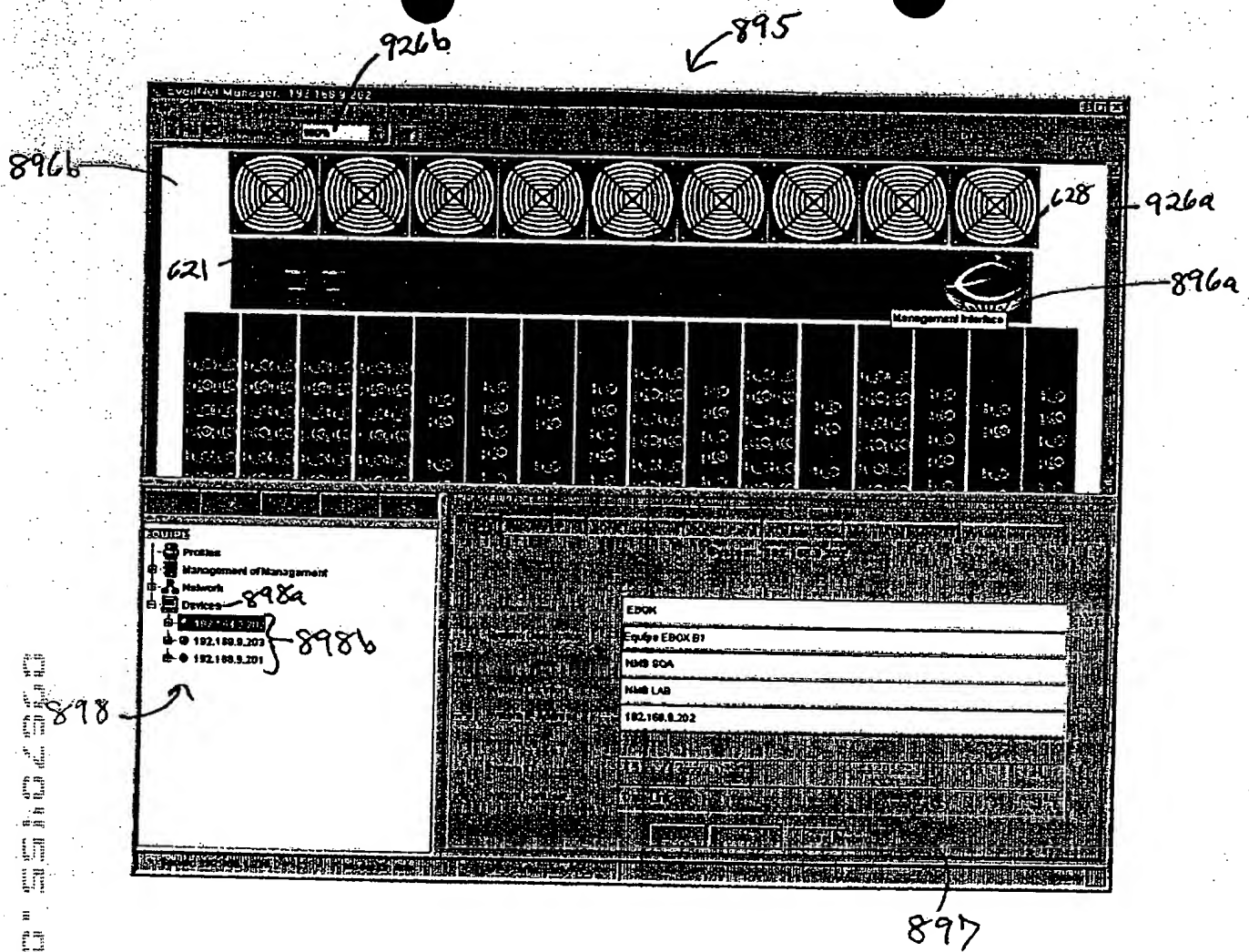


Fig. 4i

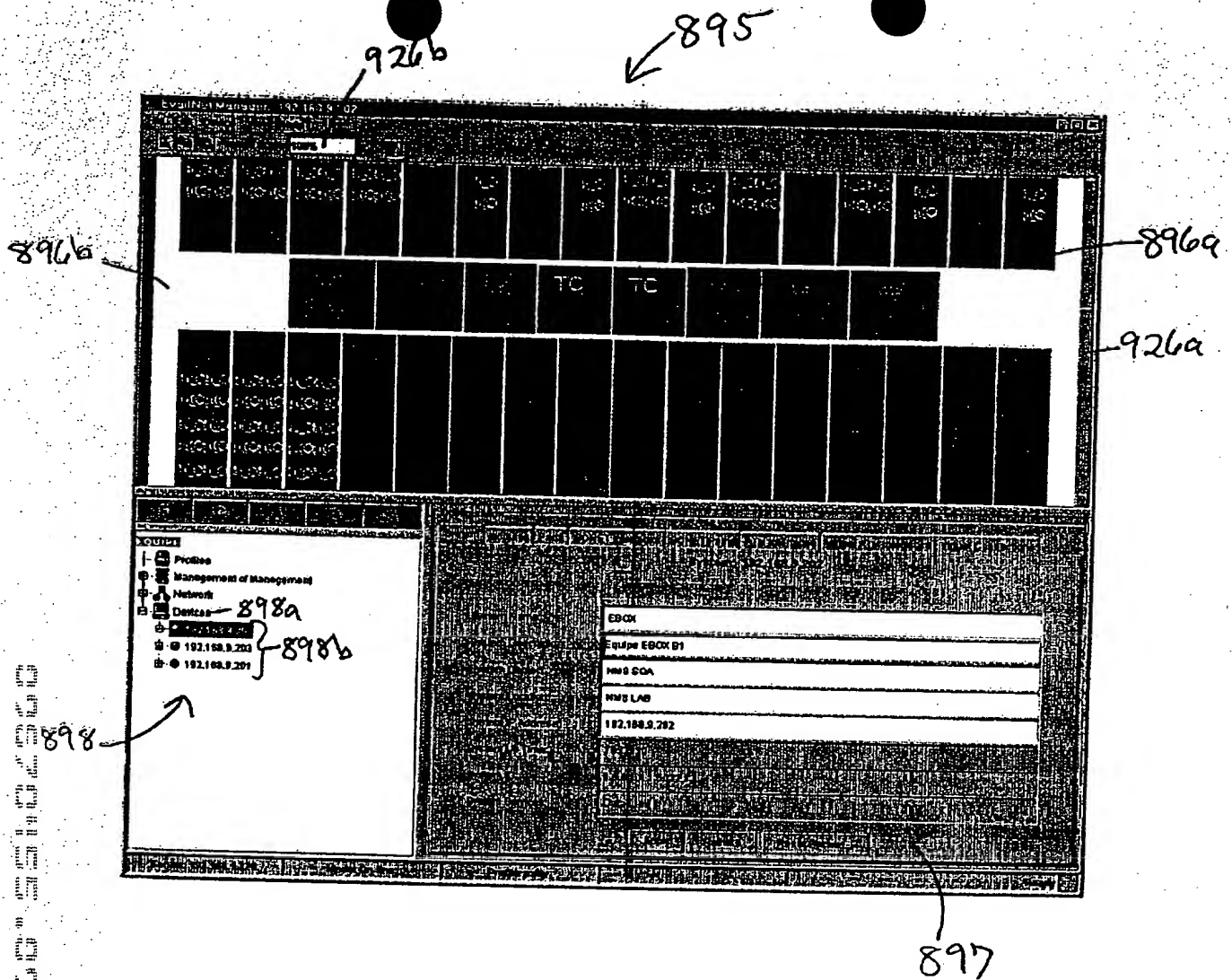


Fig. 4j

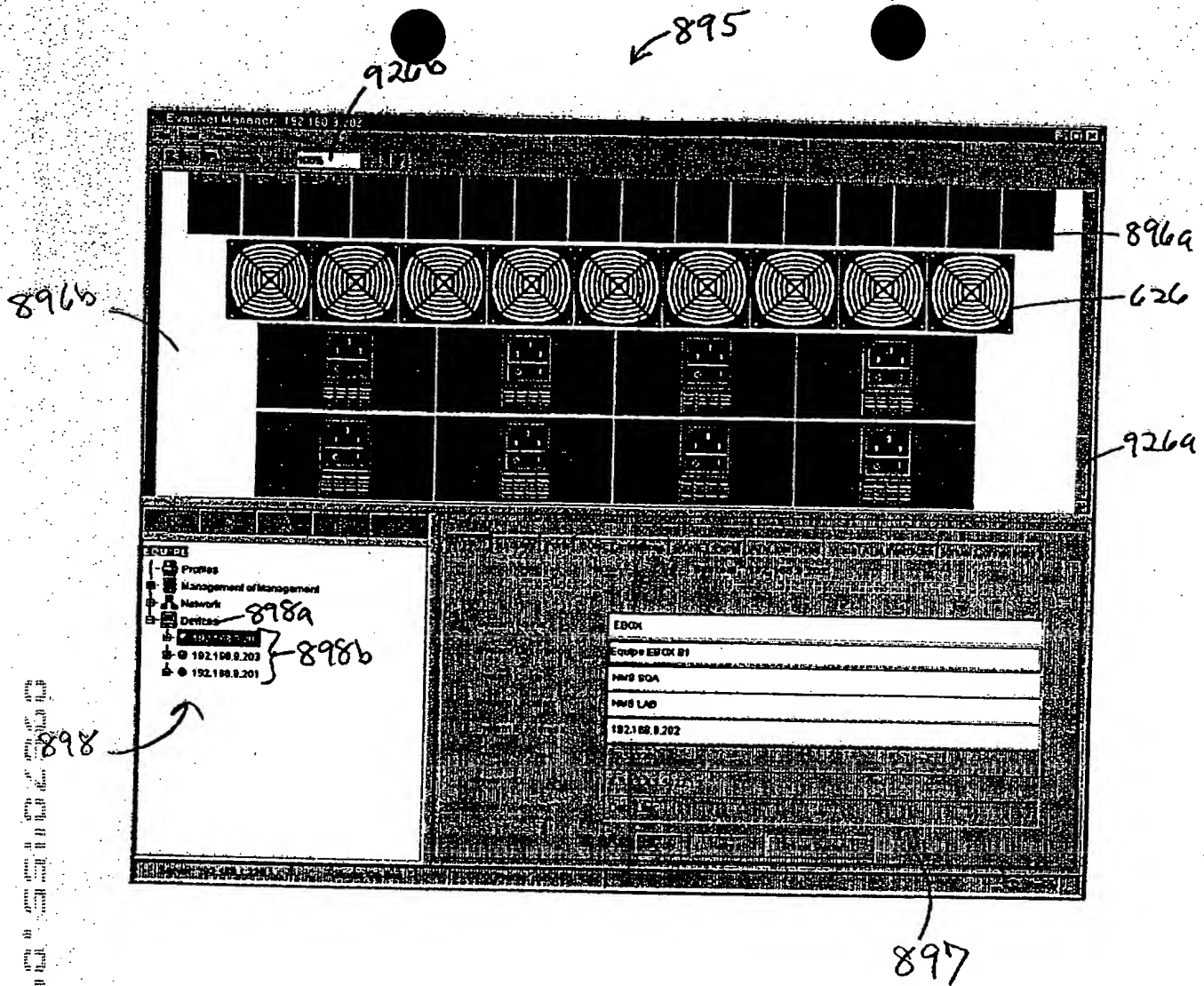


Fig. 4K

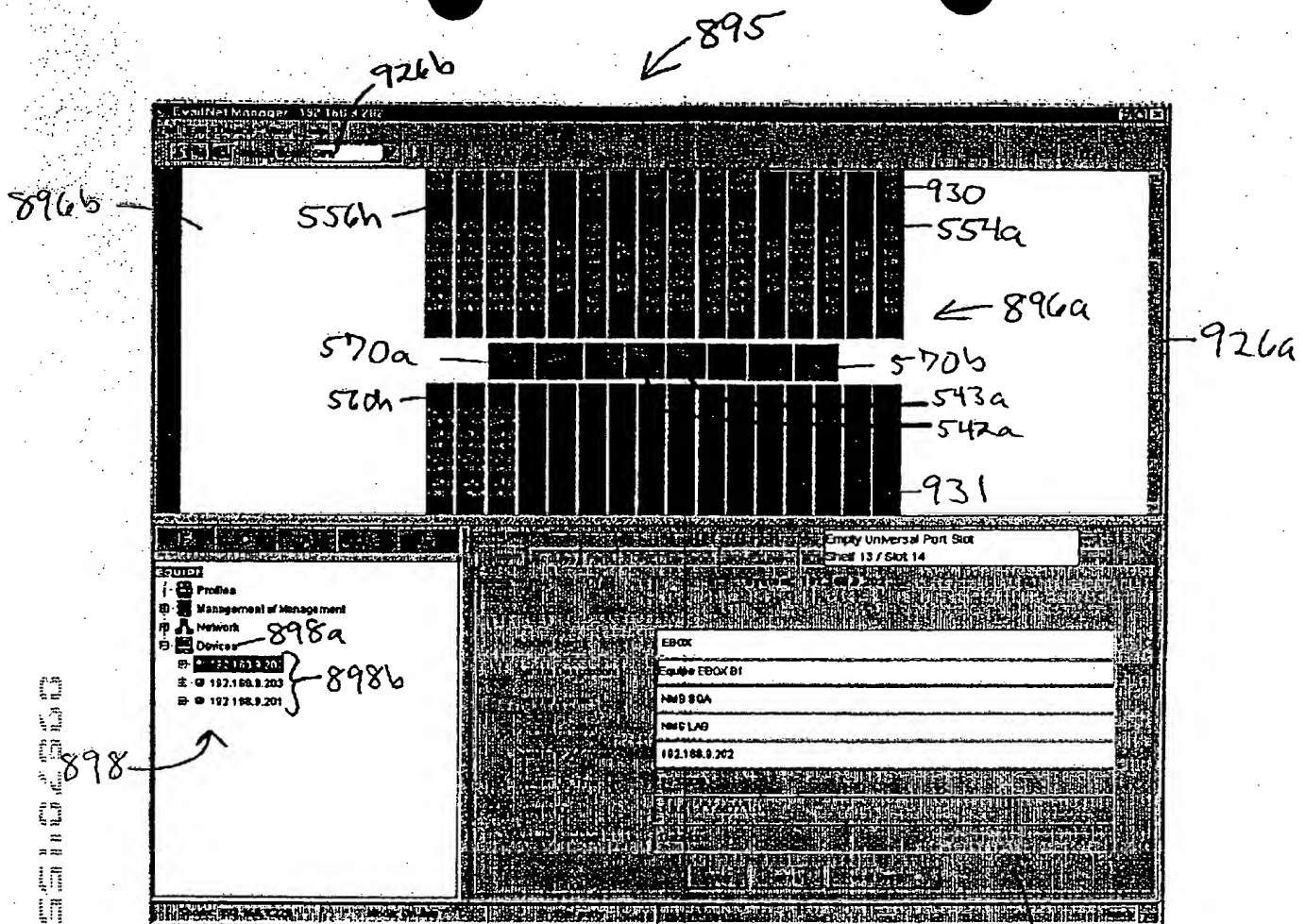


Fig. 4L



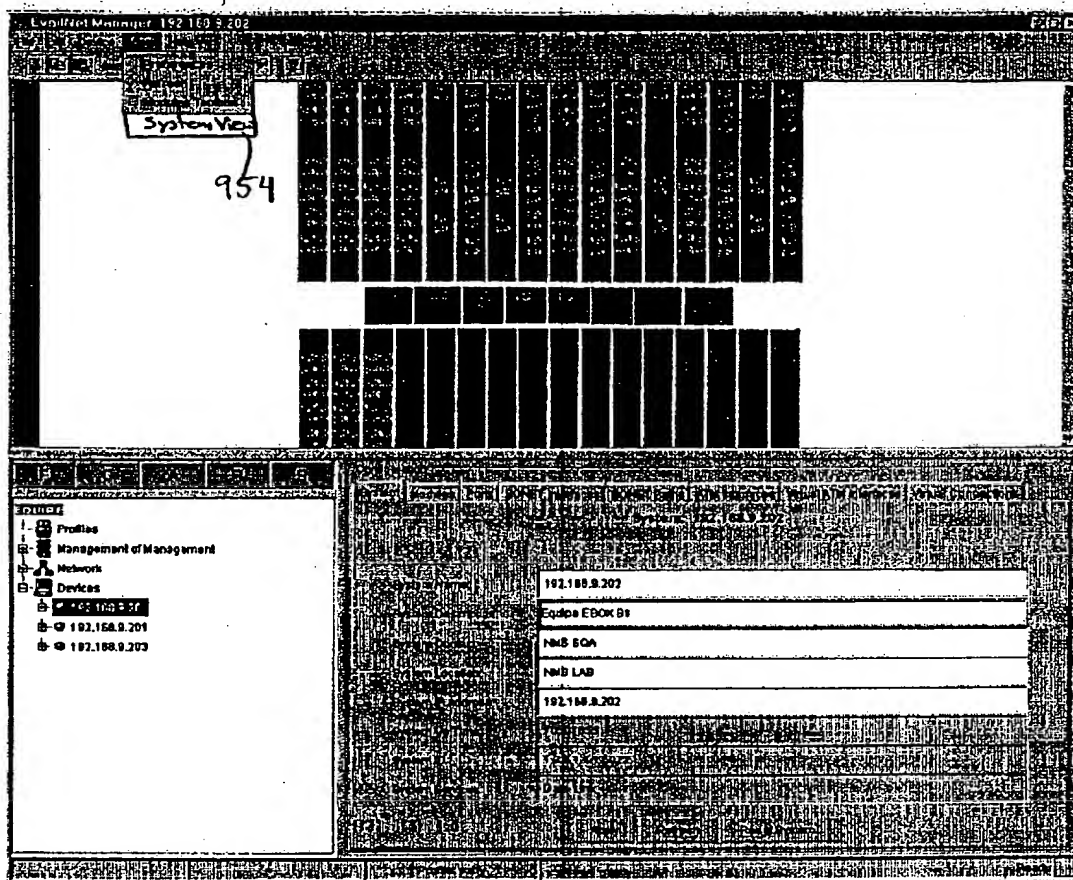
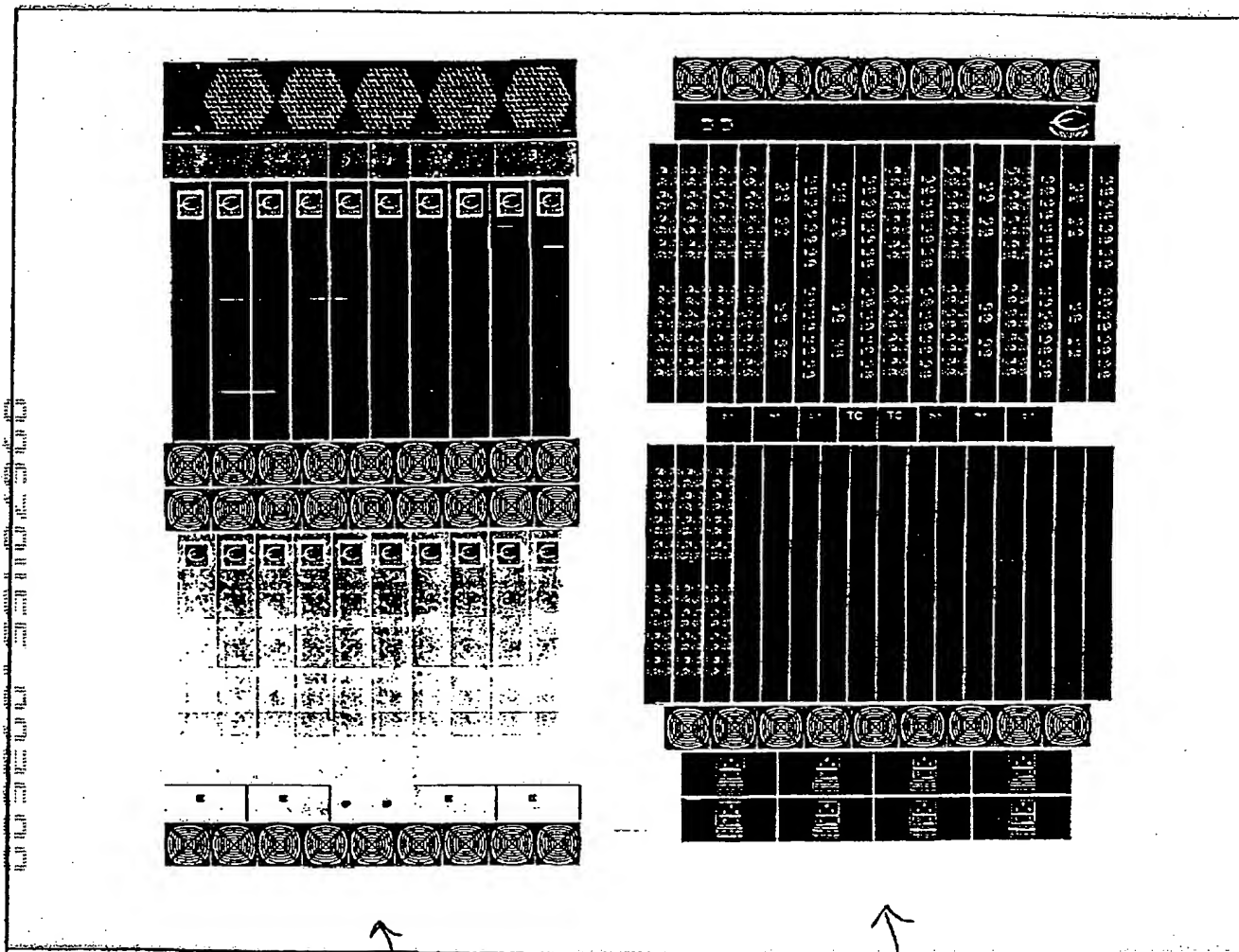


Fig. 4m



955



955a

955b

Fig. 4n

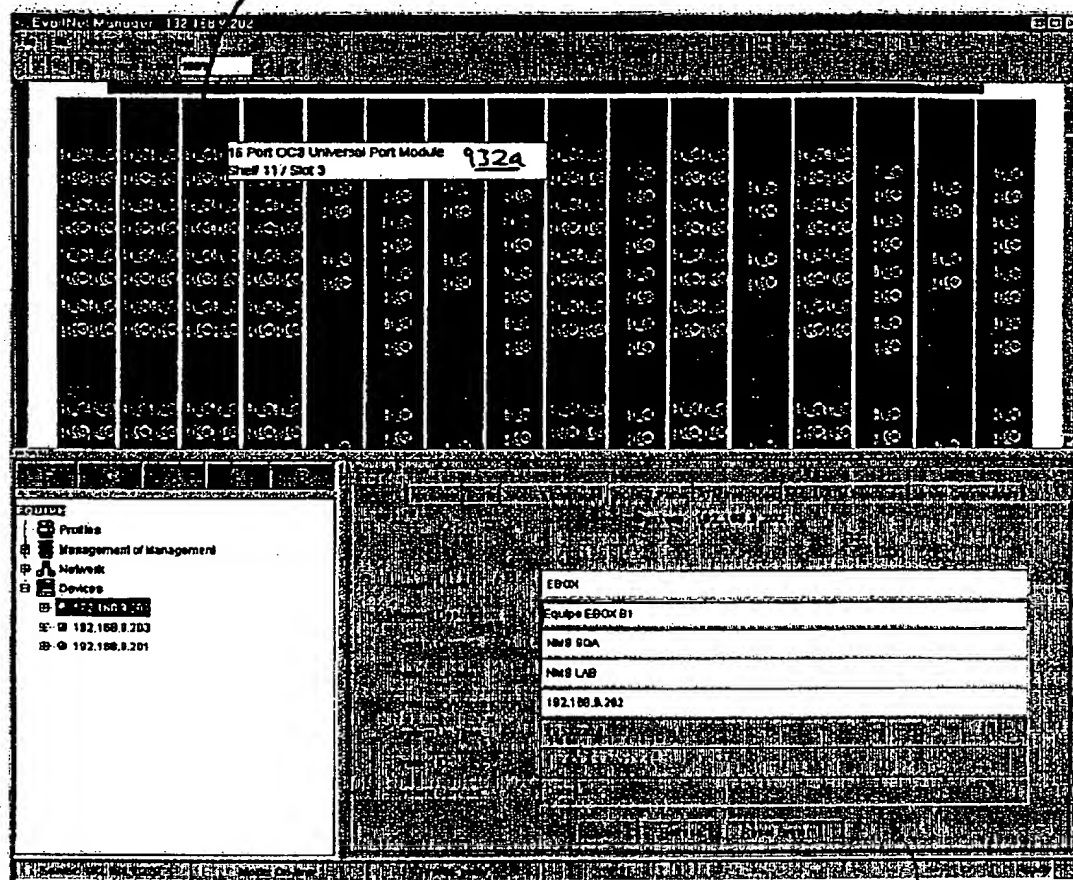


Fig. 40

897

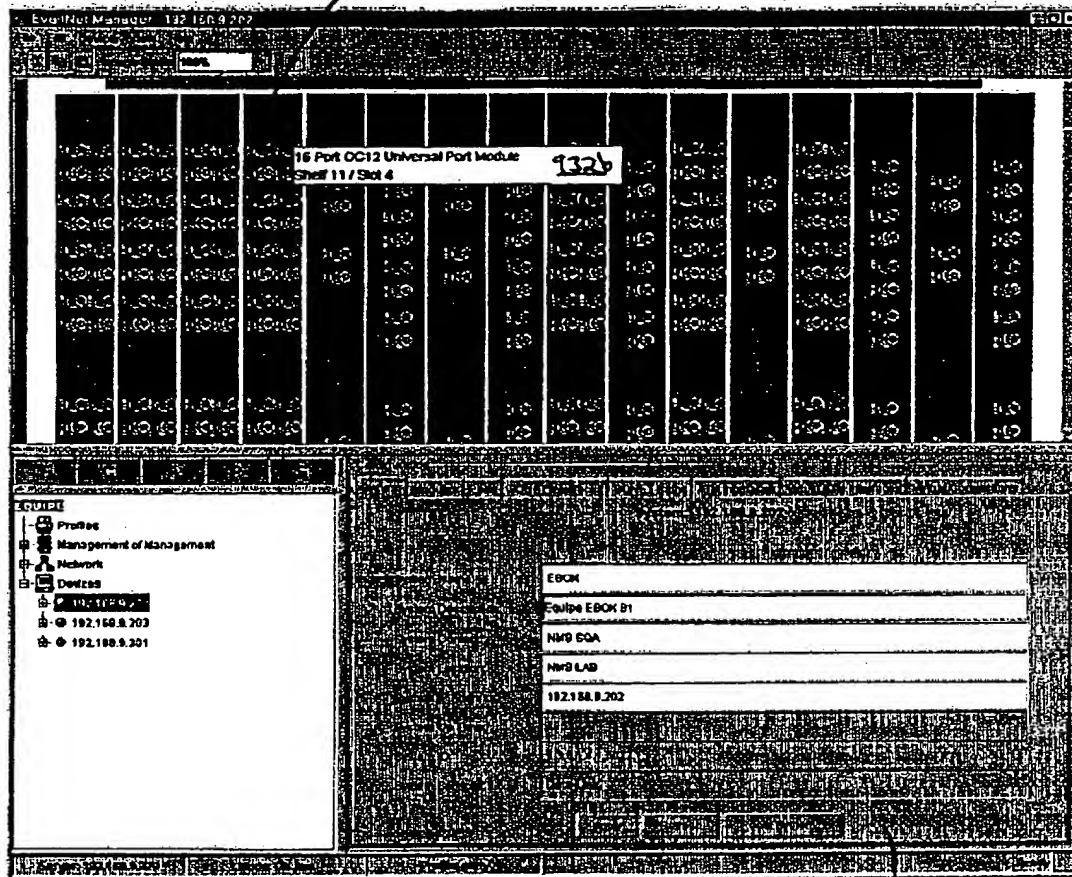


Fig. 4p

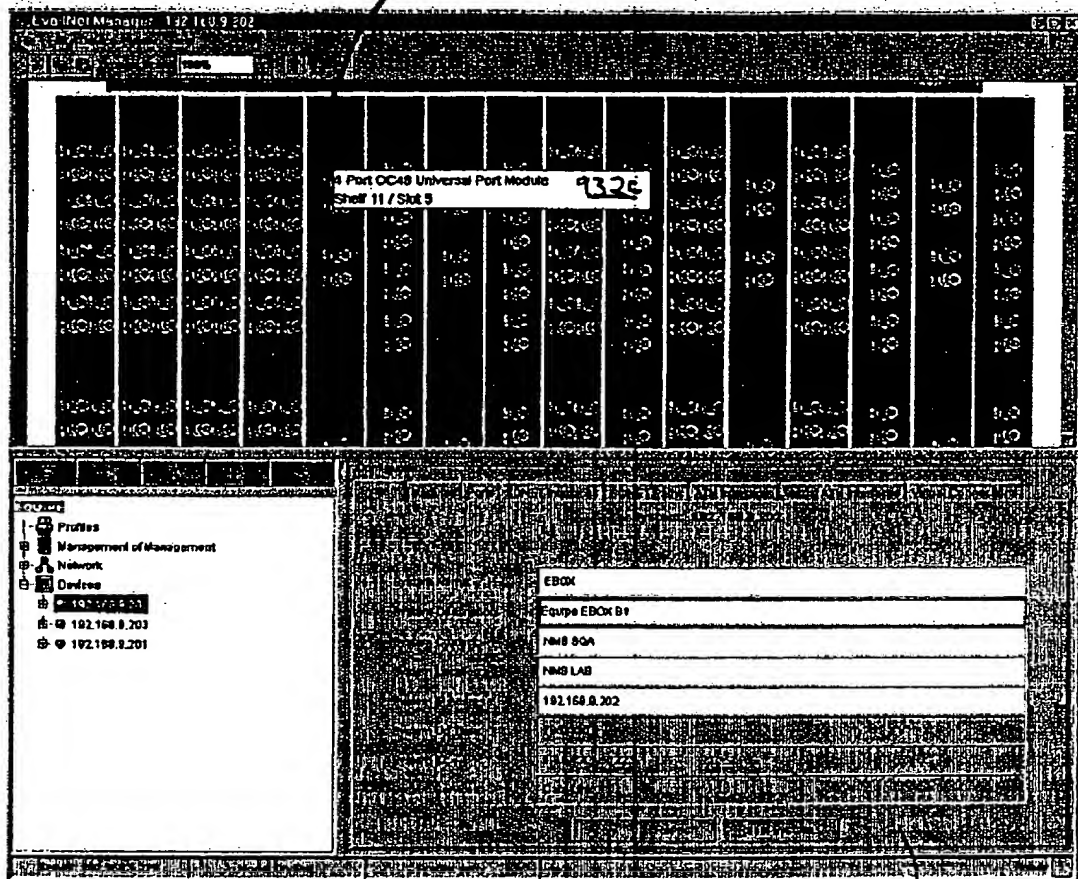
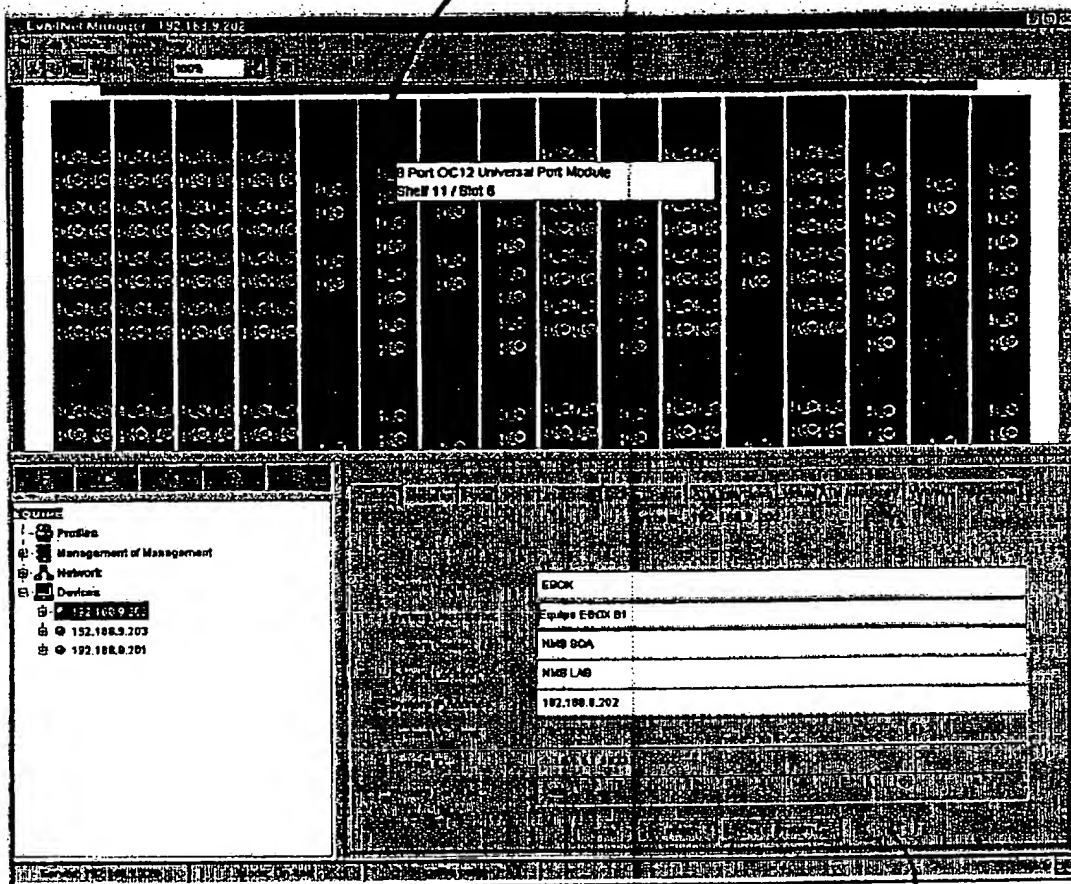


Fig. 4g

895

556c



897

Fig. 4r

00360 5104360

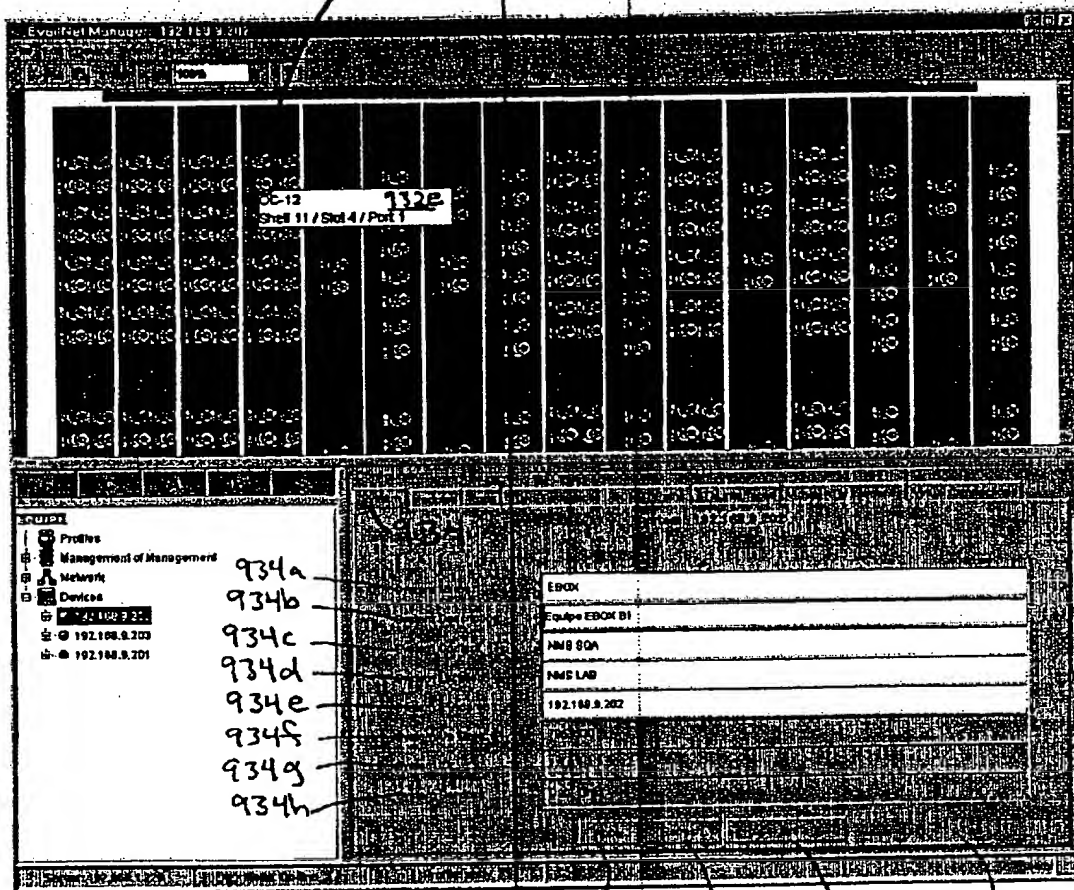


Fig. 4s

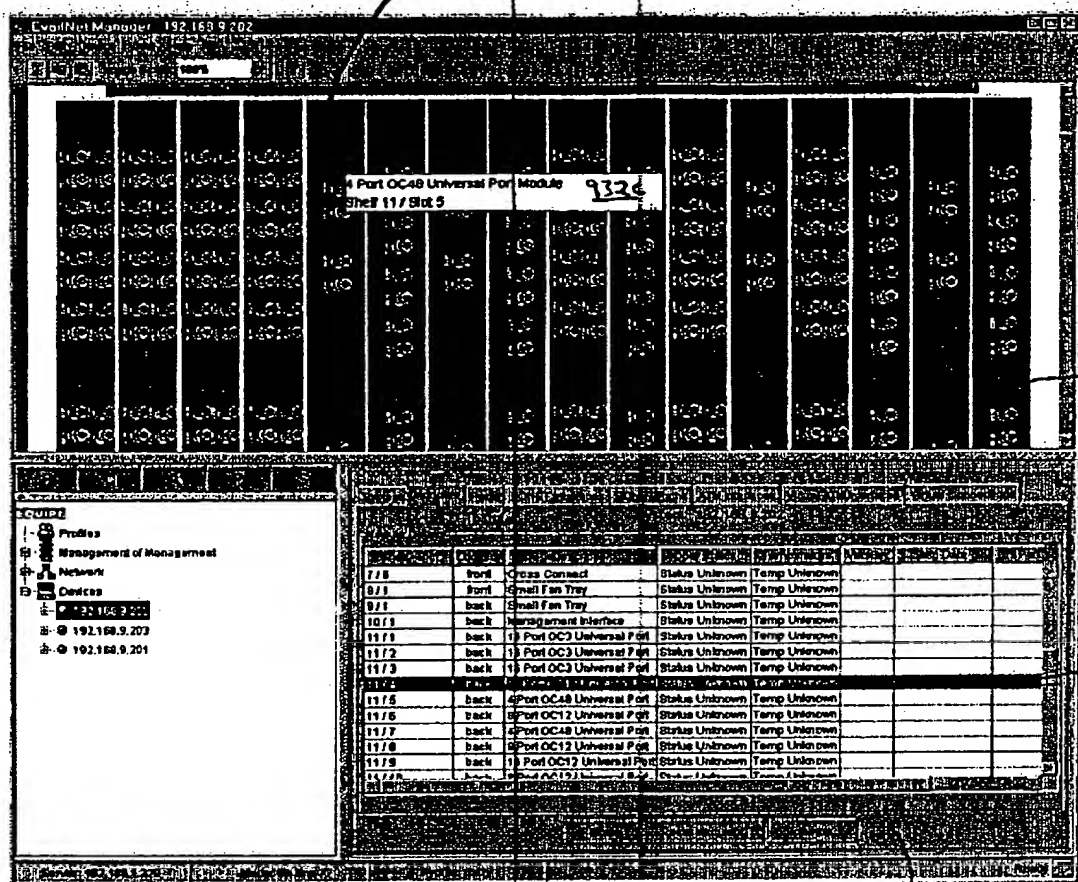


Fig. 4t

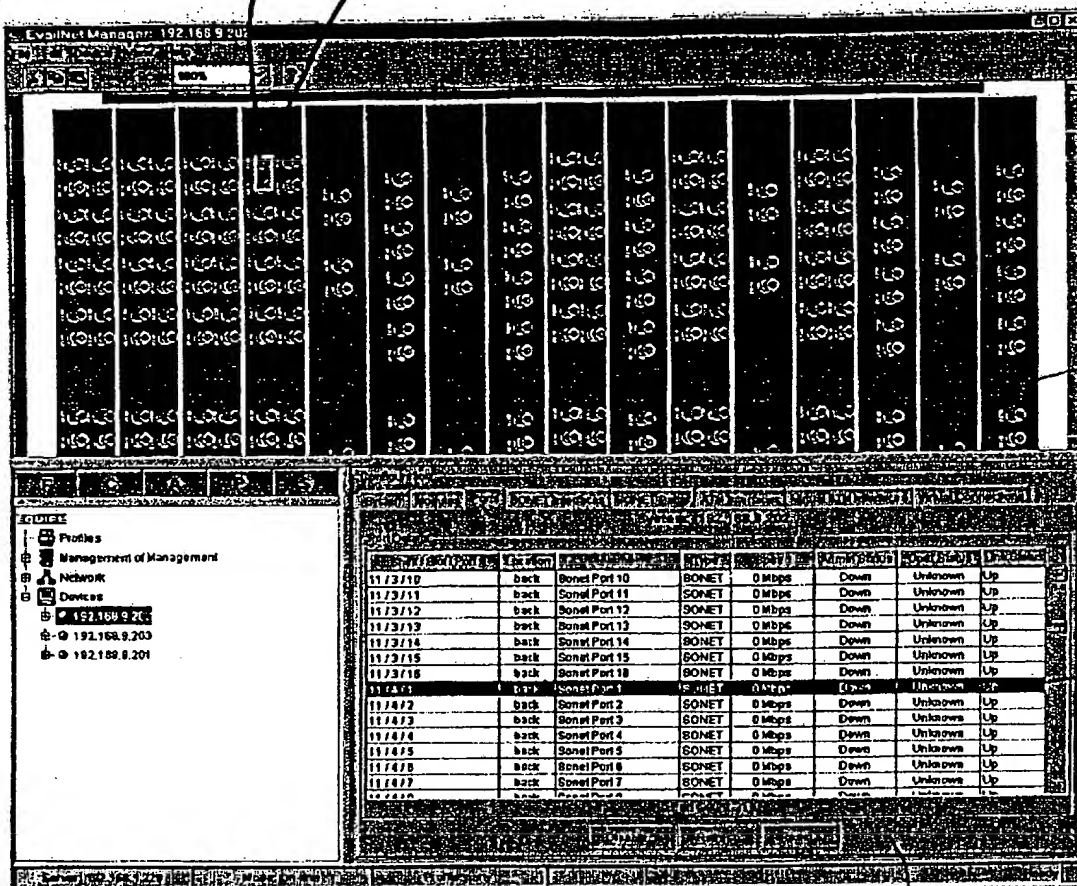
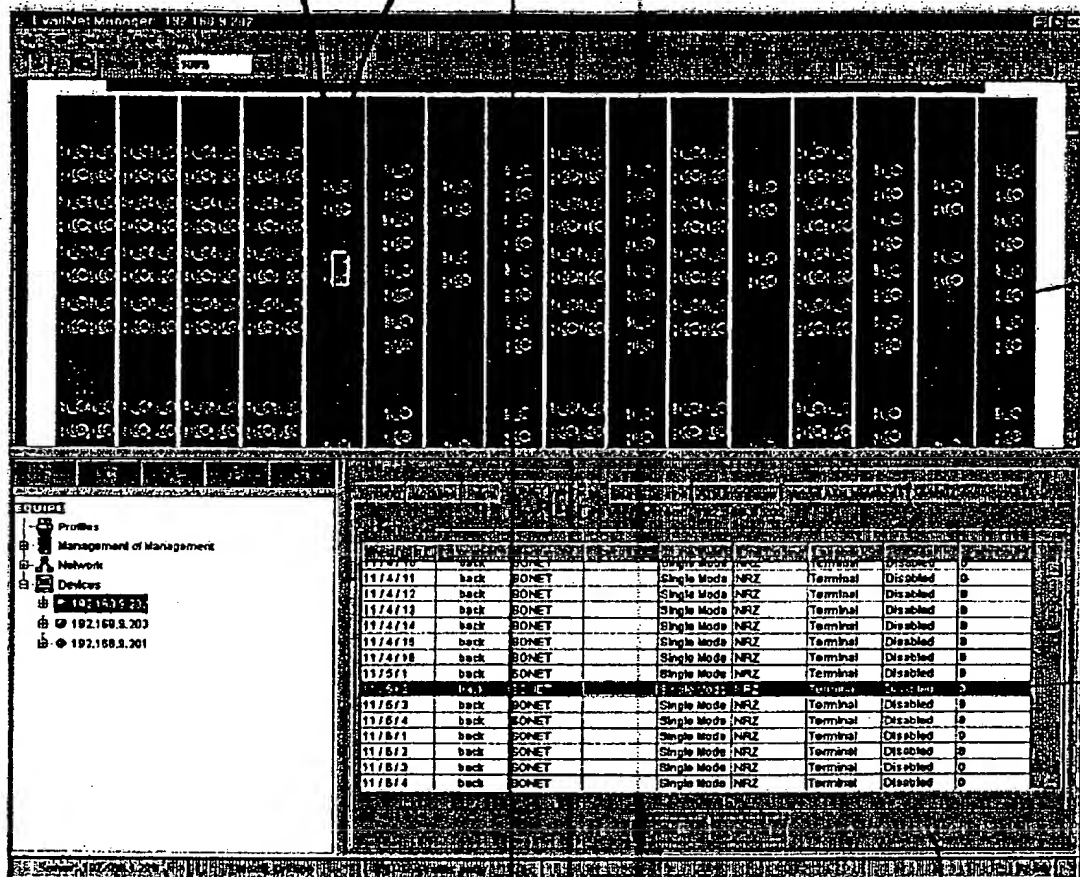


Fig. 4u





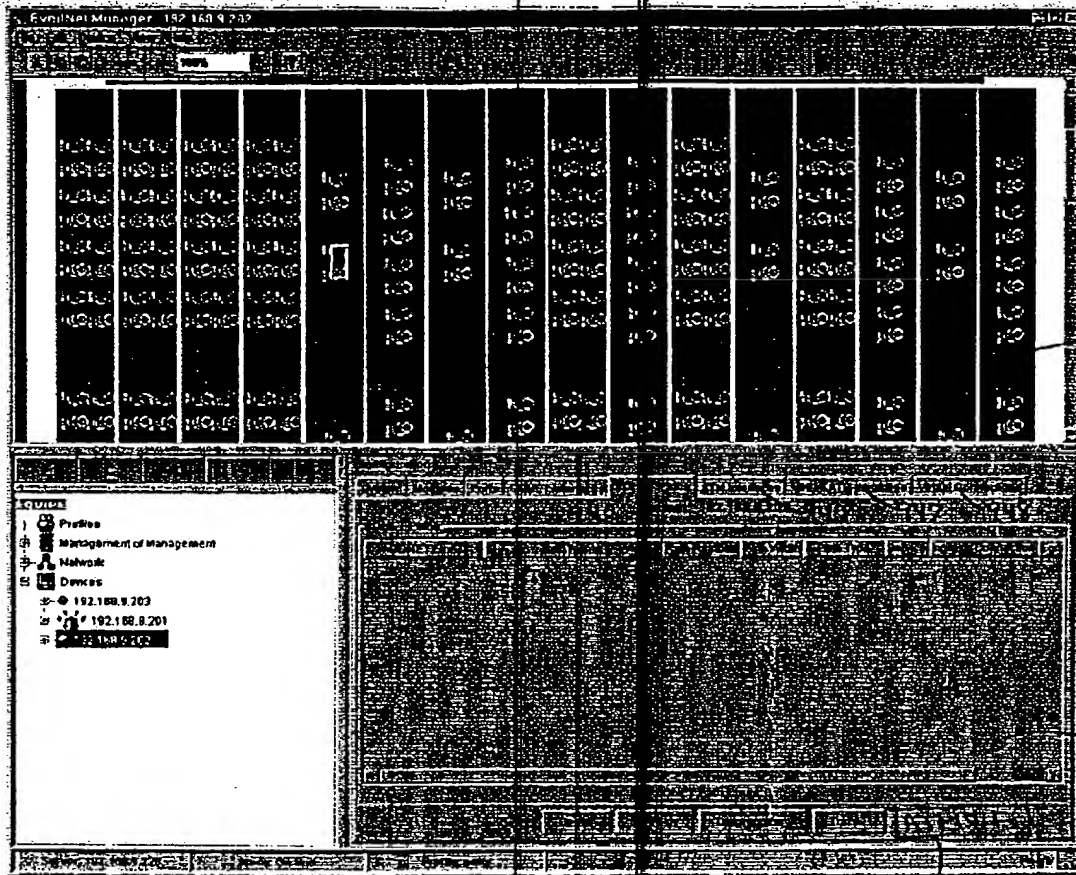


Fig. 4w

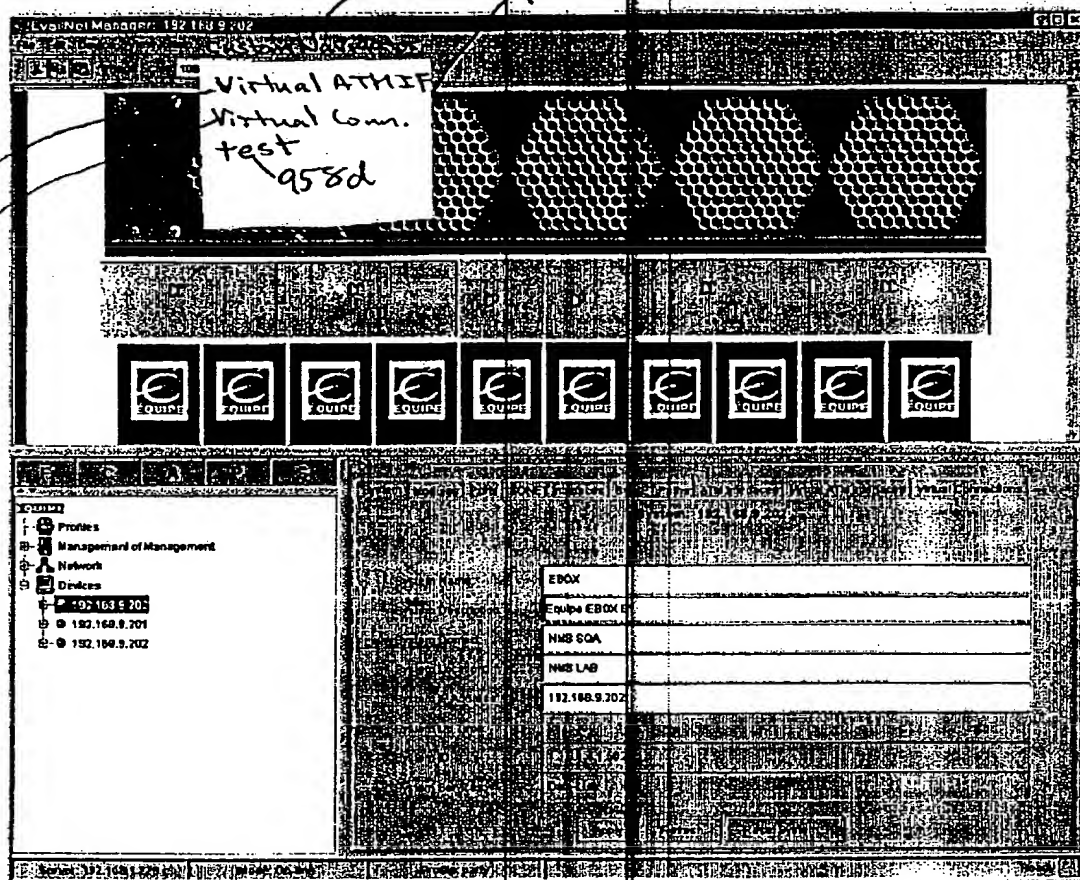


Fig. 4x

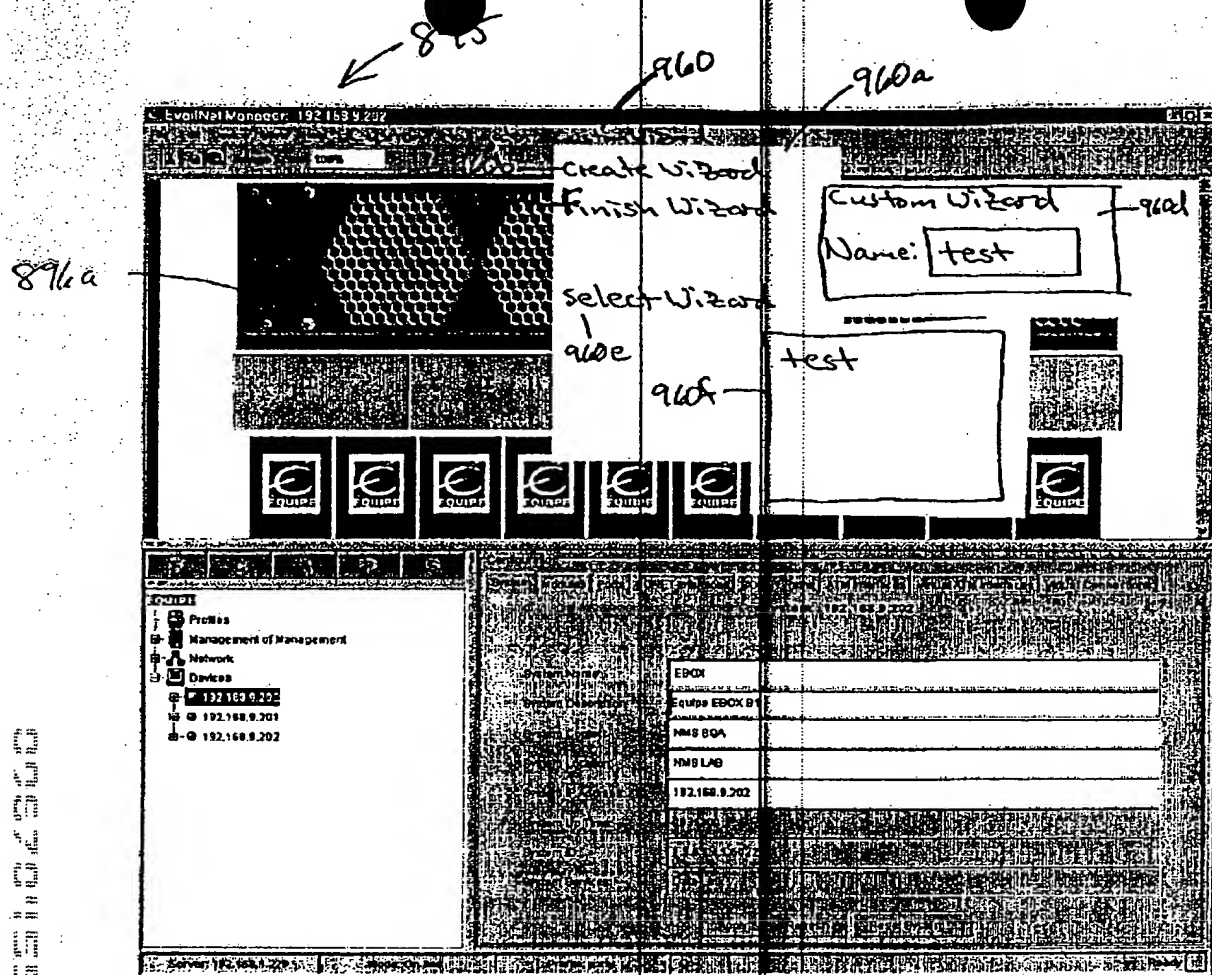
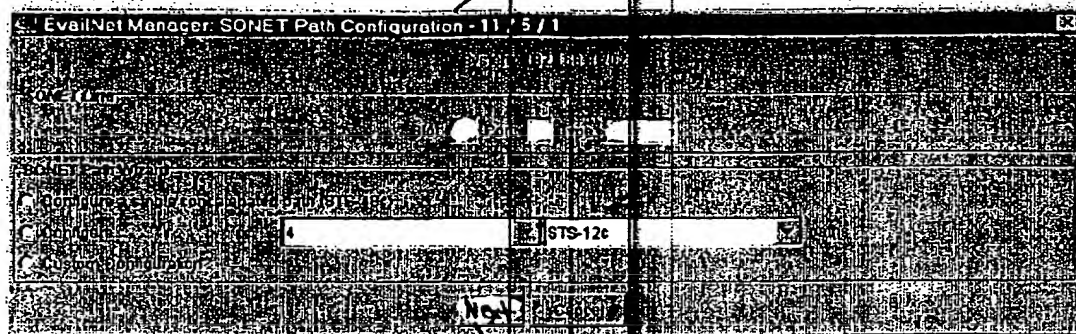


Fig. 4y

1961



-961a

9616

Fig. 42

[illegible]

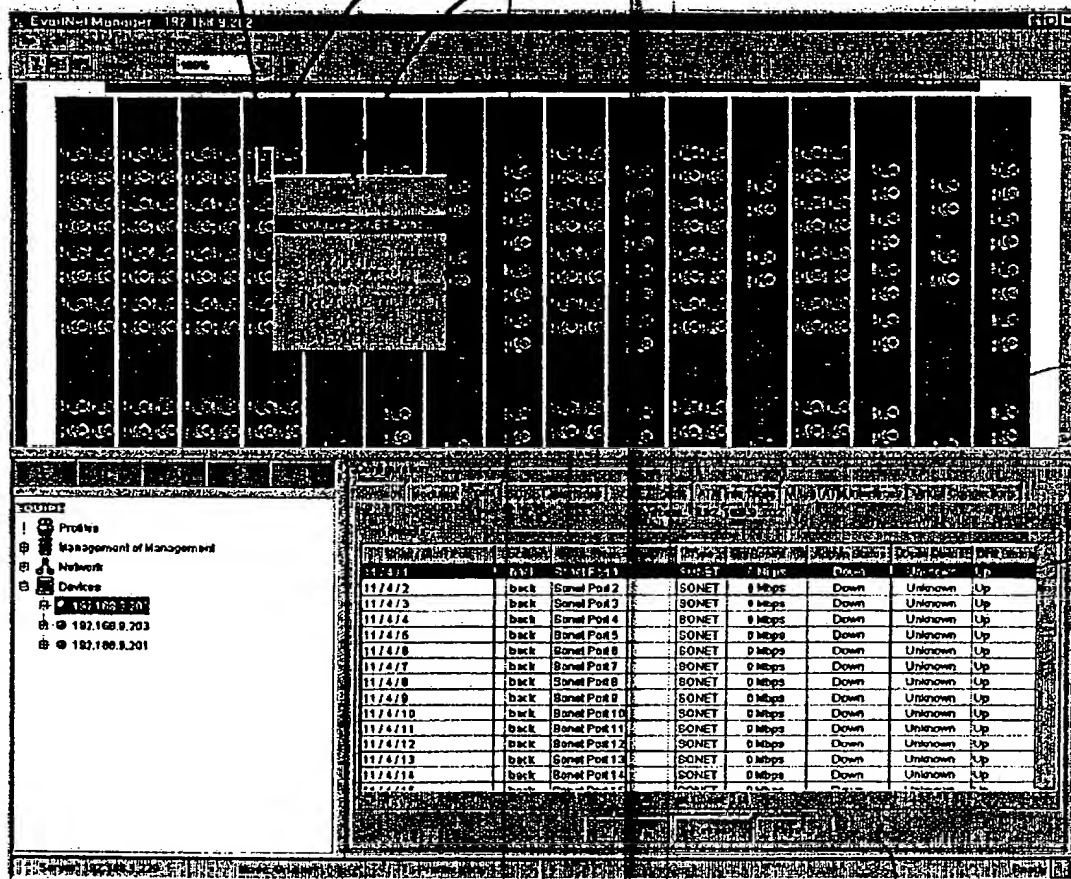
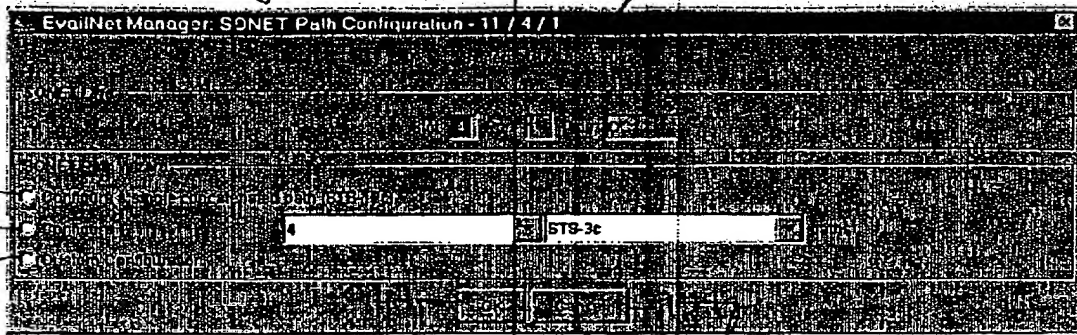


Fig. 5b

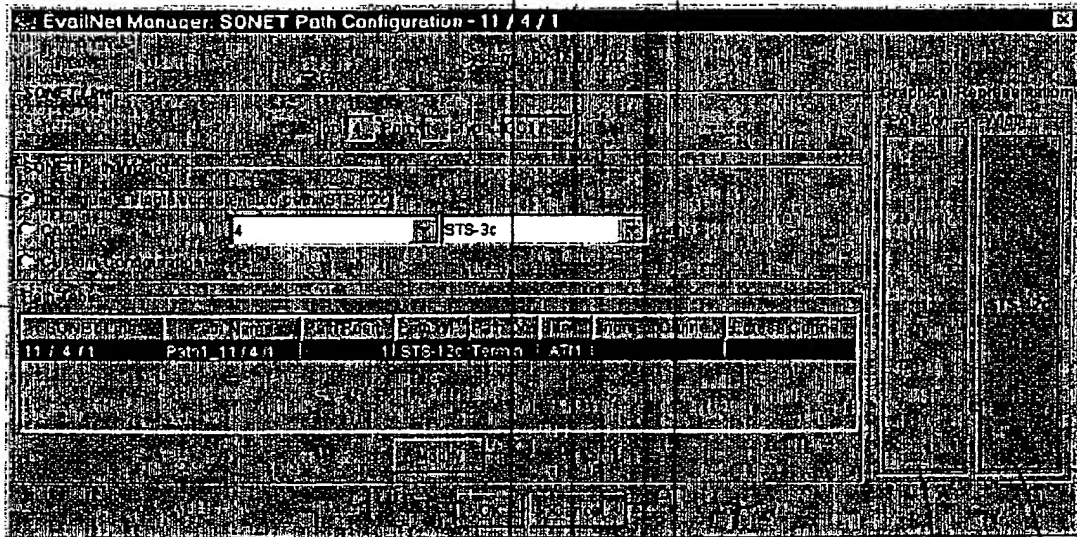
944

944b  
944c  
944d

944a



944b  
944g



944

944e

944f

Fig. 5c



Fig. 5d

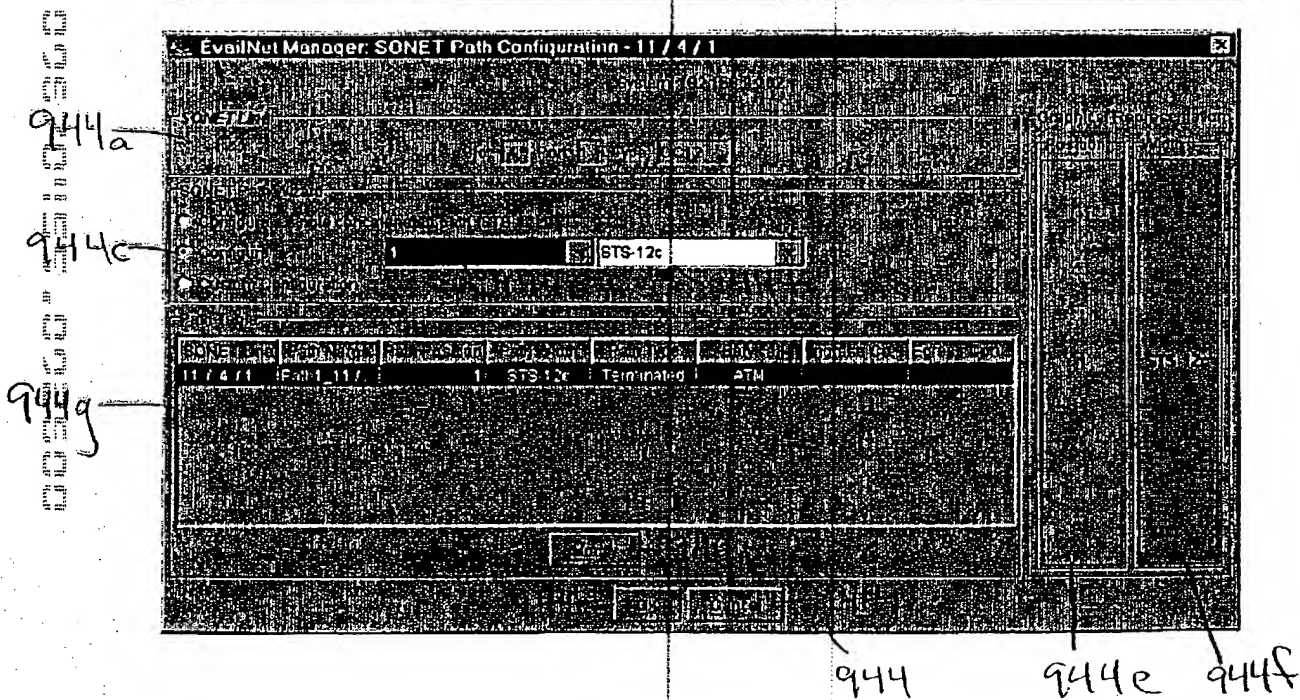
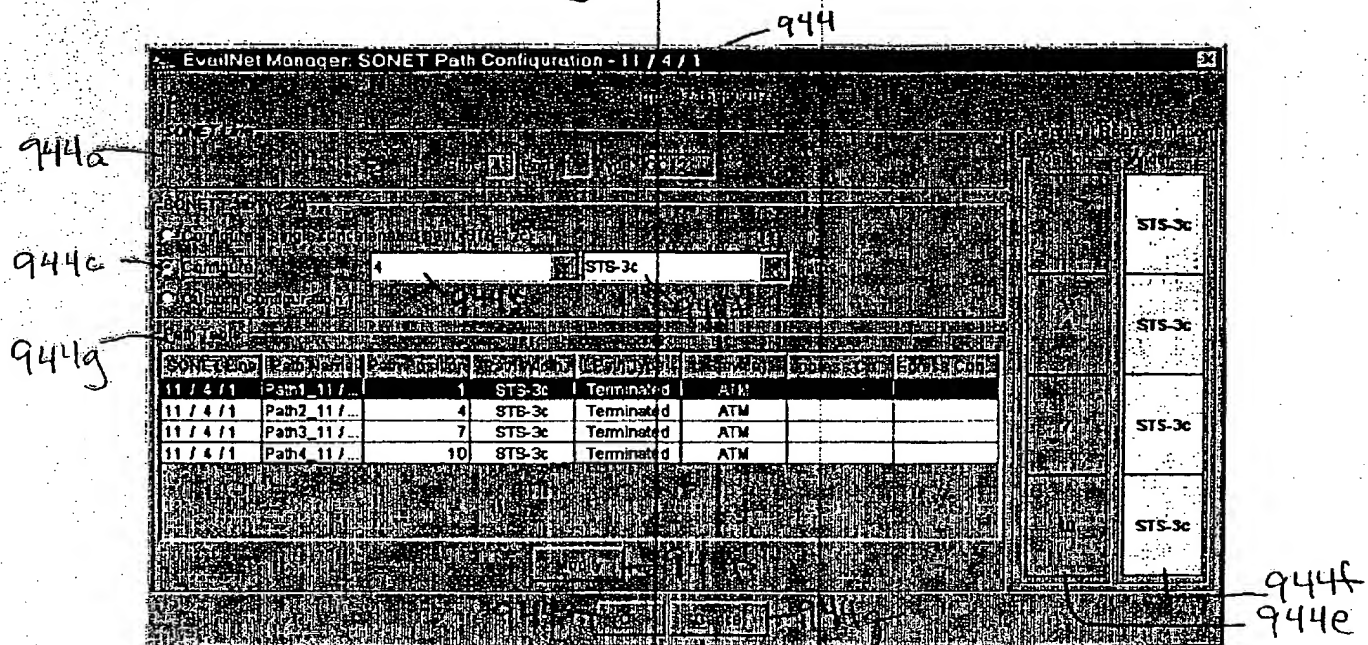


Fig. 5e



Fig. 5f

944

EvailNet Manager: SONET Path Configuration - 11 / 4 / 1

944a

944b

944h

STS-3c

944i

STS-3c - 944L

STS-3c - 944m

STS-3

944j

STS-3c

STS-3c

SONET PATH	PATH NAME	PROT. PATH	PROT. NAME	PROT. TYPE	PROT. CODE	PROT. CODE	PROT. CODE
11 / 4 / 1	Path1_11 /	1	STS-3c	Terminated	ATM		
11 / 4 / 1	Path2_11 /	4	STS-3c	Terminated	ATM		
11 / 4 / 1	Path3_11 /	7		Terminated	ATM		

944g

944f

944e

EvailNet Manager: SONET Path Configuration - 11 / 4 / 1

945a

945g

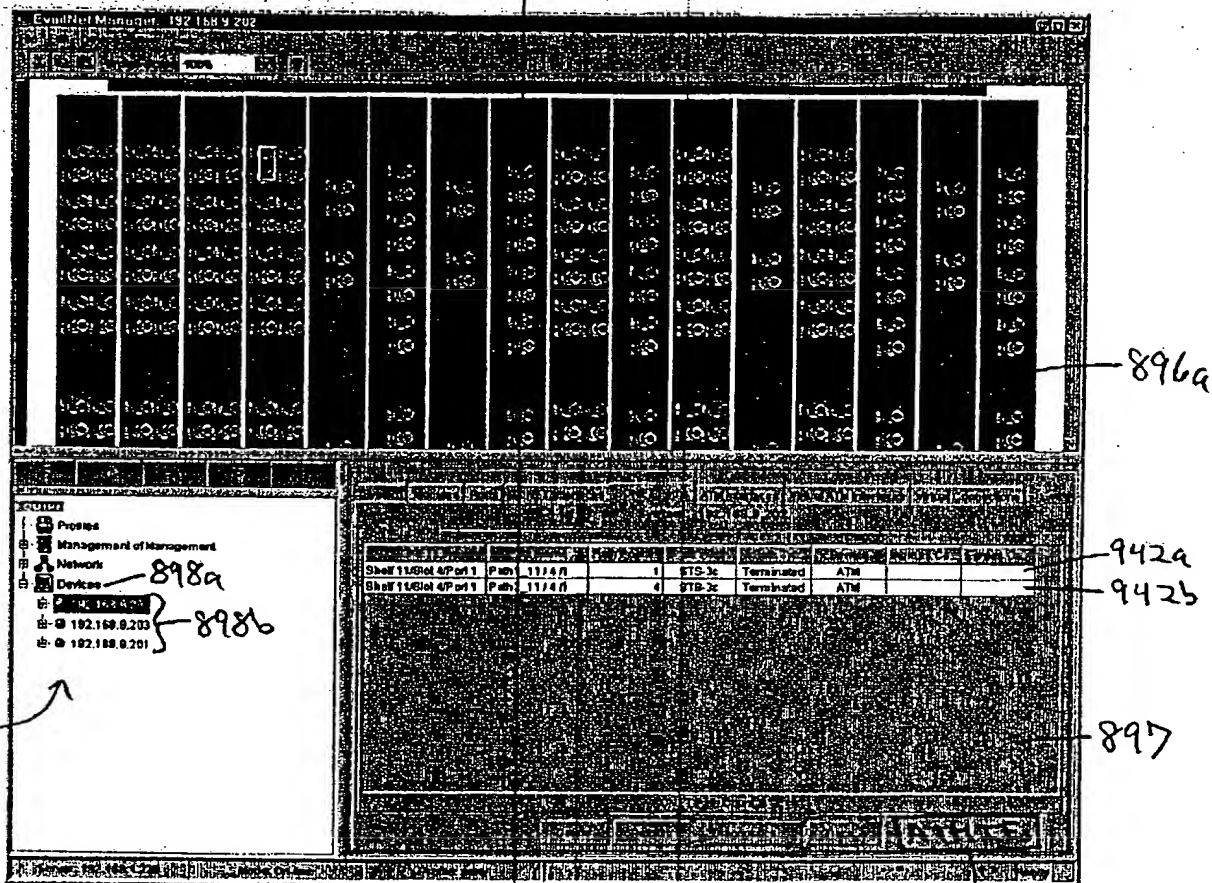
SONET PATH	PATH NAME	PROT. PATH	PROT. NAME	PROT. TYPE	PROT. CODE	PROT. CODE	PROT. CODE
Shelf 11/91...	Path1_11 /	1	3	2	1		
Shelf 11/91...	Path2_11 /	4	3	2	1		

945

945e

945f

Fig. 5g



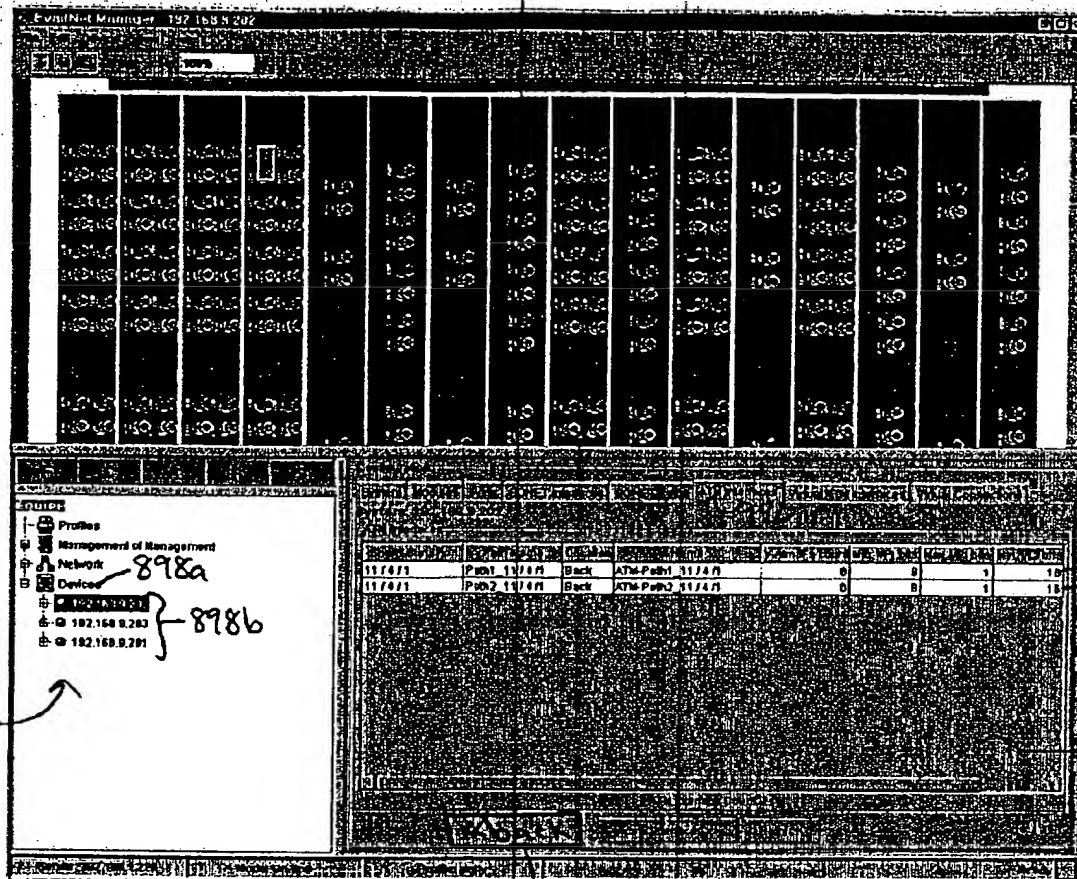


Fig. 5i

Fig. 5j

556d

941a

895

The screenshot shows the 'EquiNet Manager' interface. The main window displays a grid of data, likely representing network status or configuration. The sidebar on the left contains a tree view with the following items:

- Profiles
- Management of Management
- Network
  - 898a
  - 132.168.9.200
  - 132.168.9.201

Handwritten annotations include '898' with an arrow pointing to the sidebar, '896a' pointing to the grid, and '949a' pointing to a table within the grid.

IP Address	Device	Protocol	Mode	Terminal	Status	Count
117.2.1.1	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.2	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.3	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.4	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.5	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.6	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.7	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.8	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.9	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.10	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.11	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.12	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.13	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.14	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.15	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.16	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.17	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.18	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.19	back	SONET	Single Mode NRZ	Terminal	Disabled	0
117.2.1.20	back	SONET	Single Mode NRZ	Terminal	Disabled	0

Fig. 5K

The screenshot shows the 'EquiNet Manager: SNET Path Configuration - 11 / 5 / 2' window. It displays a configuration interface for SNET paths. Handwritten annotations include '944b', '944c', and '944d' pointing to the left sidebar, '944a' pointing to the top of the main configuration area, and '944' pointing to the bottom of the window.

Fig. 5l

944

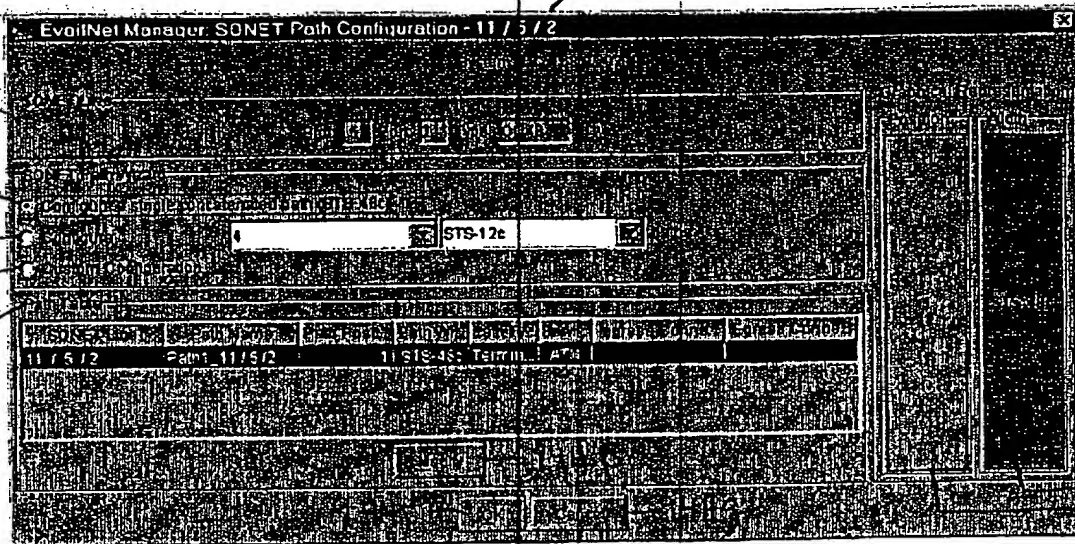
944a

944b

944c

944d

944g



944f

944e

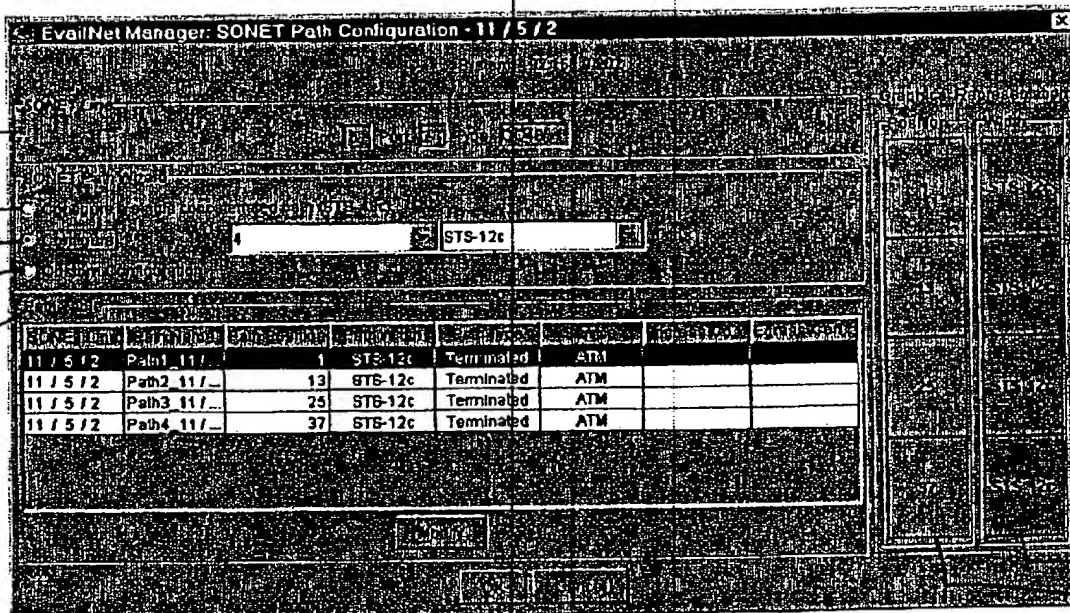
944a

944b

944c

944d

944g



944f

944e

944

Fig. 5m



Fig. 5n

944

944a

944b

944c

944d

944g

EvailNet Manager: SONET Path Configuration - 11/5/2

11/5/2 Path: 11/ 1 STS-48c Terminate: ATM

16 944s

944f 944e

944a

944b

944c

944d

944g

EvailNet Manager: SONET Path Configuration - 11/5/2

16 STS-3c

Path	Path Name	Path ID	STS	Termination	ATM
11/5/2	Path1 11/5/2	1	STS-3c	Termi	ATM
11/5/2	Path2 11/5/2	4	STS-3c	Termi	ATM
11/5/2	Path3 11/5/2	7	STS-3c	Termi	ATM
11/5/2	Path4 11/5/2	10	STS-3c	Termi	ATM
11/5/2	Path5 11/5/2	13	STS-3c	Termi	ATM
11/5/2	Path6 11/5/2	16	STS-3c	Termi	ATM
11/5/2	Path7 11/5/2	19	STS-3c	Termi	ATM

944f 944e

944

Fig. 5o

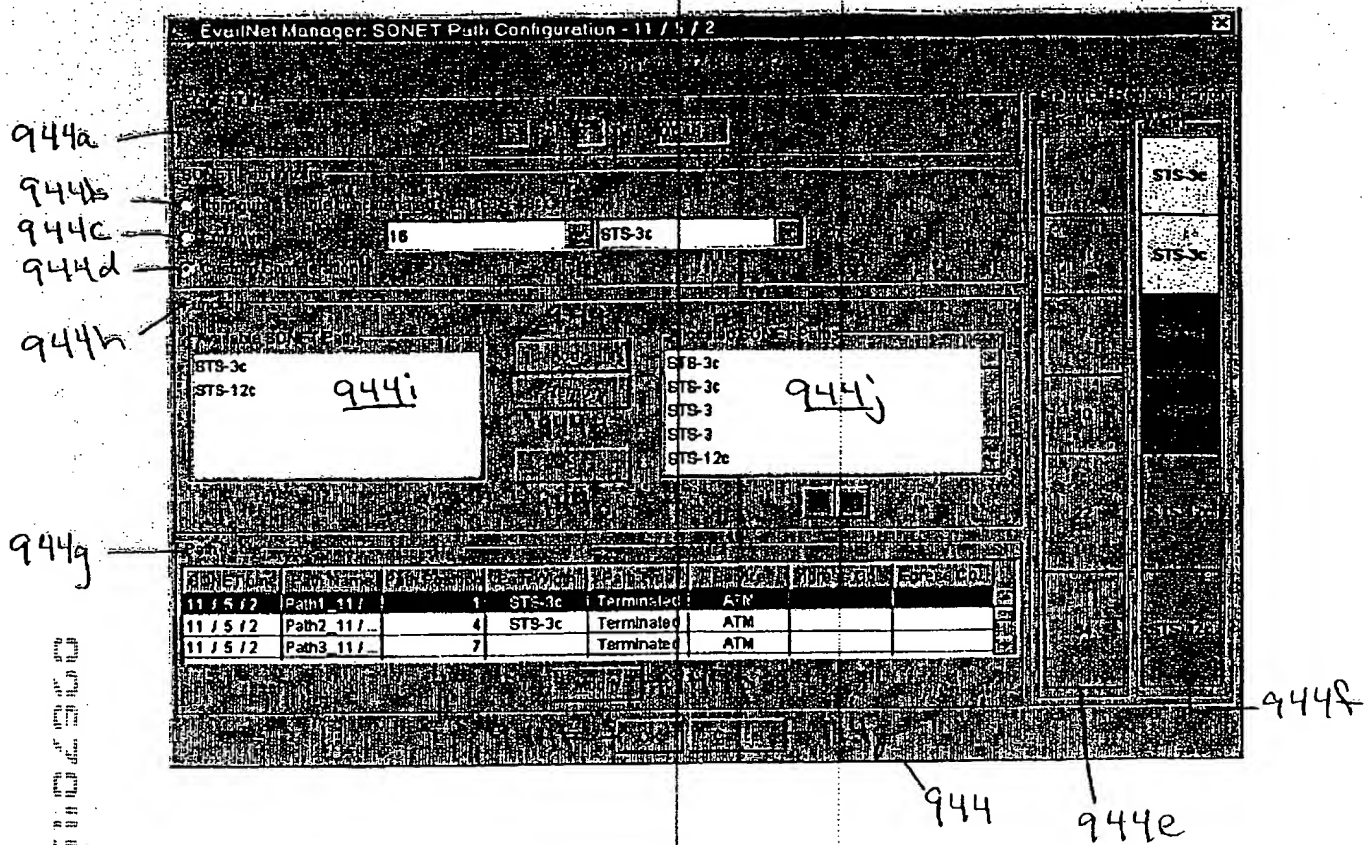
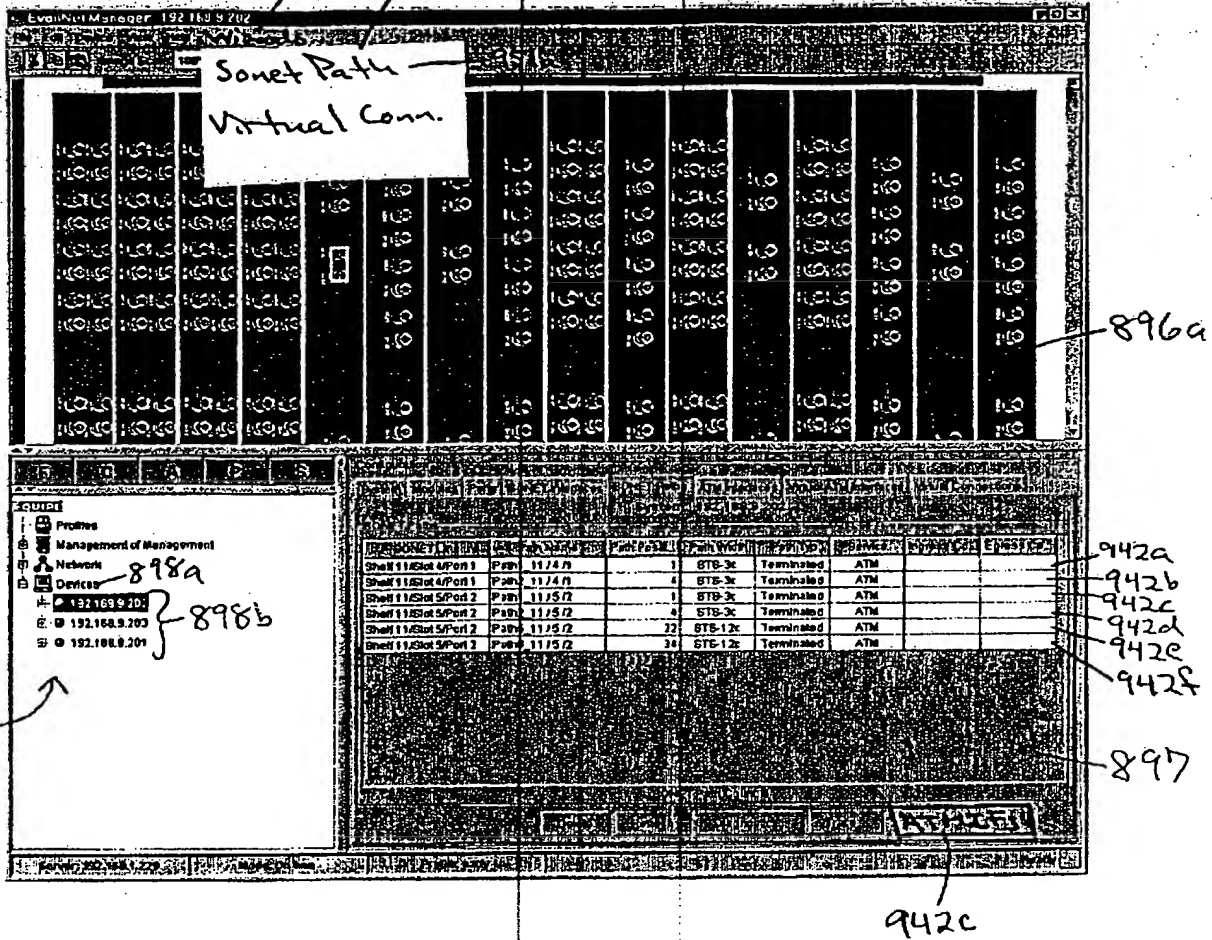
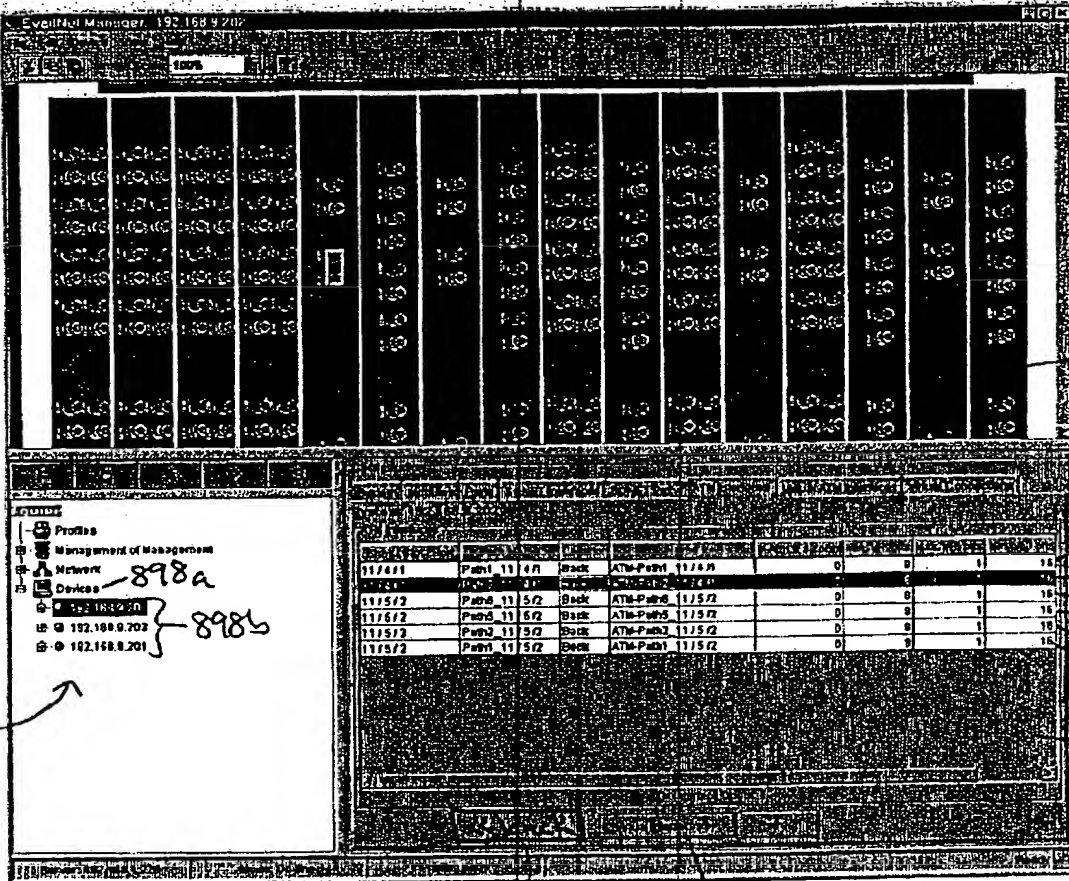


Fig. 5p





895



896a

946a  
946b  
946c  
946d  
946e  
946f

897

946h

946g

Fig. 5r

898

898a

898b

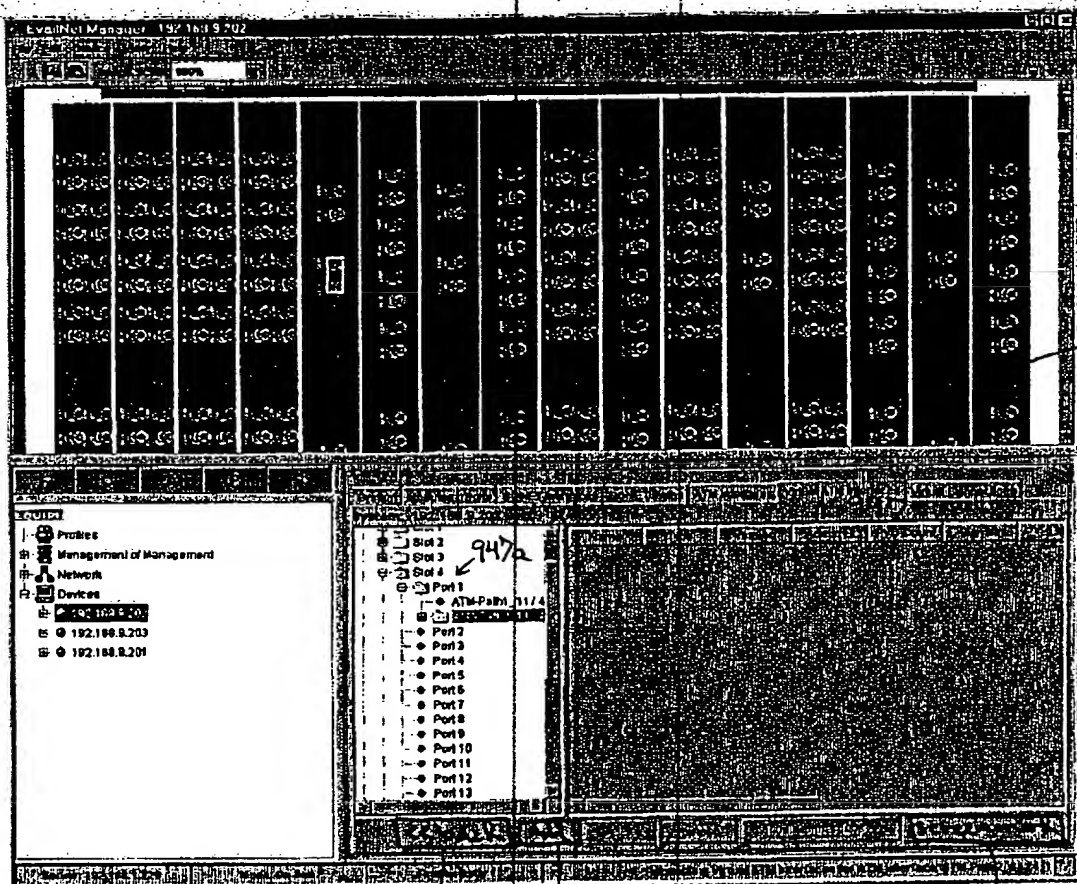


Fig. 5s

Fig. 5+

950

**Add V-ATM Interface - 192.168.9.202**

Start Slot: 11 End Slot: 12

Virtual ATM Interface: 1

Name: test1	
Conn. Link type: Direct Link	<input checked="" type="checkbox"/>
UNI Network 3.1	<input checked="" type="checkbox"/>
Admin Status: Up	<input checked="" type="checkbox"/>

OK Cancel

950d

950a

950b

950c

**EvalNet Manager: 192.168.9.202**

Ports

10.0.0.1	10.0.0.2	10.0.0.3	10.0.0.4	10.0.0.5	10.0.0.6	10.0.0.7	10.0.0.8	10.0.0.9	10.0.0.10	10.0.0.11	10.0.0.12	10.0.0.13	10.0.0.14	10.0.0.15	10.0.0.16	10.0.0.17	10.0.0.18	10.0.0.19	10.0.0.20	10.0.0.21	10.0.0.22	10.0.0.23	10.0.0.24	10.0.0.25	10.0.0.26	10.0.0.27	10.0.0.28	10.0.0.29	10.0.0.30	10.0.0.31	
10.0.0.32	10.0.0.33	10.0.0.34	10.0.0.35	10.0.0.36	10.0.0.37	10.0.0.38	10.0.0.39	10.0.0.40	10.0.0.41	10.0.0.42	10.0.0.43	10.0.0.44	10.0.0.45	10.0.0.46	10.0.0.47	10.0.0.48	10.0.0.49	10.0.0.50	10.0.0.51	10.0.0.52	10.0.0.53	10.0.0.54	10.0.0.55	10.0.0.56	10.0.0.57	10.0.0.58	10.0.0.59	10.0.0.60	10.0.0.61	10.0.0.62	10.0.0.63
10.0.0.64	10.0.0.65	10.0.0.66	10.0.0.67	10.0.0.68	10.0.0.69	10.0.0.70	10.0.0.71	10.0.0.72	10.0.0.73	10.0.0.74	10.0.0.75	10.0.0.76	10.0.0.77	10.0.0.78	10.0.0.79	10.0.0.80	10.0.0.81	10.0.0.82	10.0.0.83	10.0.0.84	10.0.0.85	10.0.0.86	10.0.0.87	10.0.0.88	10.0.0.89	10.0.0.90	10.0.0.91	10.0.0.92	10.0.0.93	10.0.0.94	10.0.0.95
10.0.0.96	10.0.0.97	10.0.0.98	10.0.0.99	10.0.0.100	10.0.0.101	10.0.0.102	10.0.0.103	10.0.0.104	10.0.0.105	10.0.0.106	10.0.0.107	10.0.0.108	10.0.0.109	10.0.0.110	10.0.0.111	10.0.0.112	10.0.0.113	10.0.0.114	10.0.0.115	10.0.0.116	10.0.0.117	10.0.0.118	10.0.0.119	10.0.0.120	10.0.0.121	10.0.0.122	10.0.0.123	10.0.0.124	10.0.0.125	10.0.0.126	10.0.0.127
10.0.0.128	10.0.0.129	10.0.0.130	10.0.0.131	10.0.0.132	10.0.0.133	10.0.0.134	10.0.0.135	10.0.0.136	10.0.0.137	10.0.0.138	10.0.0.139	10.0.0.140	10.0.0.141	10.0.0.142	10.0.0.143	10.0.0.144	10.0.0.145	10.0.0.146	10.0.0.147	10.0.0.148	10.0.0.149	10.0.0.150	10.0.0.151	10.0.0.152	10.0.0.153	10.0.0.154	10.0.0.155	10.0.0.156	10.0.0.157	10.0.0.158	10.0.0.159
10.0.0.160	10.0.0.161	10.0.0.162	10.0.0.163	10.0.0.164	10.0.0.165	10.0.0.166	10.0.0.167	10.0.0.168	10.0.0.169	10.0.0.170	10.0.0.171	10.0.0.172	10.0.0.173	10.0.0.174	10.0.0.175	10.0.0.176	10.0.0.177	10.0.0.178	10.0.0.179	10.0.0.180	10.0.0.181	10.0.0.182	10.0.0.183	10.0.0.184	10.0.0.185	10.0.0.186	10.0.0.187	10.0.0.188	10.0.0.189	10.0.0.190	10.0.0.191
10.0.0.192	10.0.0.193	10.0.0.194	10.0.0.195	10.0.0.196	10.0.0.197	10.0.0.198	10.0.0.199	10.0.0.200	10.0.0.201	10.0.0.202	10.0.0.203	10.0.0.204	10.0.0.205	10.0.0.206	10.0.0.207	10.0.0.208	10.0.0.209	10.0.0.210	10.0.0.211	10.0.0.212	10.0.0.213	10.0.0.214	10.0.0.215	10.0.0.216	10.0.0.217	10.0.0.218	10.0.0.219	10.0.0.220	10.0.0.221	10.0.0.222	10.0.0.223
10.0.0.224	10.0.0.225	10.0.0.226	10.0.0.227	10.0.0.228	10.0.0.229	10.0.0.230	10.0.0.231	10.0.0.232	10.0.0.233	10.0.0.234	10.0.0.235	10.0.0.236	10.0.0.237	10.0.0.238	10.0.0.239	10.0.0.240	10.0.0.241	10.0.0.242	10.0.0.243	10.0.0.244	10.0.0.245	10.0.0.246	10.0.0.247	10.0.0.248	10.0.0.249	10.0.0.250	10.0.0.251	10.0.0.252	10.0.0.253	10.0.0.254	10.0.0.255

Profiles

- Management of Management
- Network
- Devices

192.168.9.202

- Slot 11
- Slot 12
- Slot 2
- Slot 3
- Slot 4
- Port 1
- ATM-Port 1 / 147
- Port 2
- Port 3
- Port 4
- Port 5
- Port 6
- Port 7
- Port 8
- Port 9

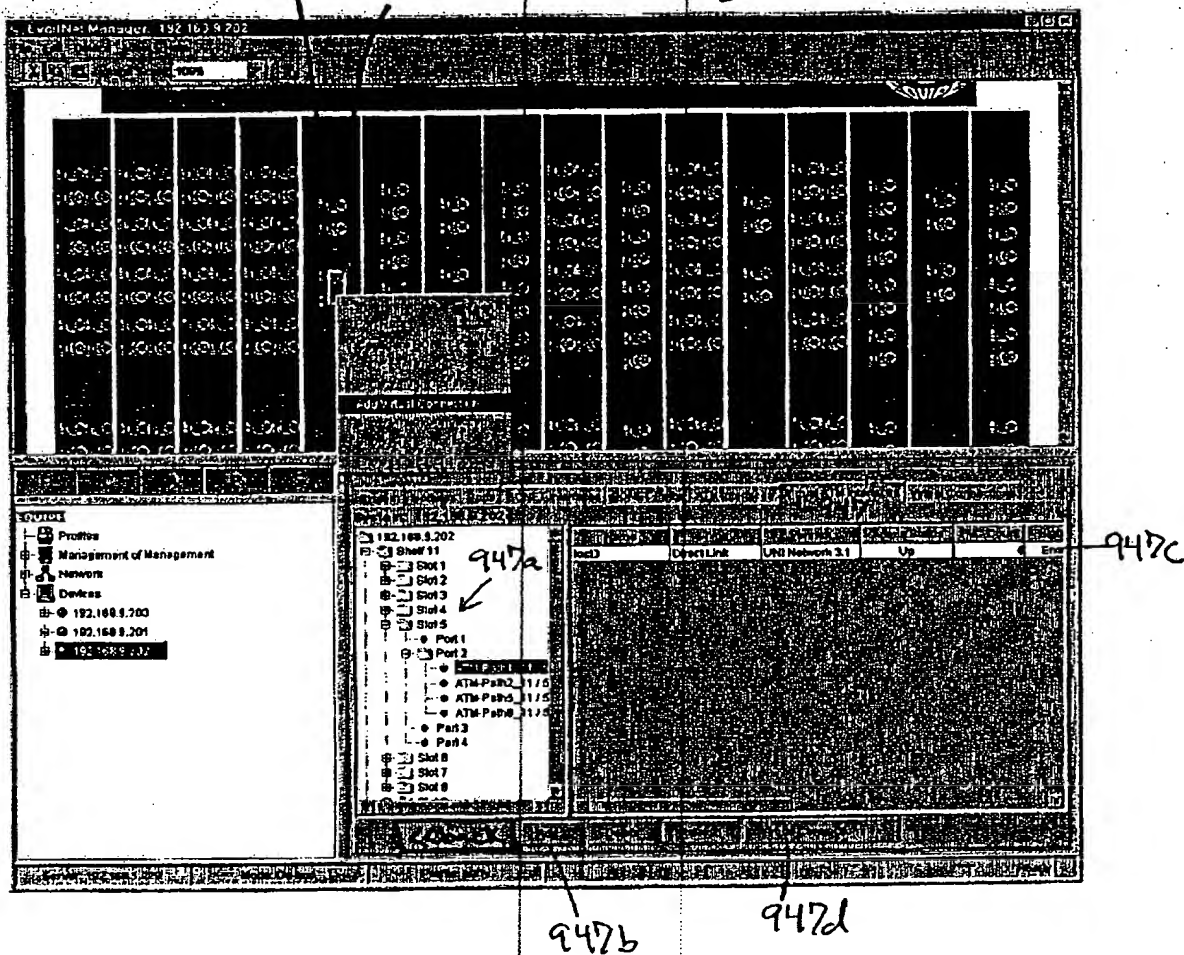
test1 Direct Link UNI Network 3.1 Up

OK Cancel

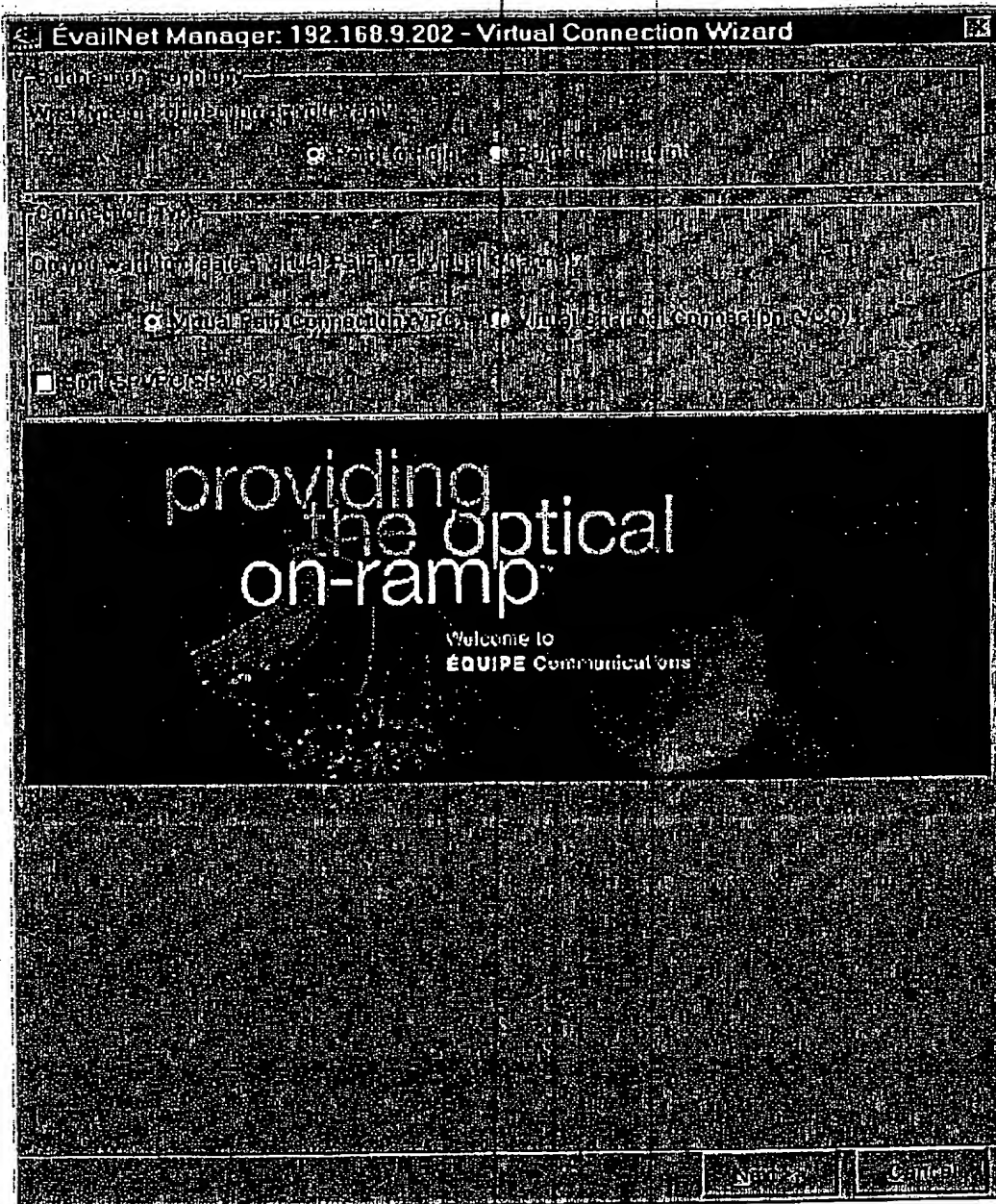
947c

947b

Fig. 5u



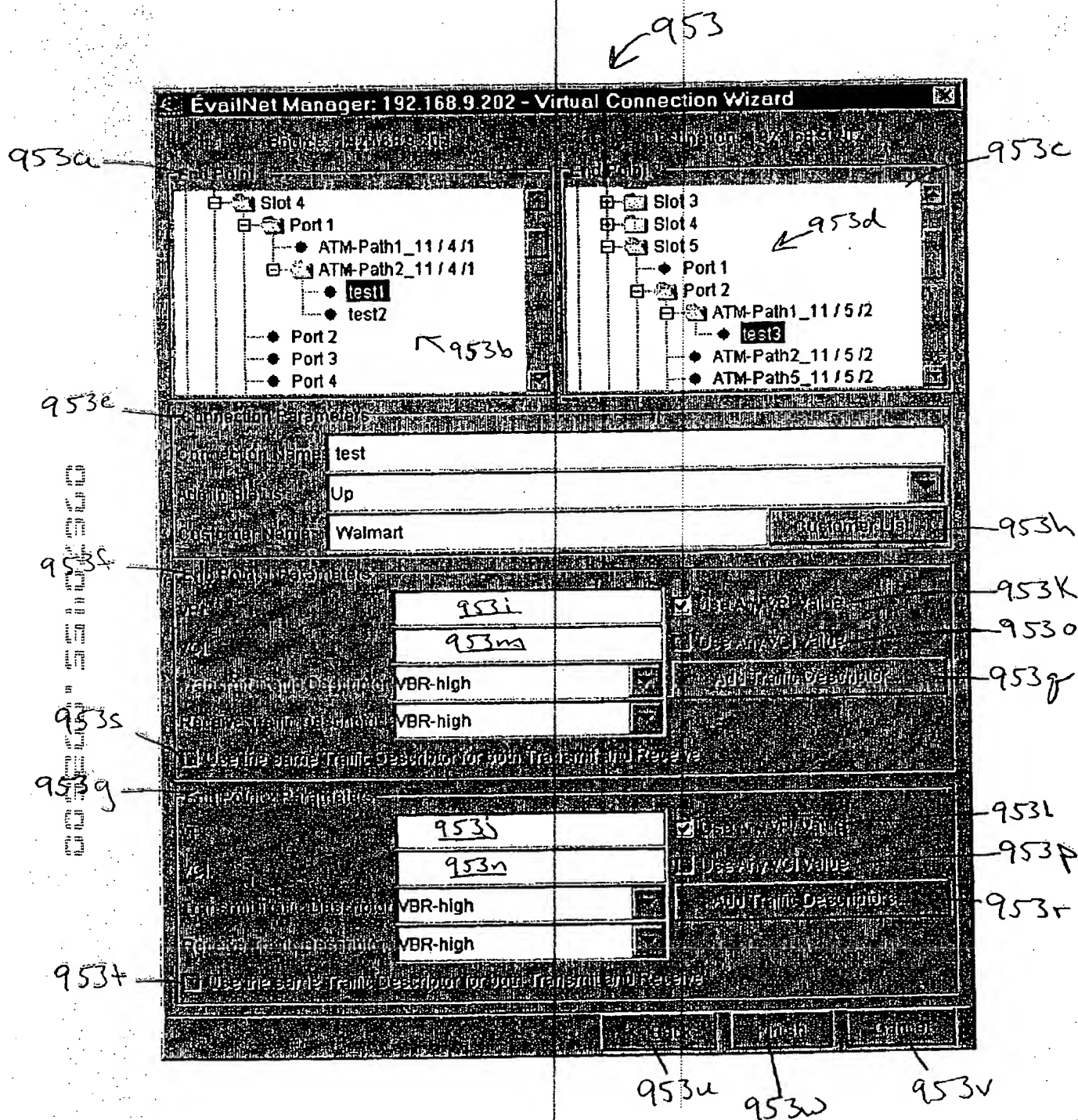
952



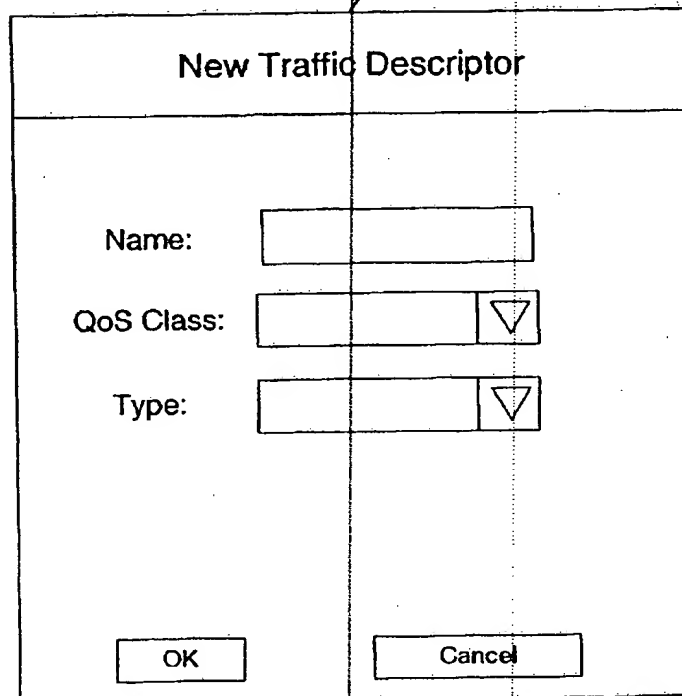
952a

952b

Fig. 5w



956



The image shows a dialog box titled "New Traffic Descriptor". It contains three input fields: "Name:" with a text box, "QoS Class:" with a text box and a dropdown arrow, and "Type:" with a text box and a dropdown arrow. At the bottom, there are two buttons: "OK" and "Cancel".

New Traffic Descriptor

Name:

QoS Class:  ▼

Type:  ▼

OK Cancel

Fig. 5Y



895

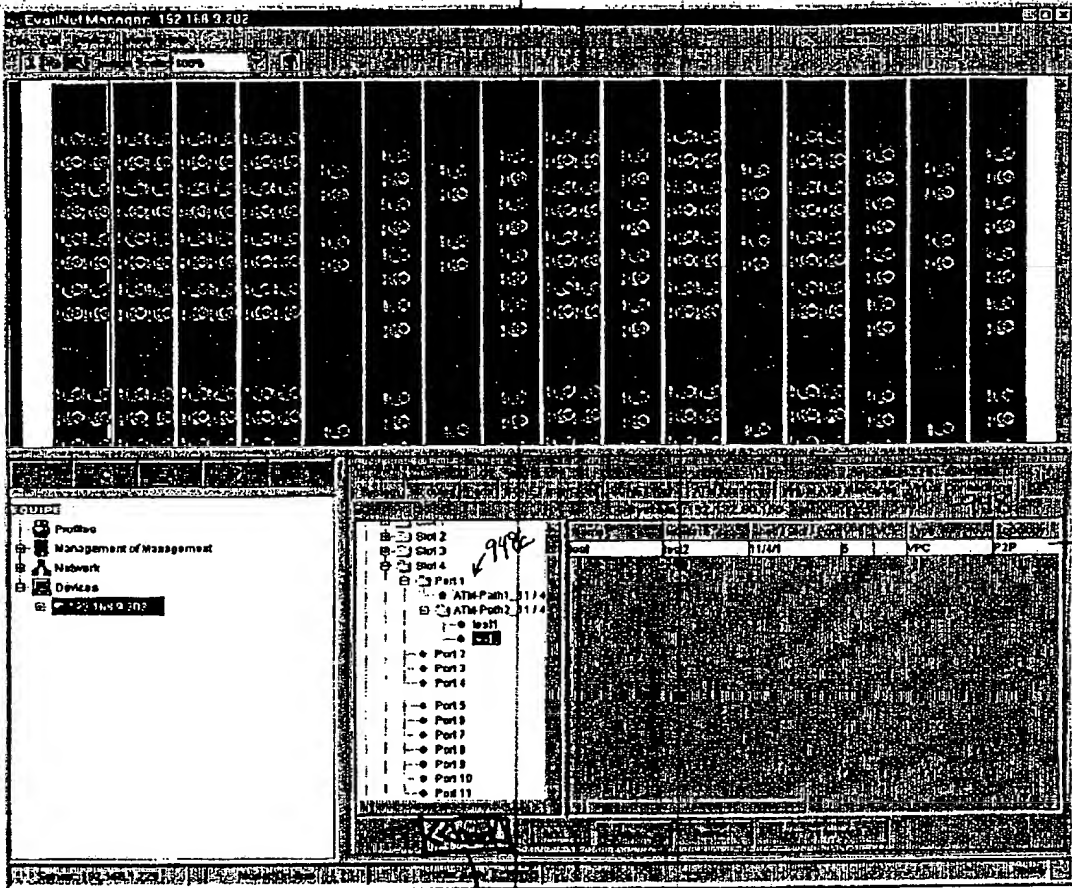


Fig. 57



895

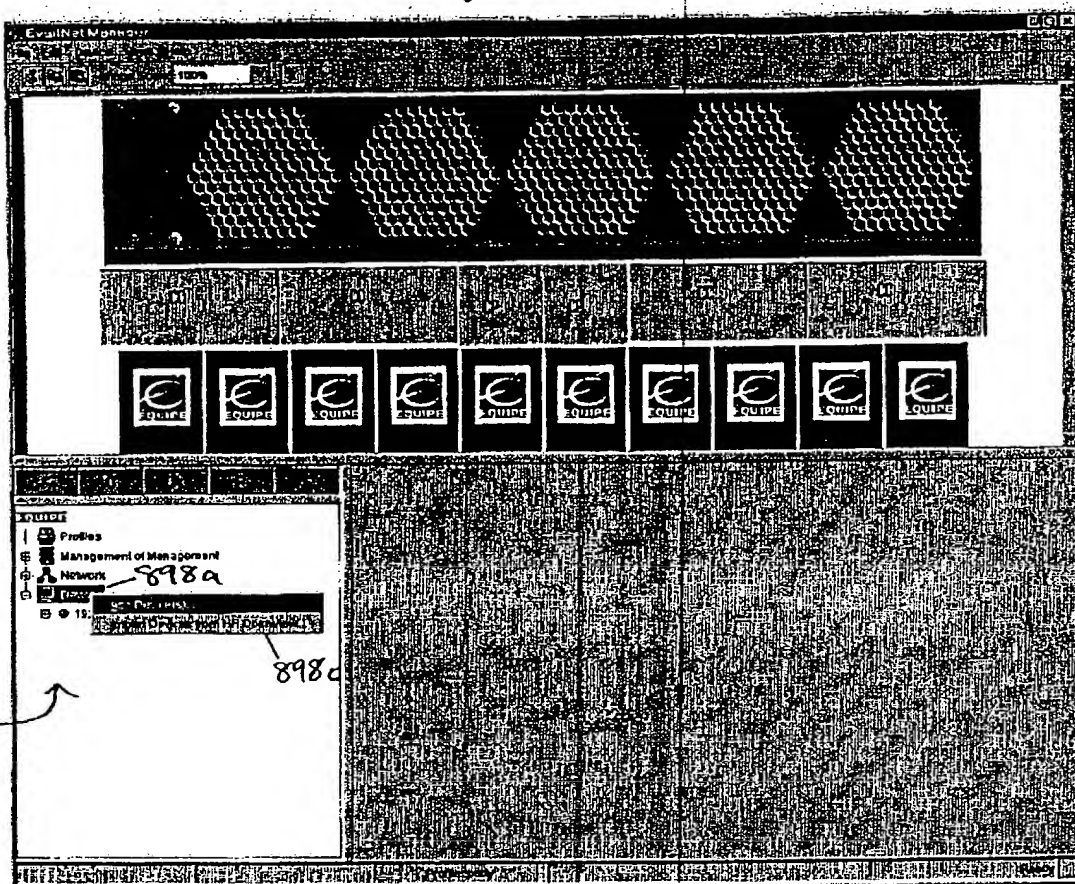


Fig. 6a

Fig. 6b

Fig. 6b is a screenshot of a Windows-style dialog box titled "AddDeleteDeviceDlg". The dialog contains the following elements:

- Title Bar:** "AddDeleteDeviceDlg" with a close button (X).
- Text Field:** Labeled "Enter IP address of device", containing the text "192.168.9.201".
- Checkbox:** Labeled "Mark as device in use (online mode)".
- Buttons:** "Add" and "Delete" buttons are located below the checkbox.
- List Box:** Labeled "On Line Device", currently empty.
- Bottom Buttons:** "OK", "Cancel", and "Delete" buttons.

898e

898d

898g

Fig. 6c

Fig. 6c is a screenshot of the same "AddDeleteDeviceDlg" dialog box, showing a different state:

- Title Bar:** "AddDeleteDeviceDlg" with a close button (X).
- Text Field:** Labeled "Enter IP address of device", which is now empty.
- Checkbox:** Labeled "Mark as device in use (online mode)".
- Buttons:** "Add" and "Delete" buttons are located below the checkbox. The "Add" button is disabled.
- List Box:** Labeled "On Line Device", now containing one entry: "192.168.9.201" with a checkbox to its left.
- Bottom Buttons:** "OK", "Cancel", and "Delete" buttons.

898d

898g

898m

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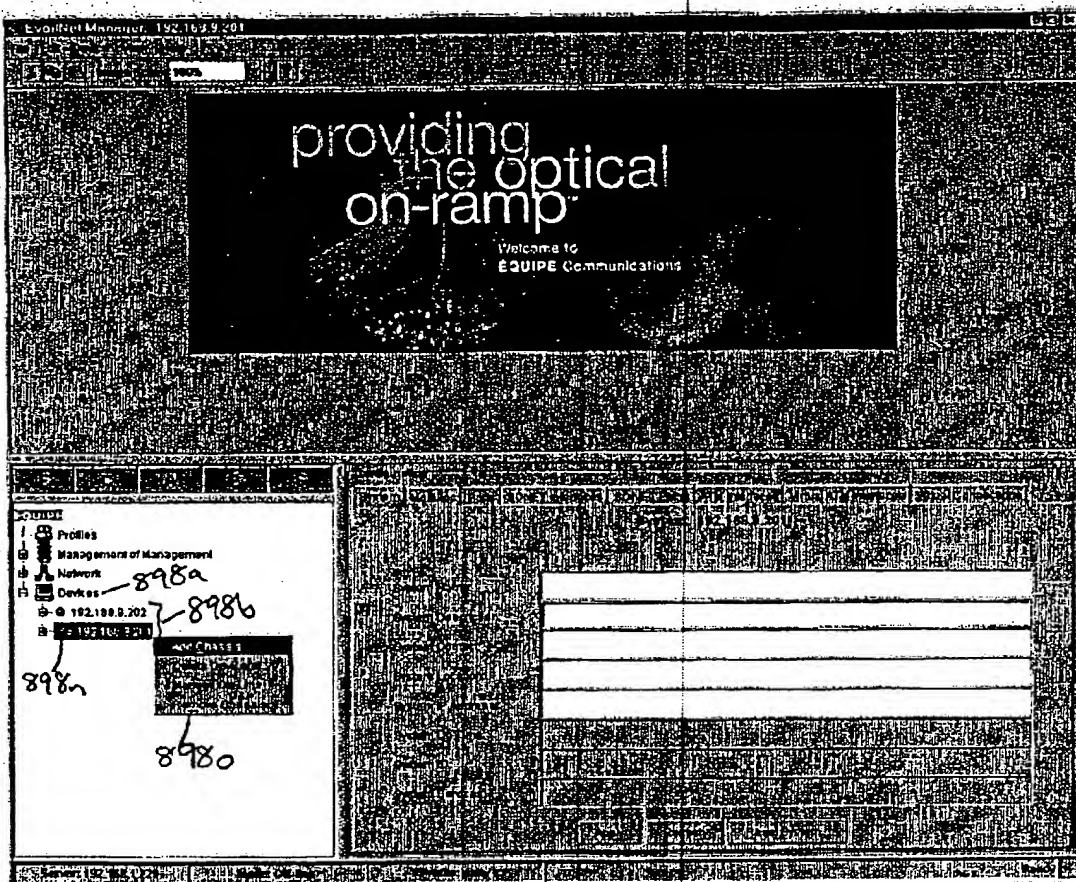


Fig. 6d

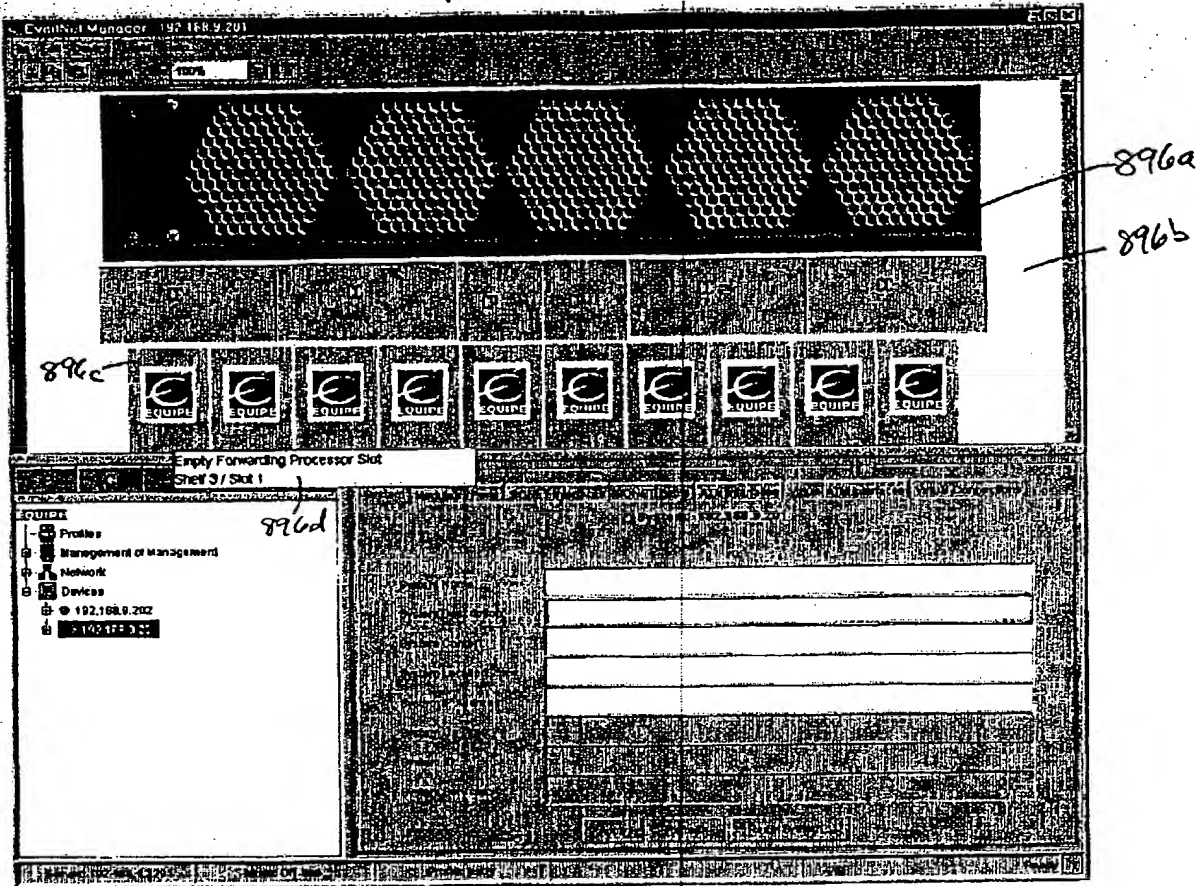


Fig. 6e

895

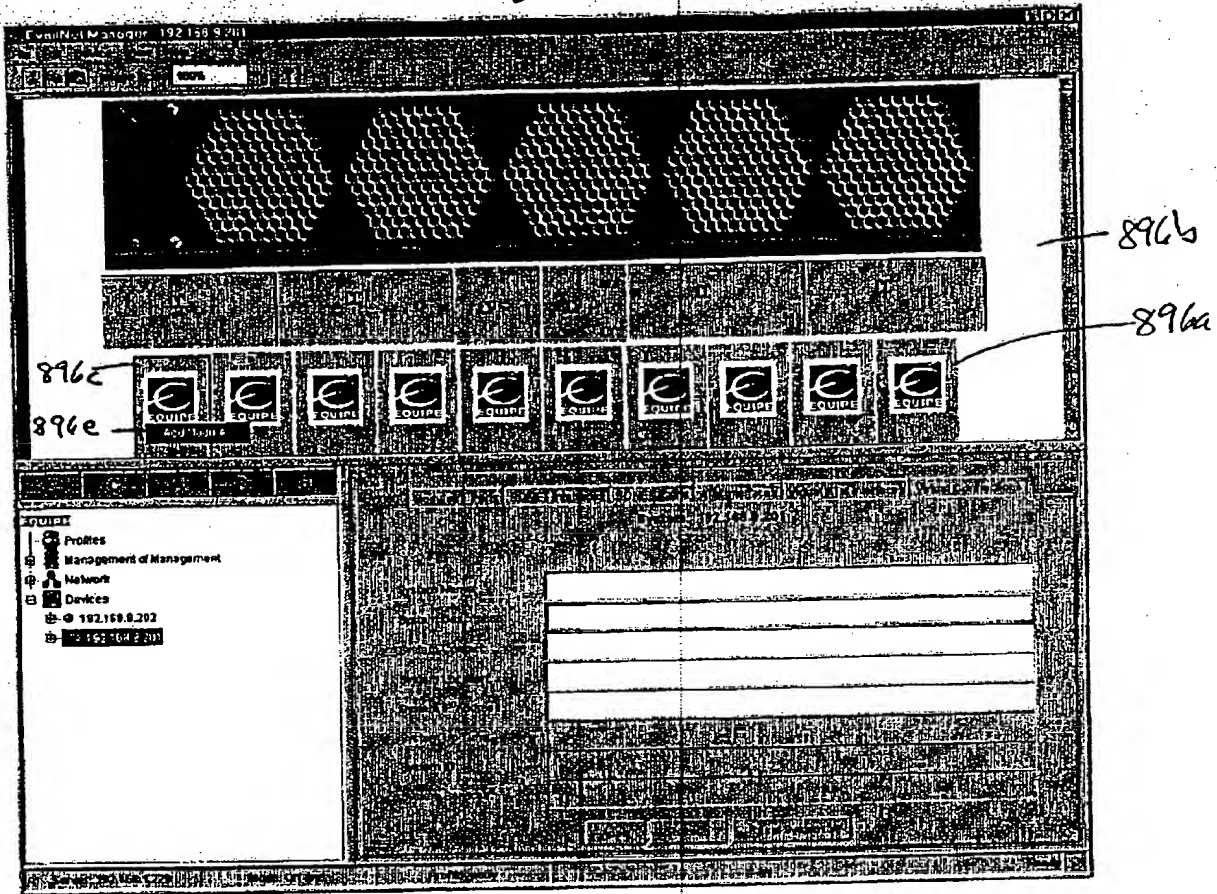


Fig. 6f

895

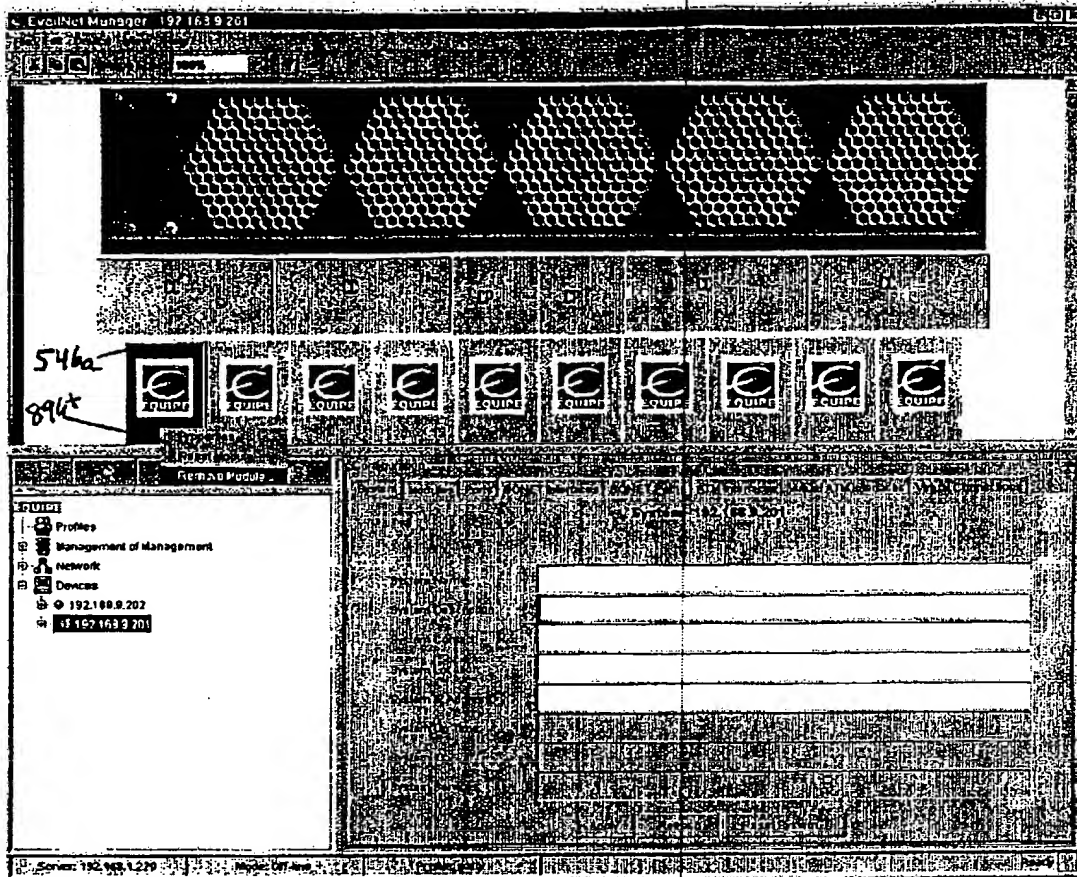


Fig. 69



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896f

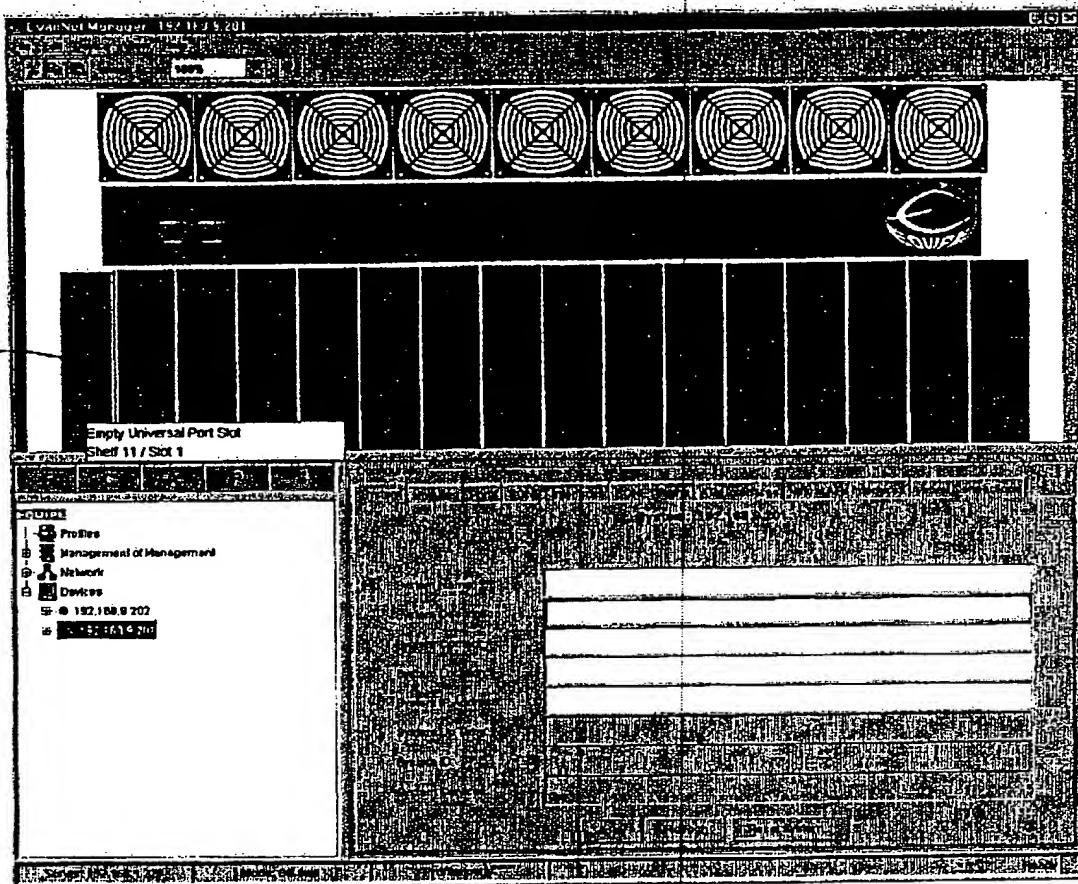


Fig.6h

895

896f

896g

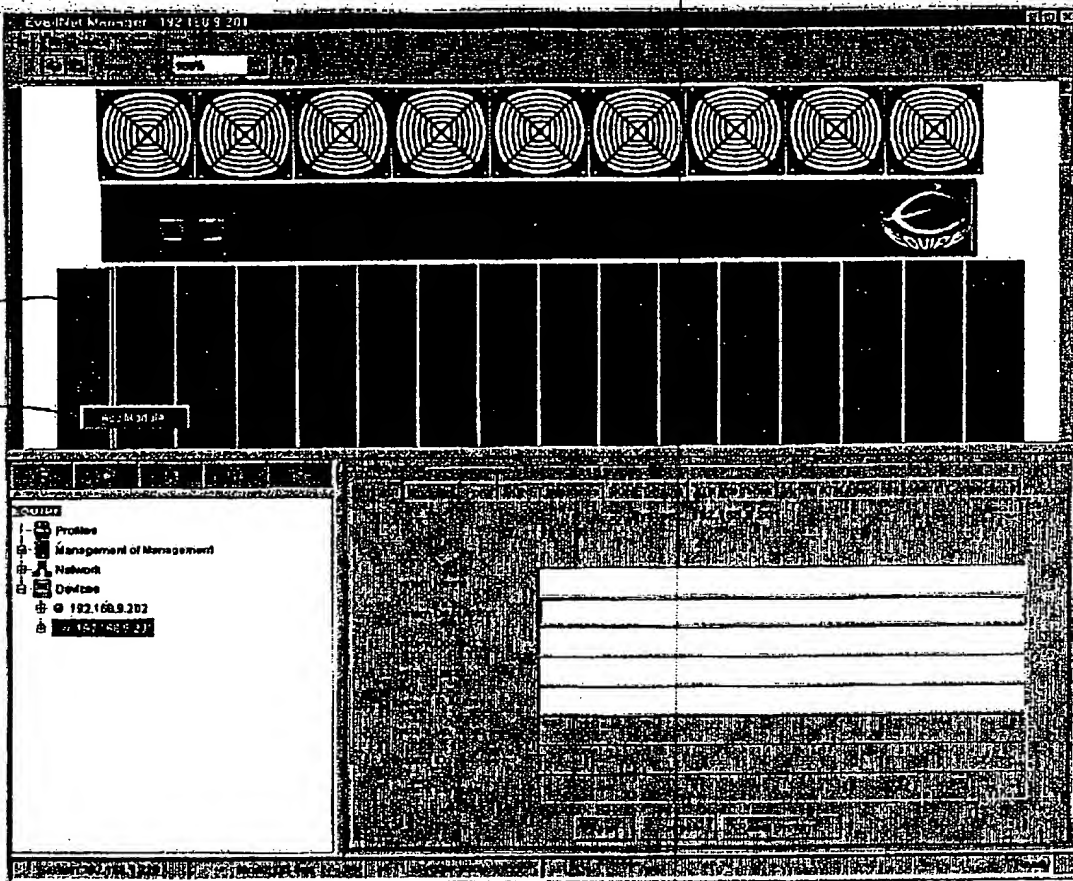


Fig. 6i



Fig. 6j

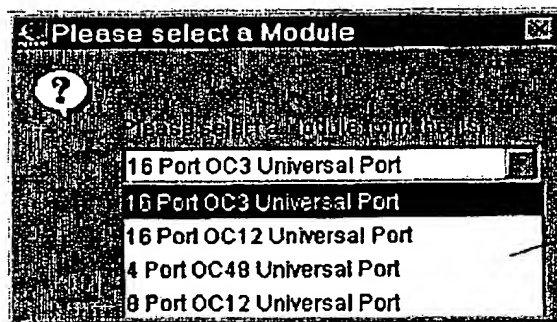
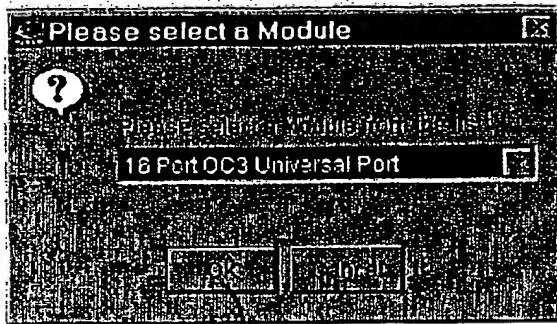


Fig. 6K

895

556i

556h

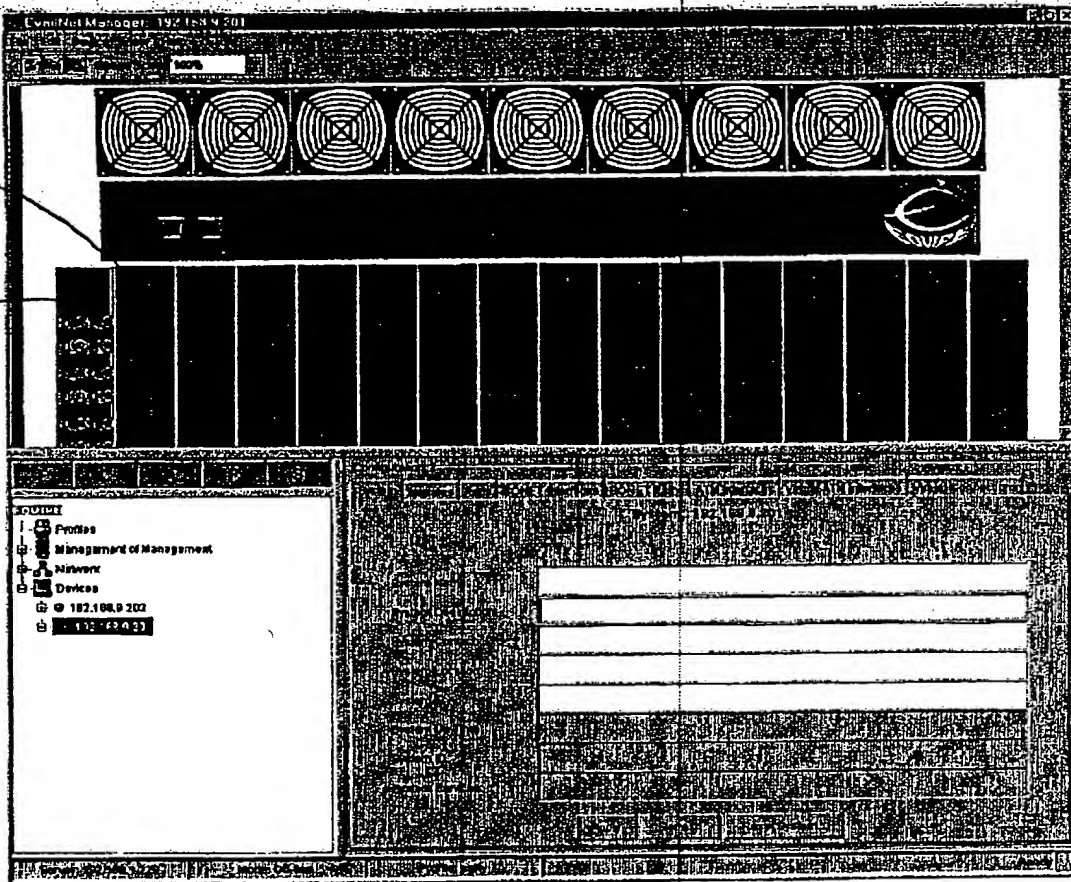


Fig. 61

895

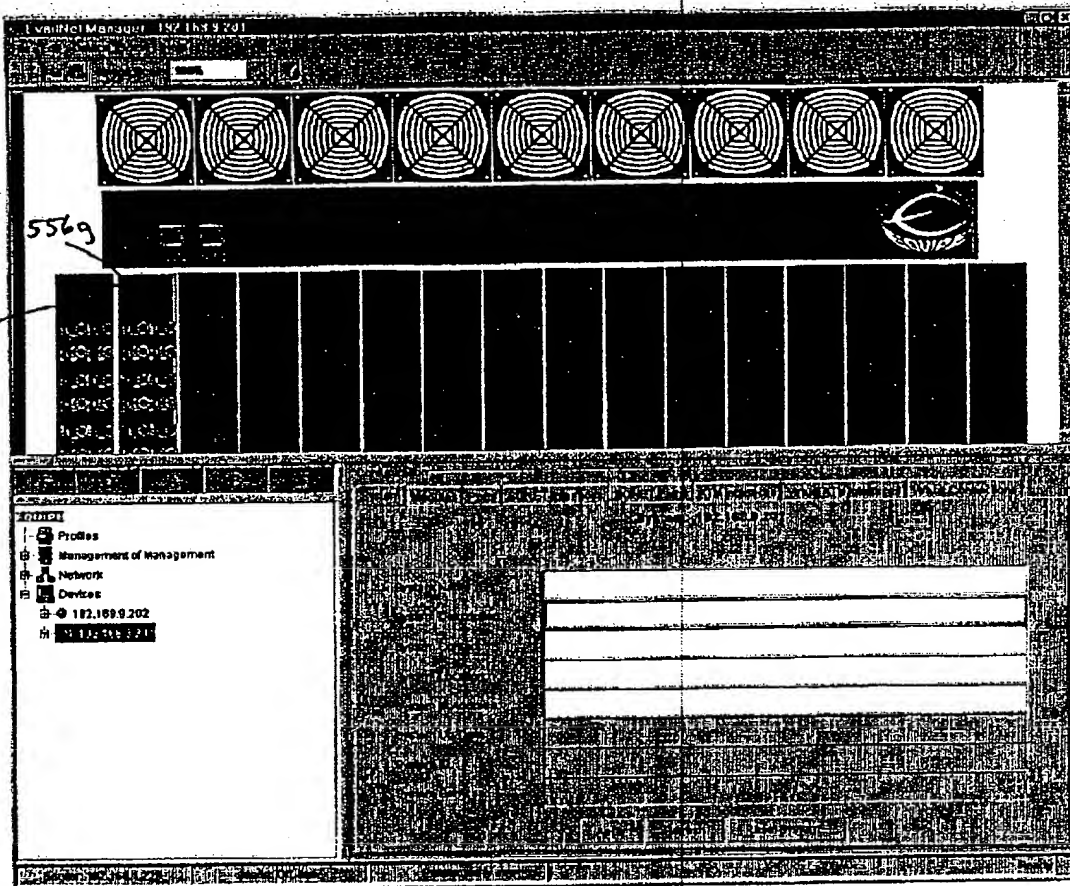


Fig. 6m

895

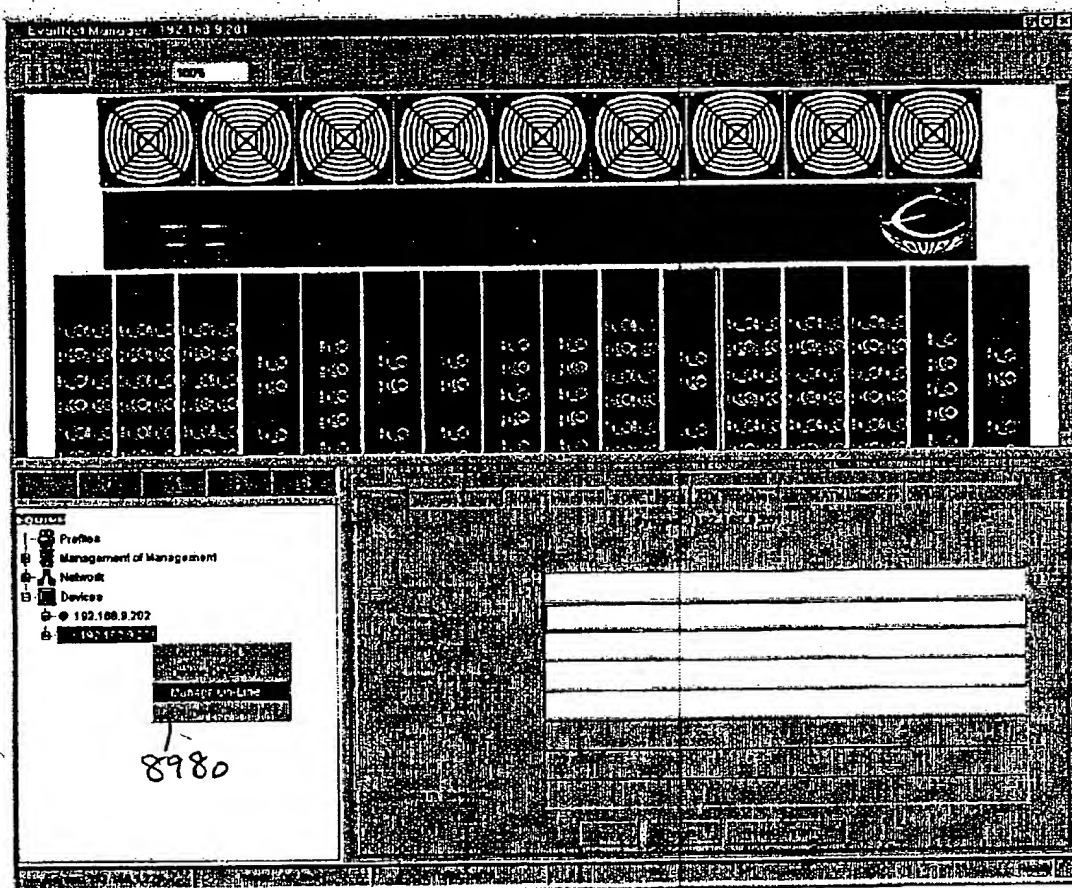


Fig. 6n

895

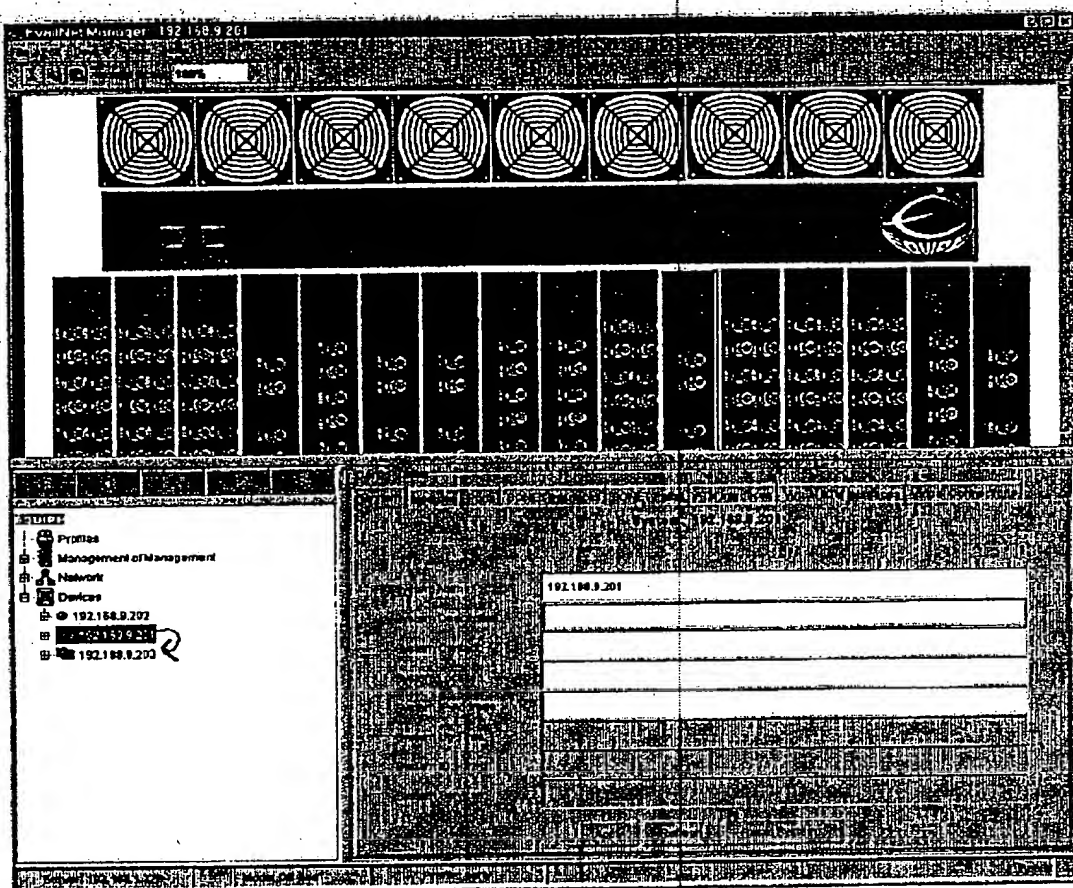


Fig. 60

895

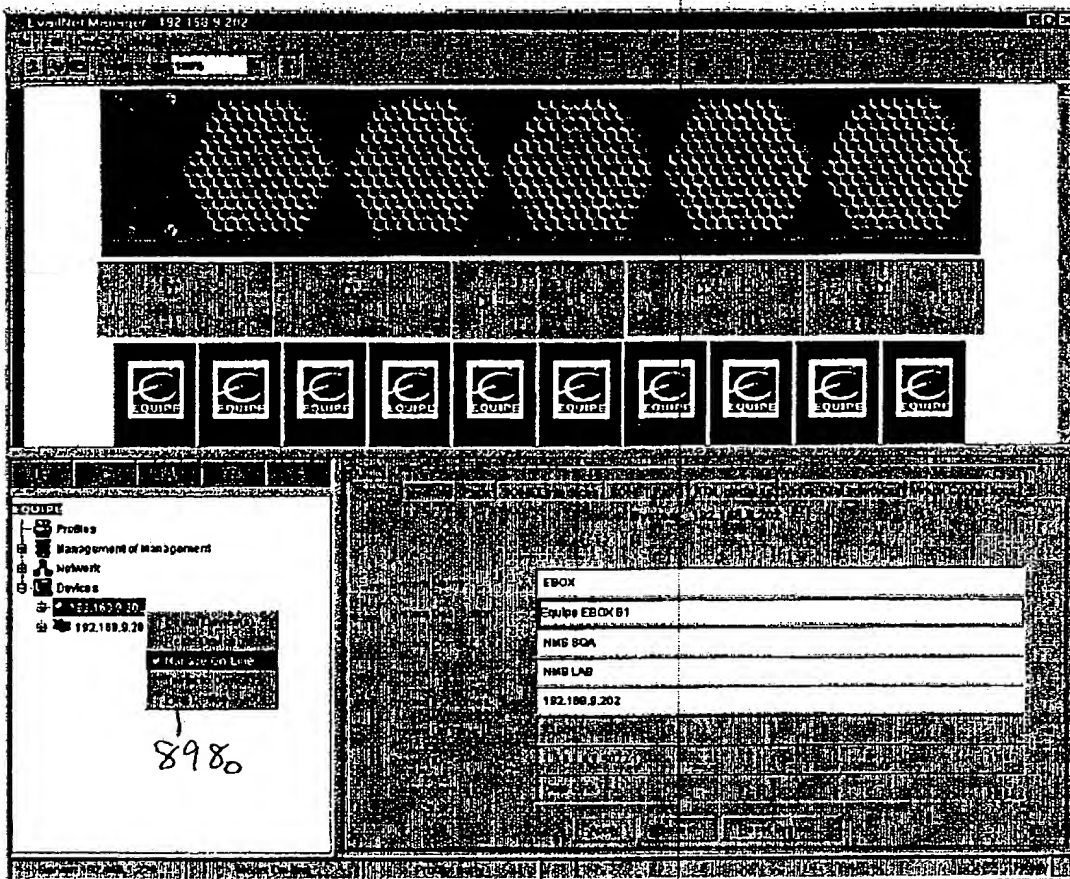


Fig. 6P

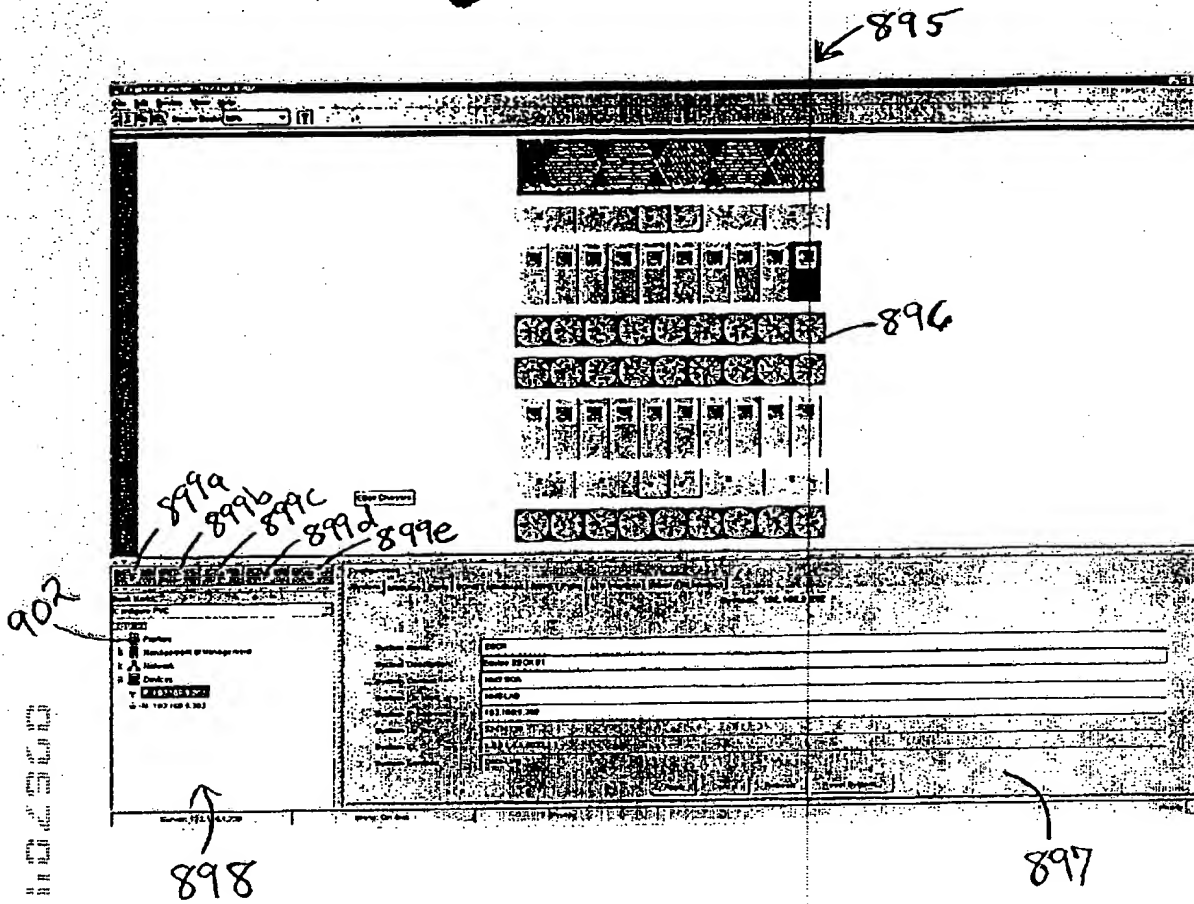


Fig. 7a

EvaluNet Manager: Fault - Event Summary			
System: 192.168.55.160			
System	Event	Event Number	Description
1.1.55.6	Fan OverTemp	44	"Fan marginally functioning"
1.1.55.7	New Board Ins...	75	"New board inserted"
OK			

Fig. 7b



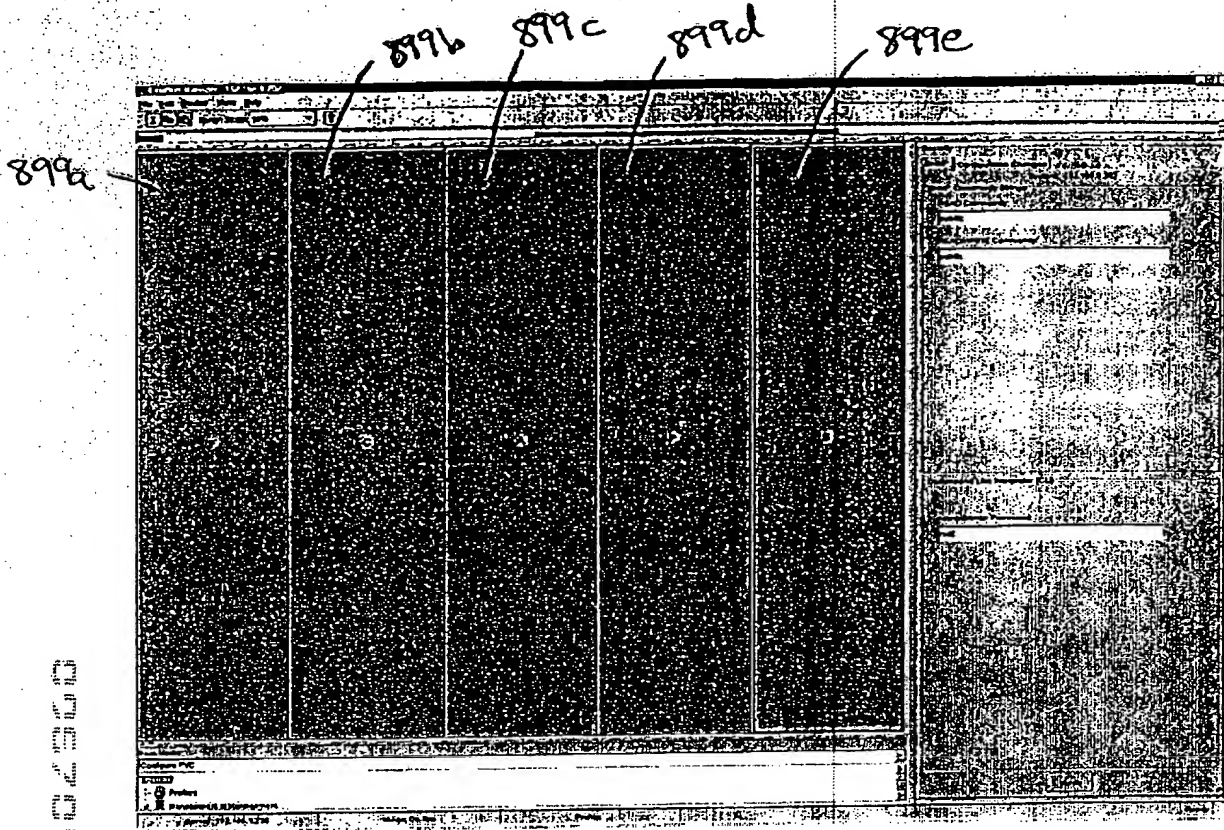
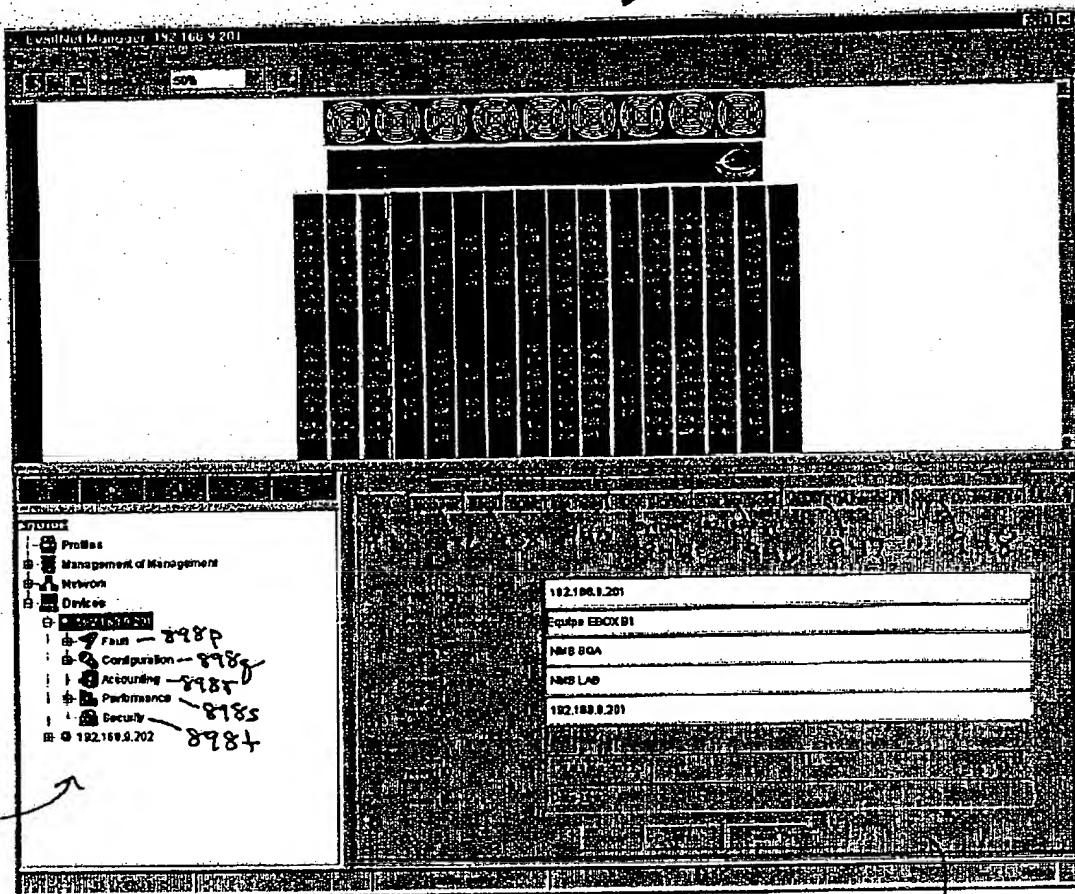


Fig.7c

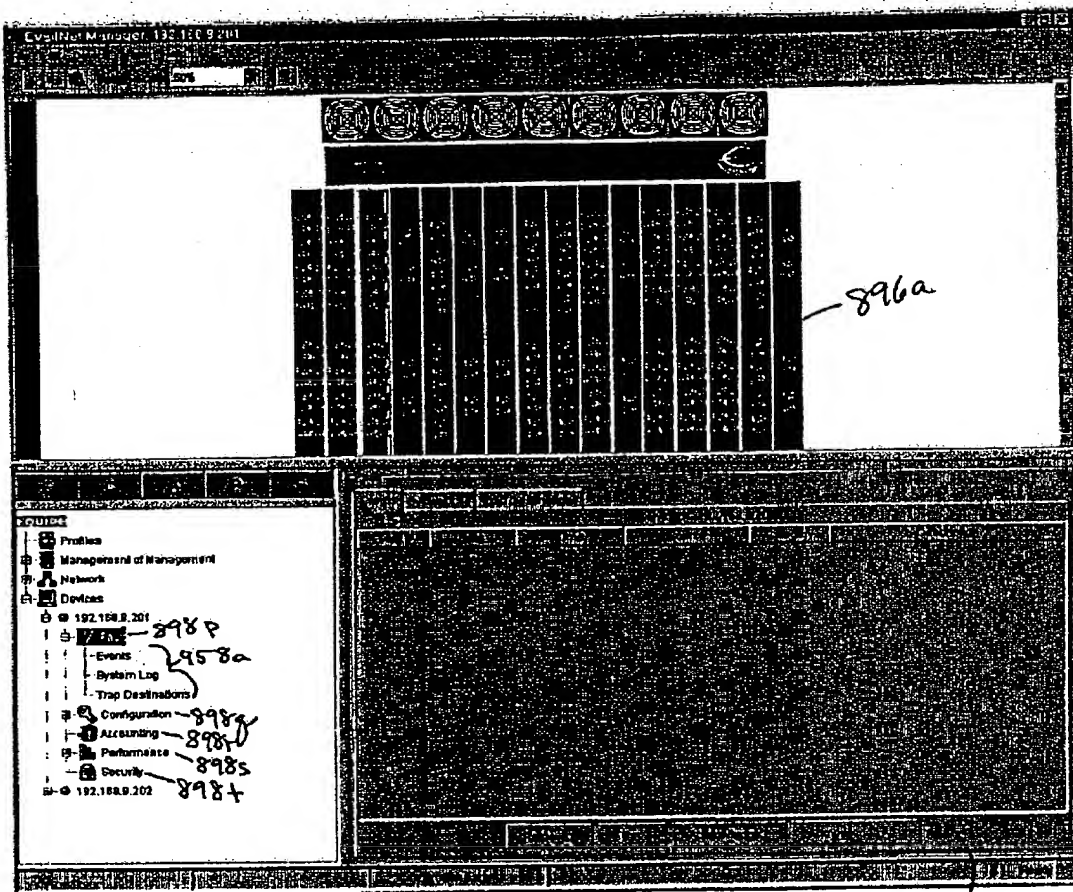
895



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Fig. 7d

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Fig. 7e

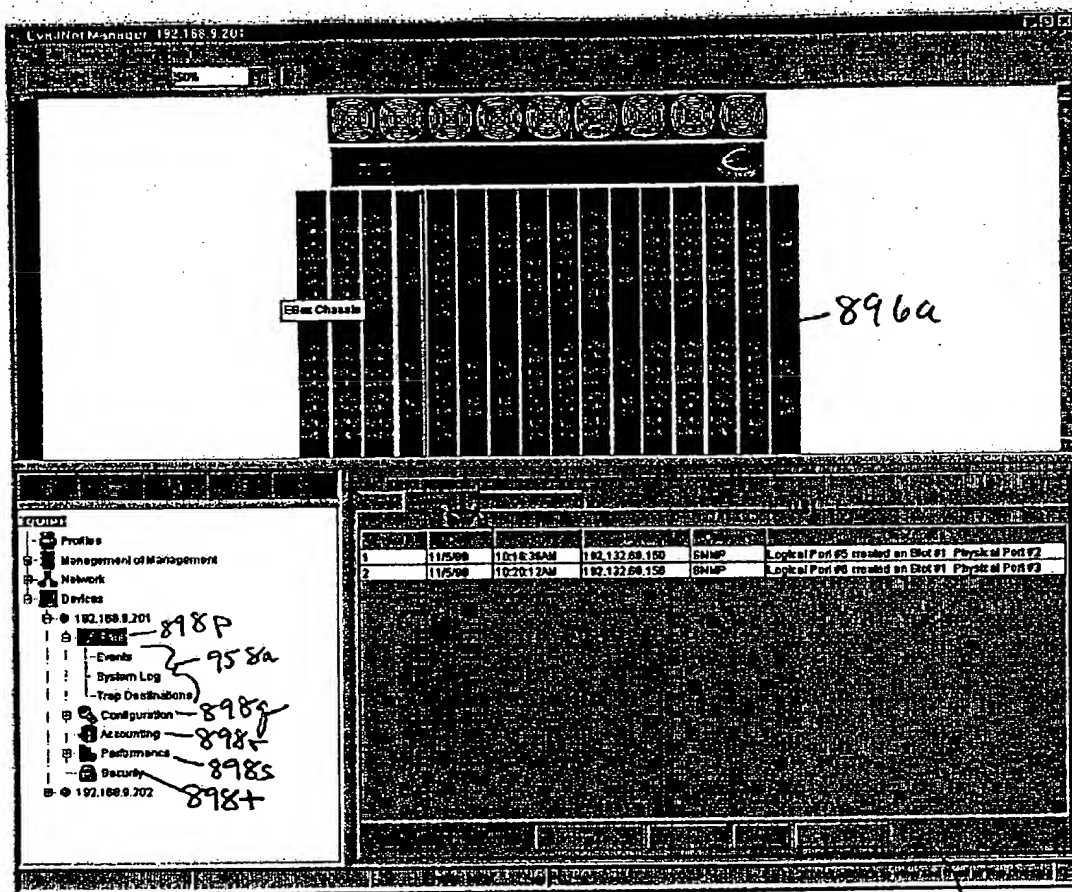
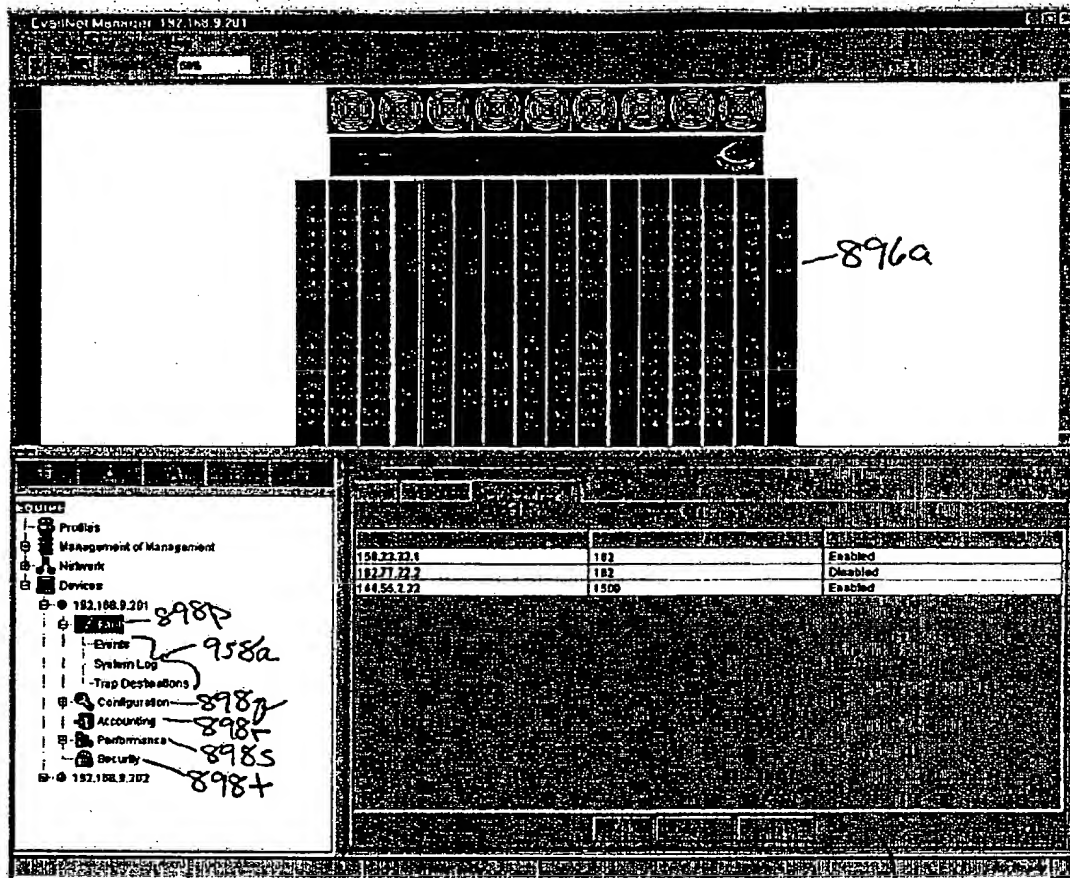


Fig. 24



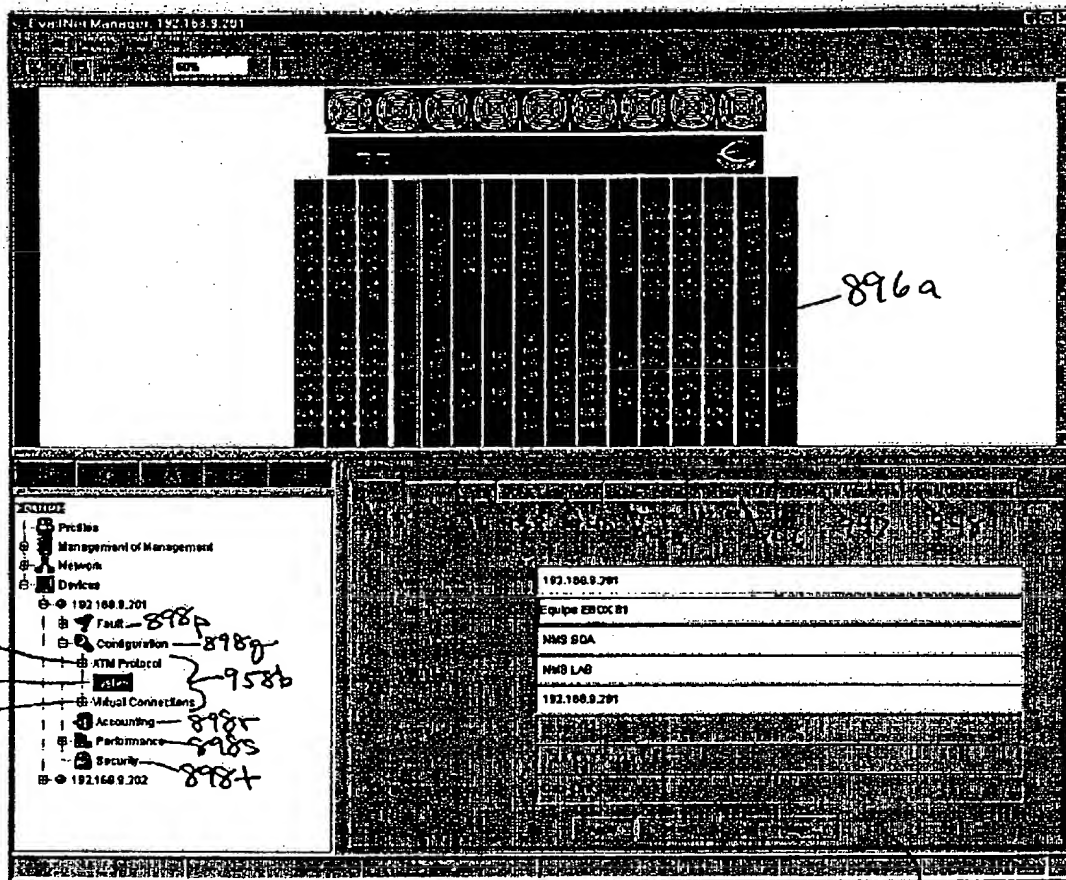
897

Fig. 7g

895

958c  
958d  
958e

192.168.9.201  
Fault - 898P  
Configuration - 898g  
ATM Protocol - 958b  
Mutual Connections  
Accounting - 898r  
Performance - 898s  
Security - 898t  
192.168.9.202



896a

897

Fig. 7h

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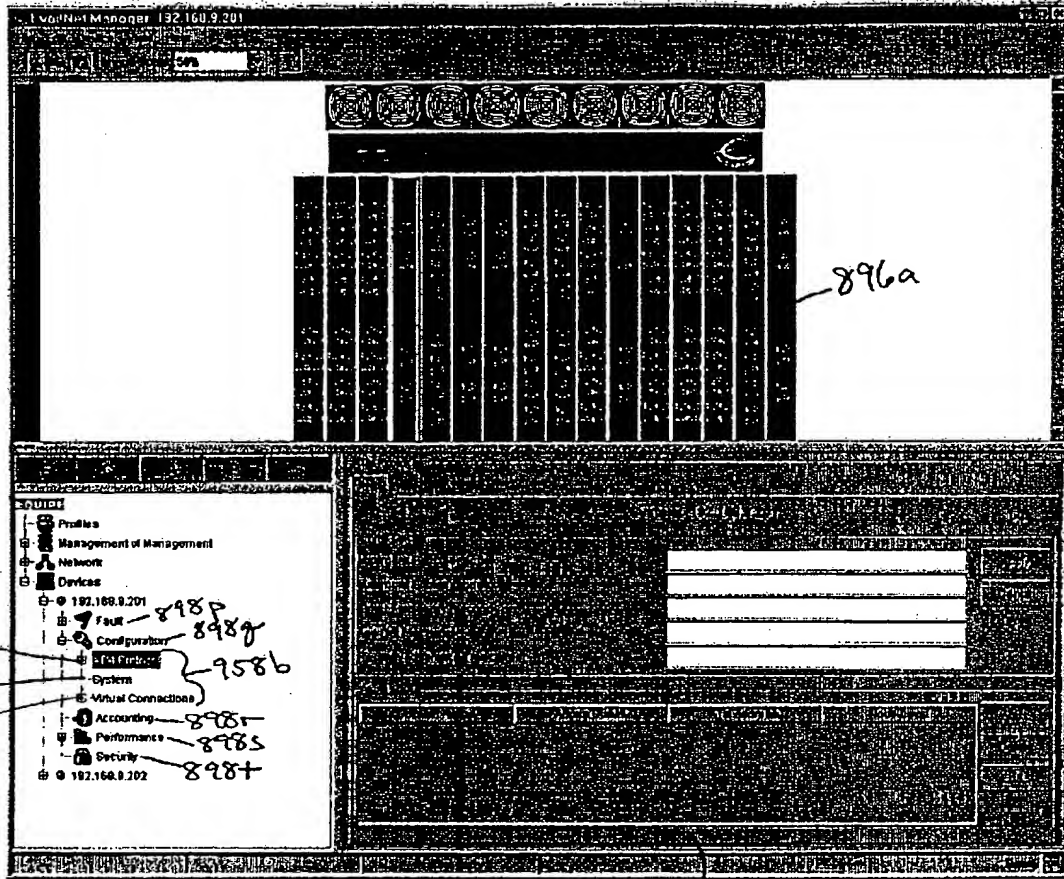


Fig. 7i

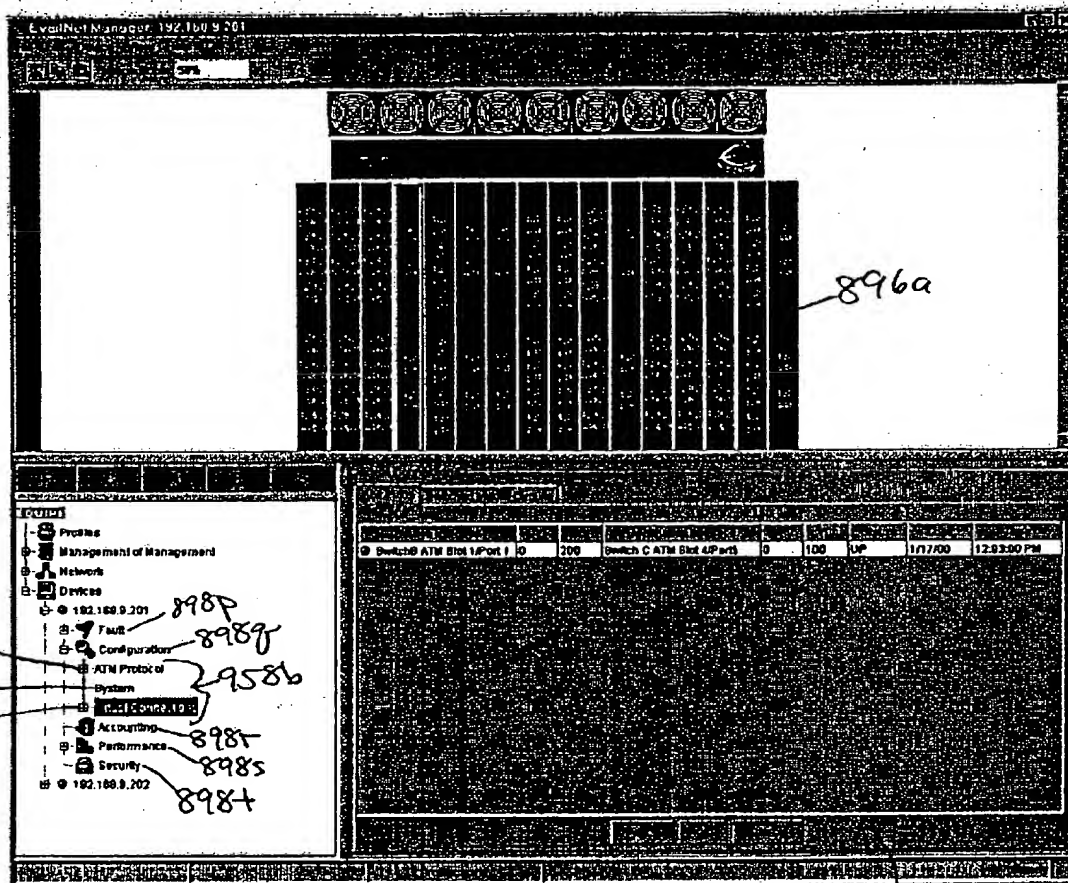


Fig. 7j



← 895

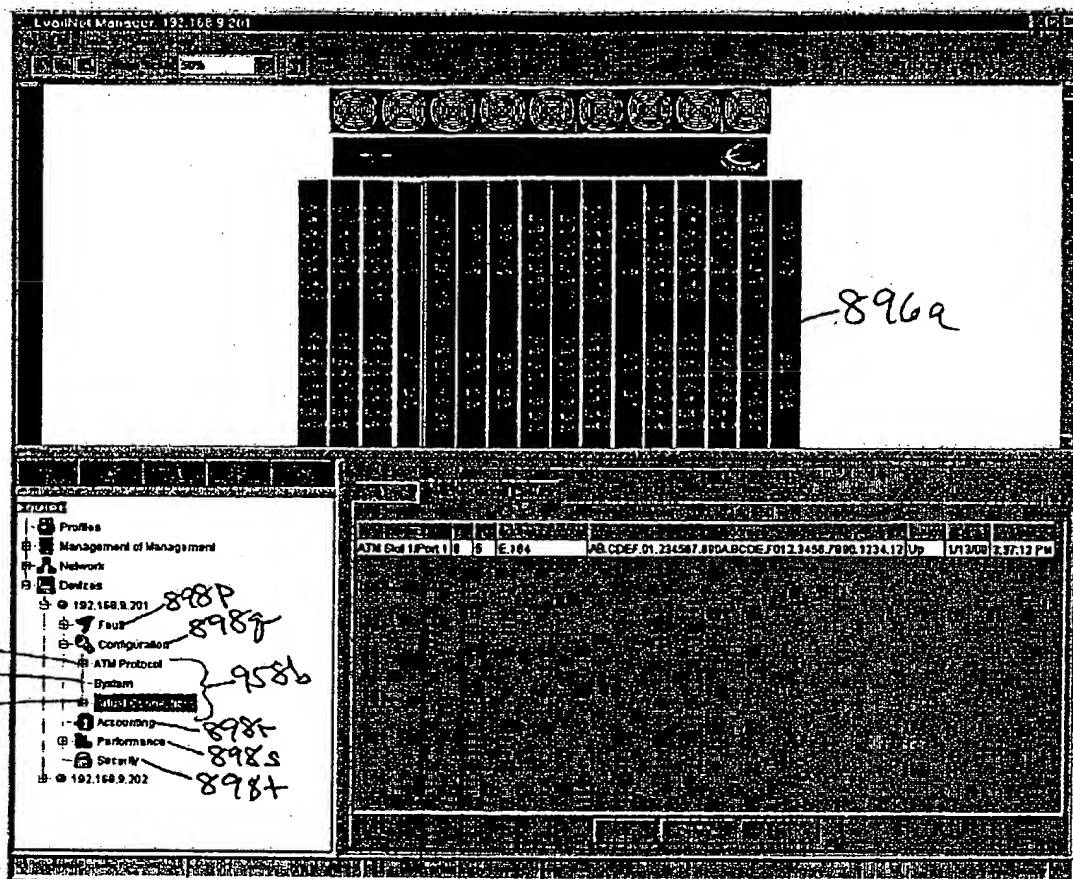


Fig. 7K

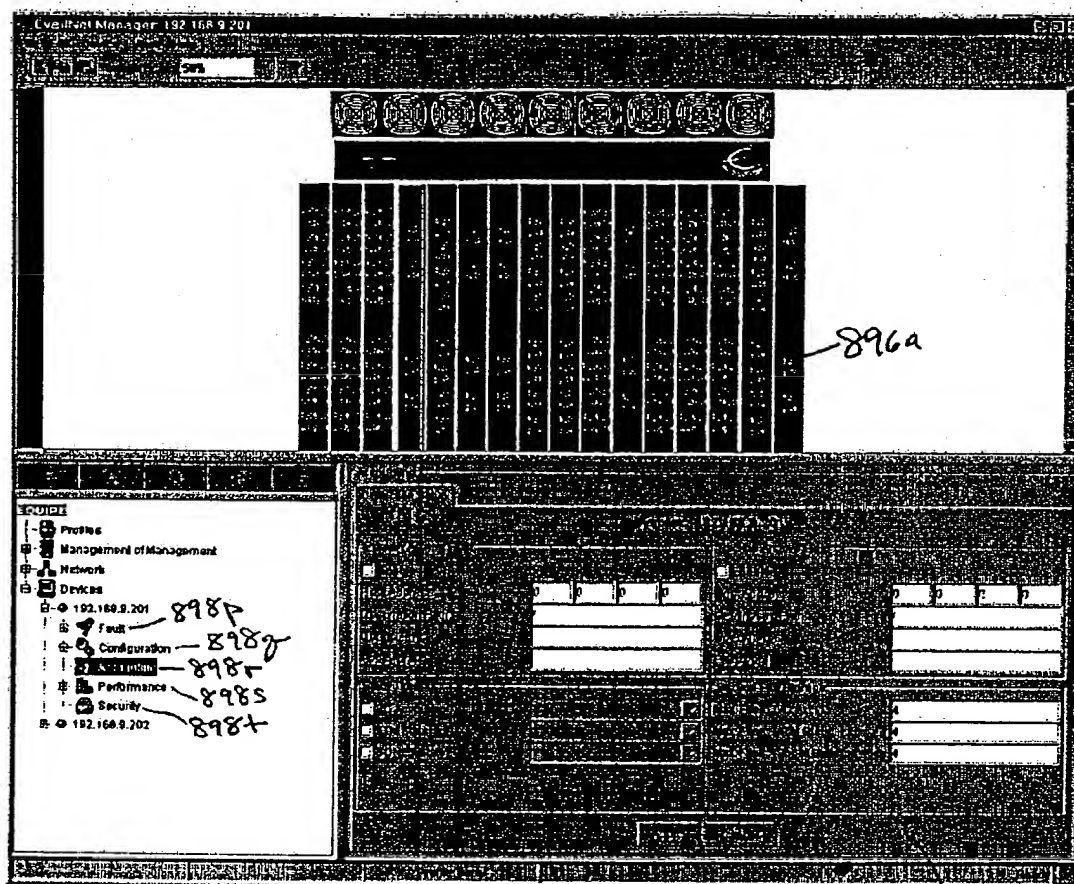


Fig. 7L

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896a

898P  
898g  
898f  
898s  
898t  
958g  
958h  
958i  
958j  
958k

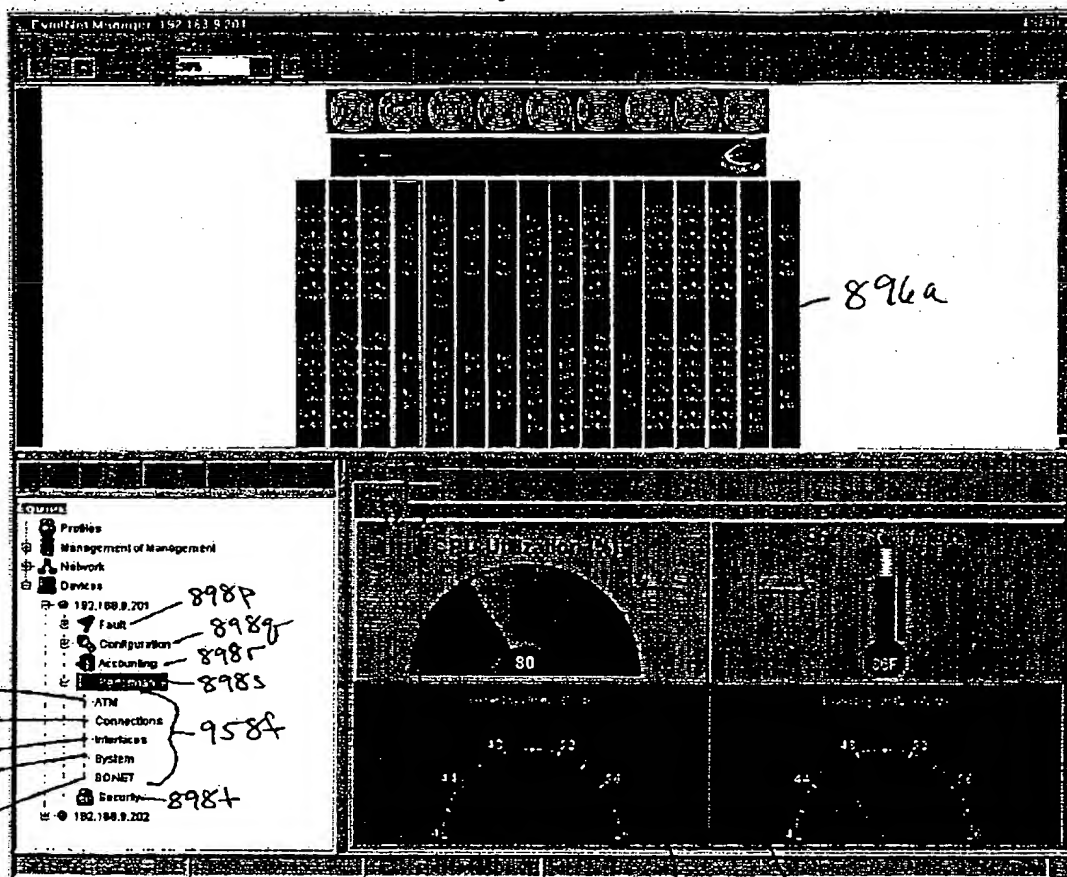


Fig. 7m

895

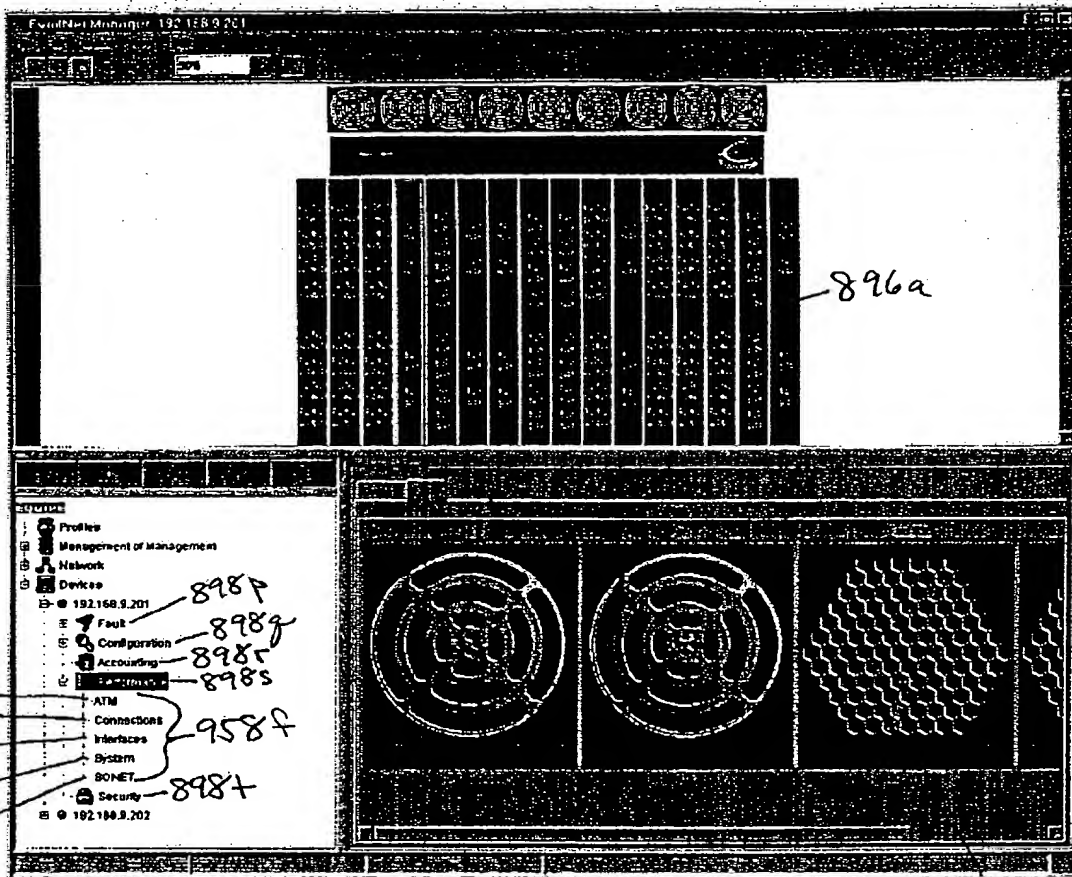


Fig. 7n

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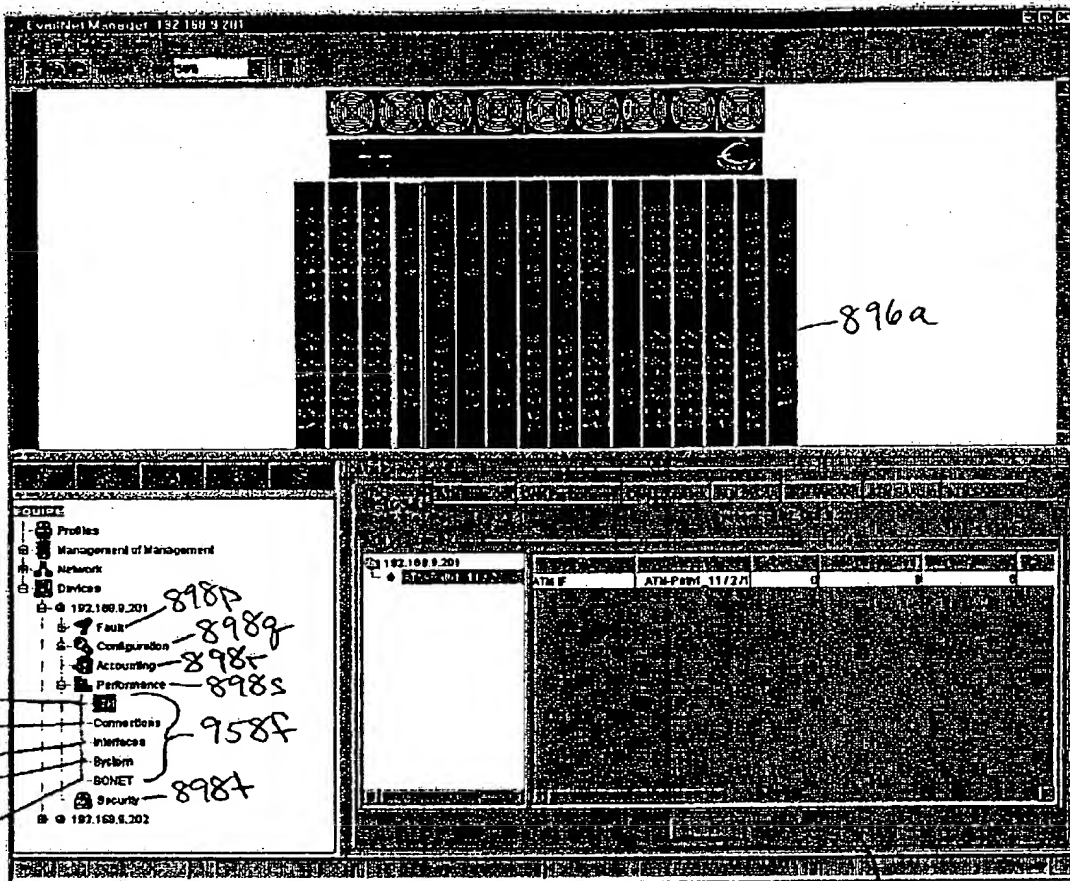
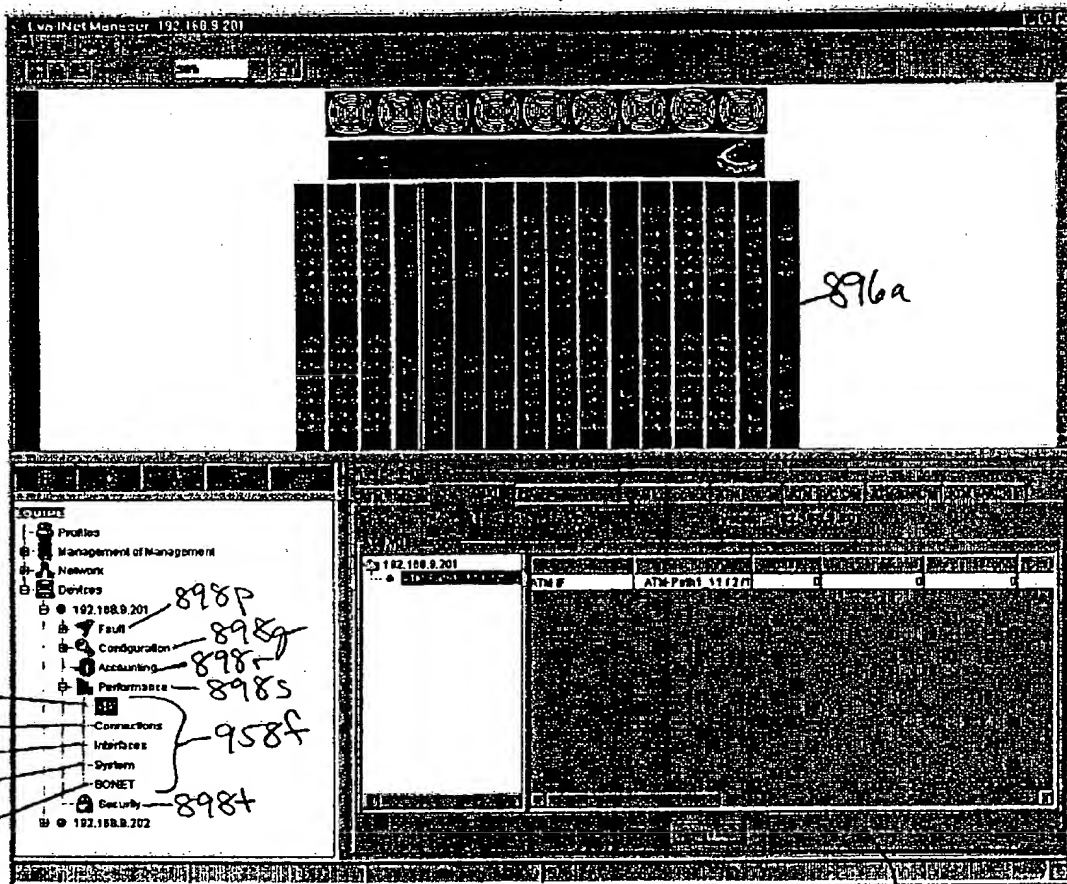


Fig. 70

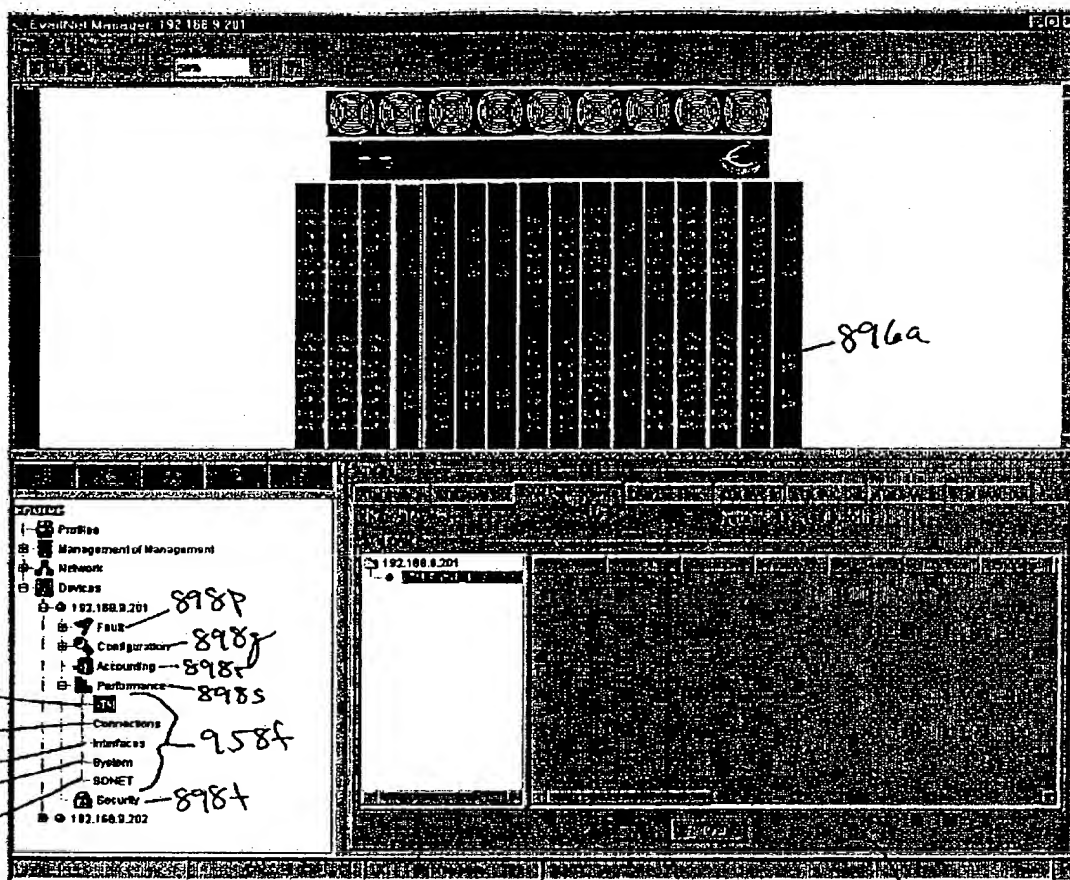
895



897

Fig. 7p

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Fig. 7g

895

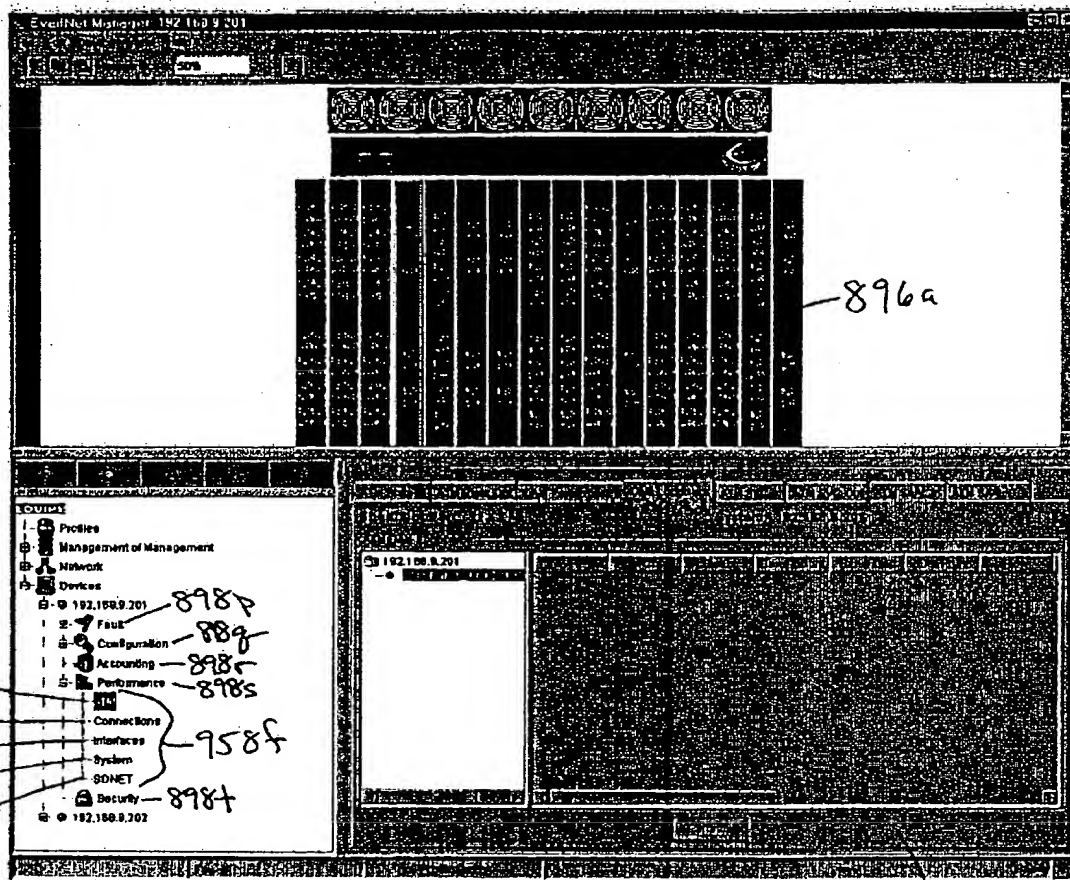
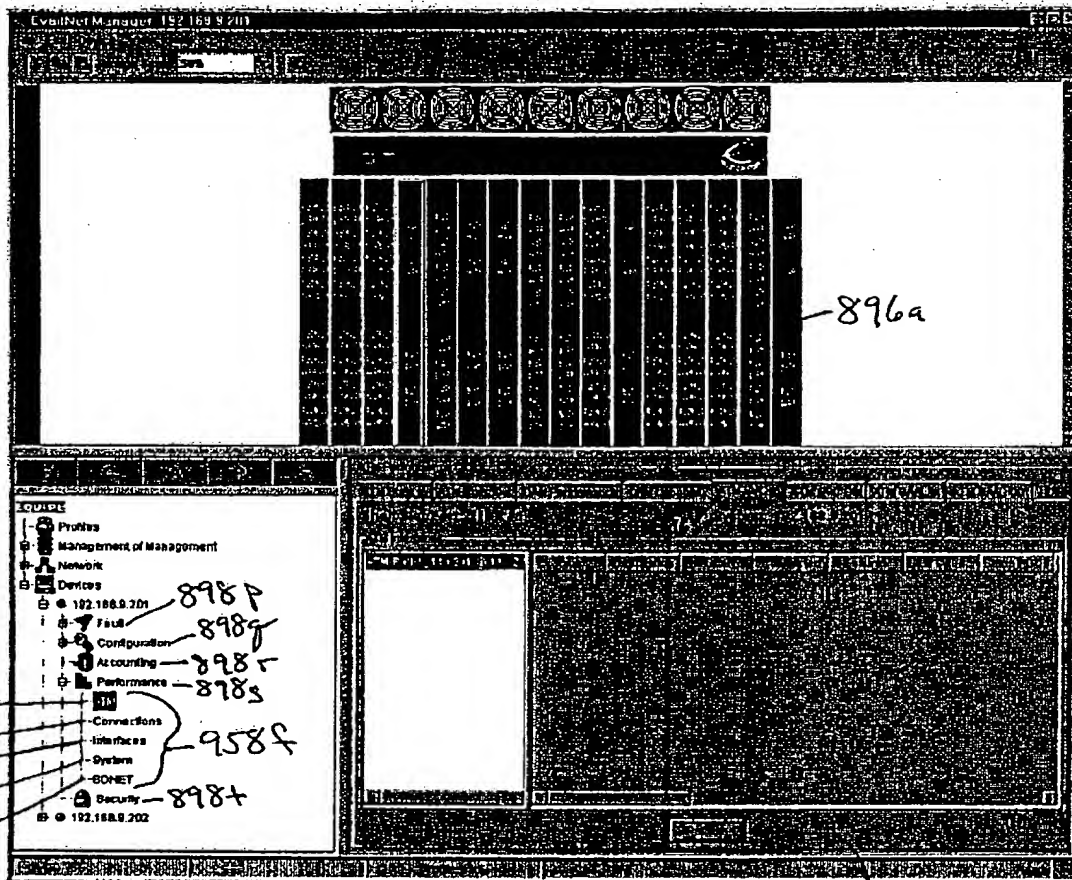


Fig. 7r



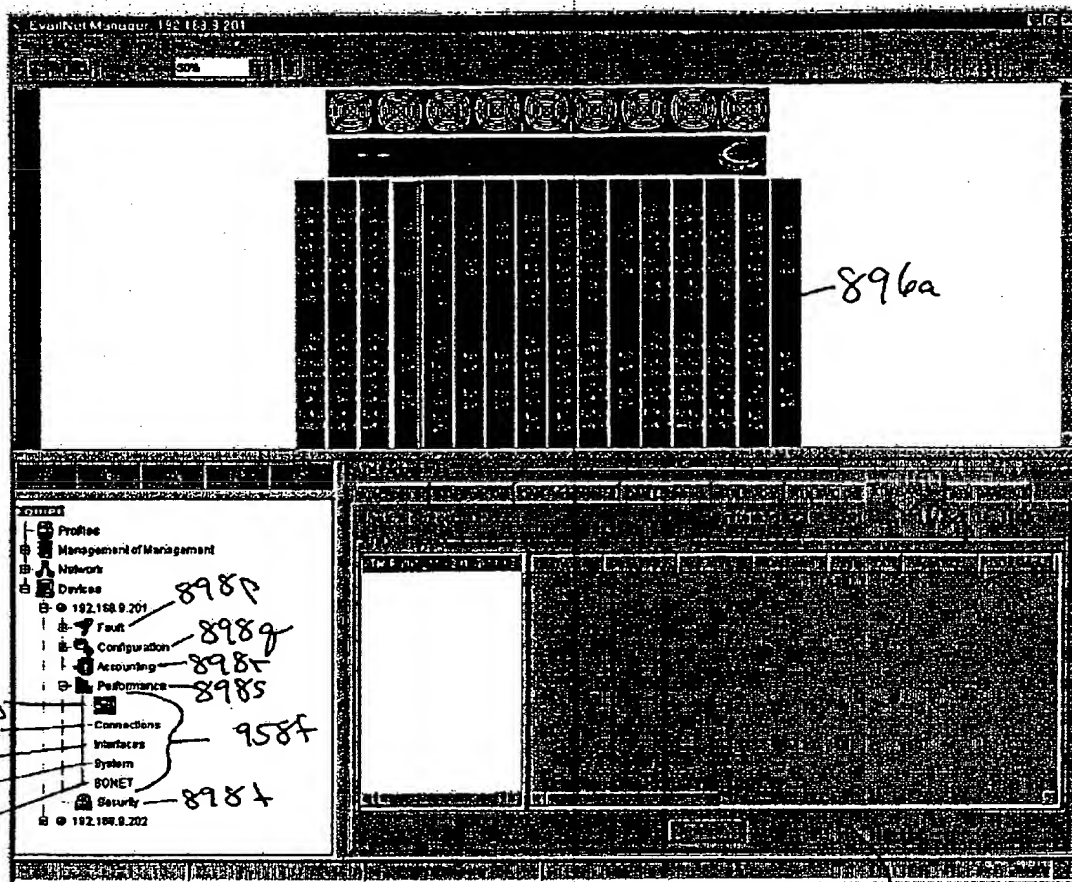
895



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Fig. 7s

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Fig. 7u

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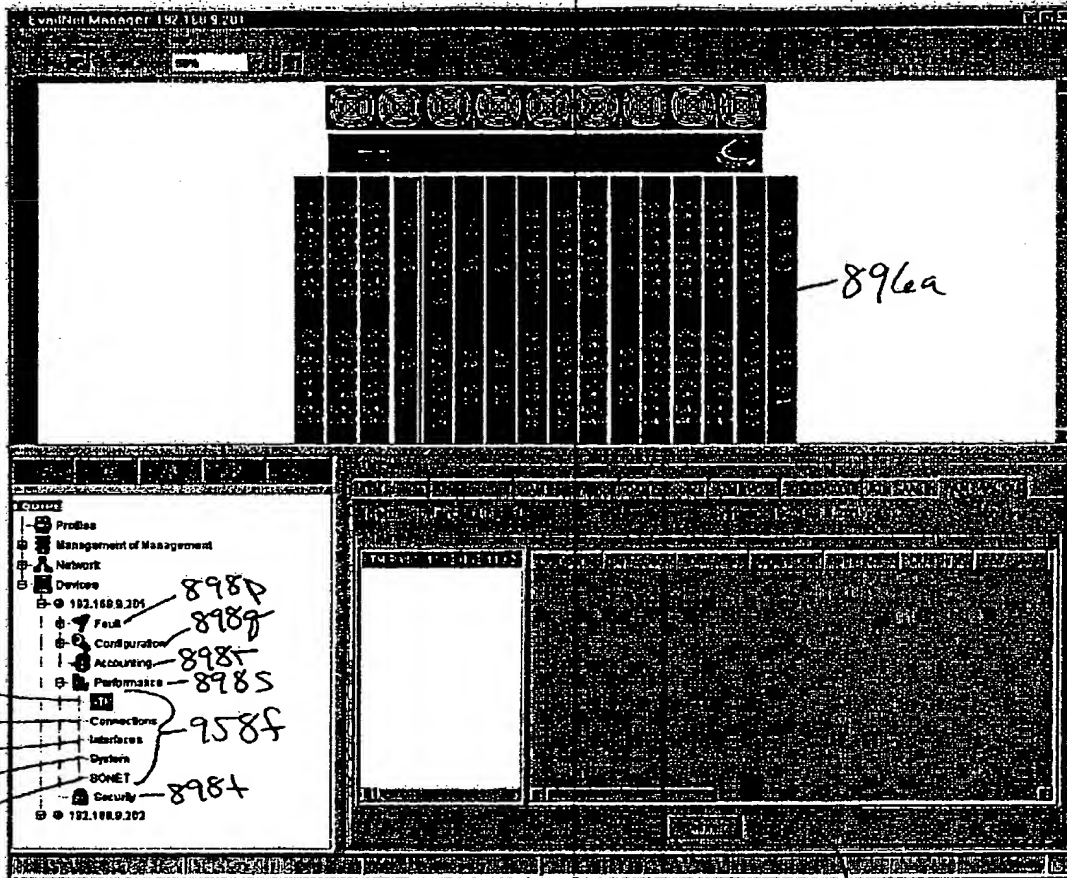
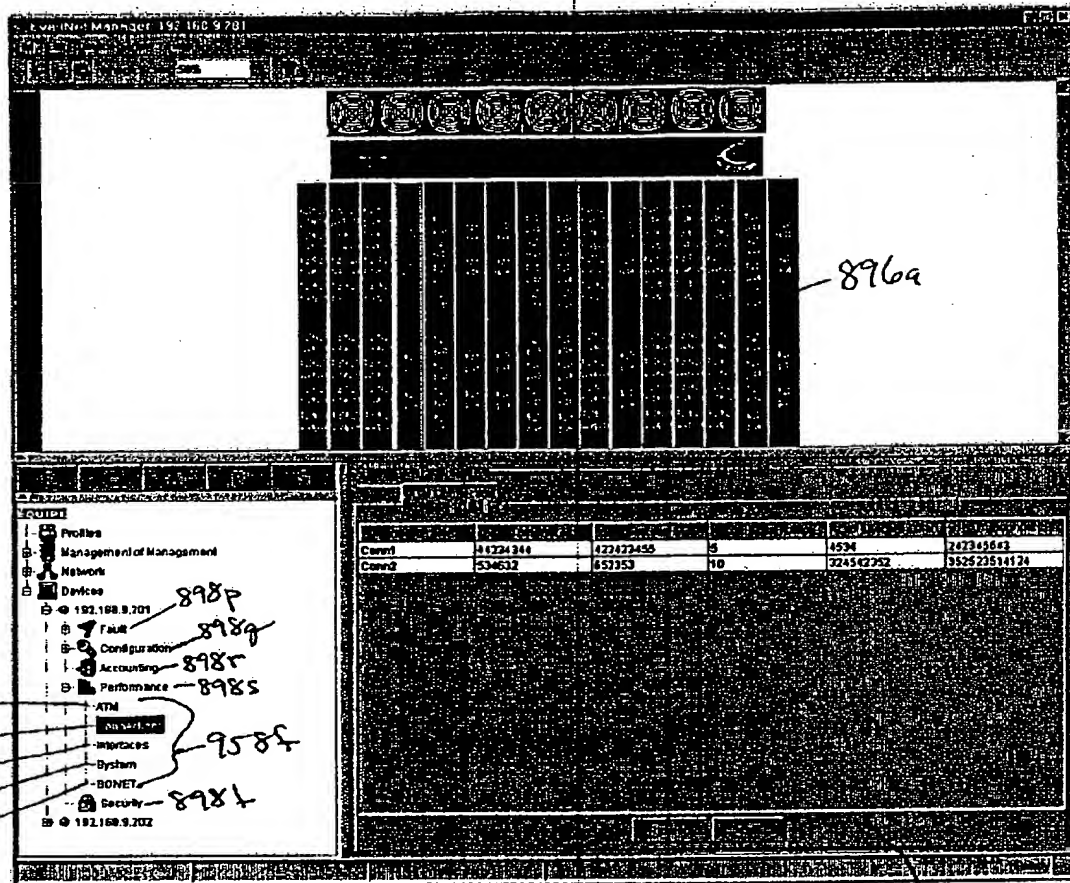
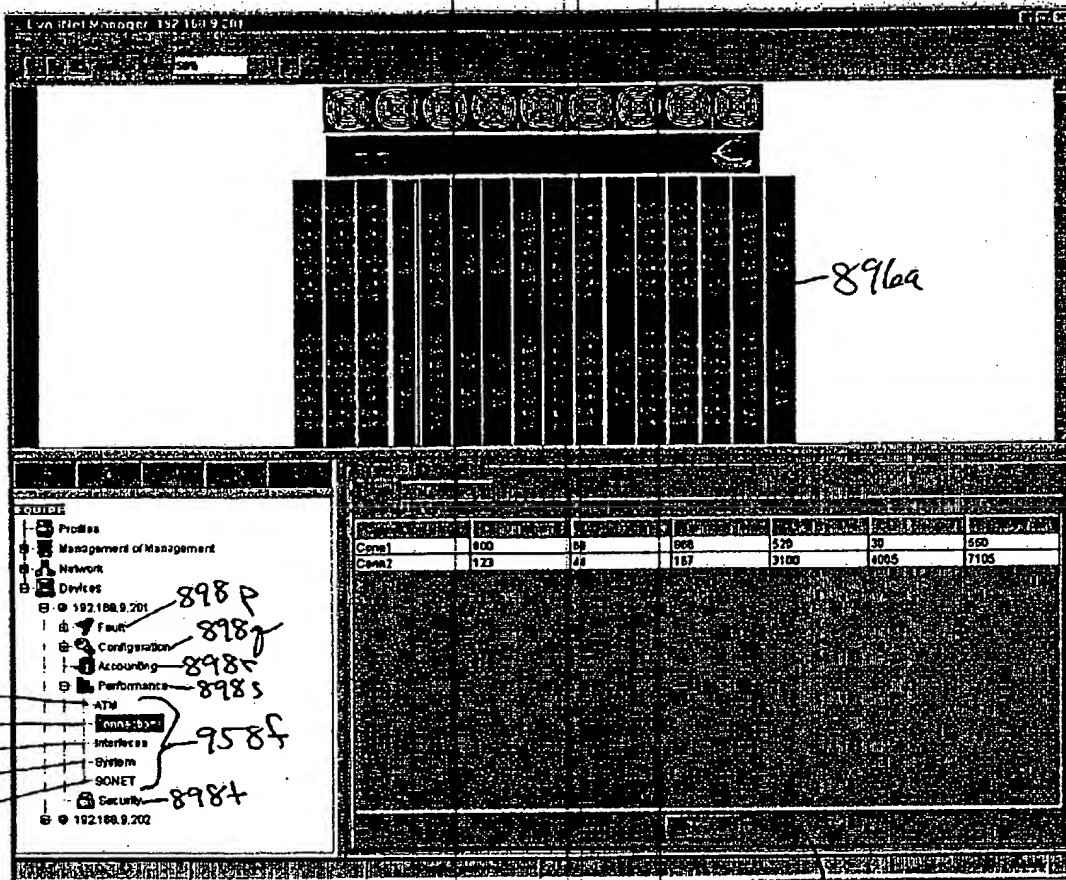


Fig. 7v



895



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Fig. 7x

895

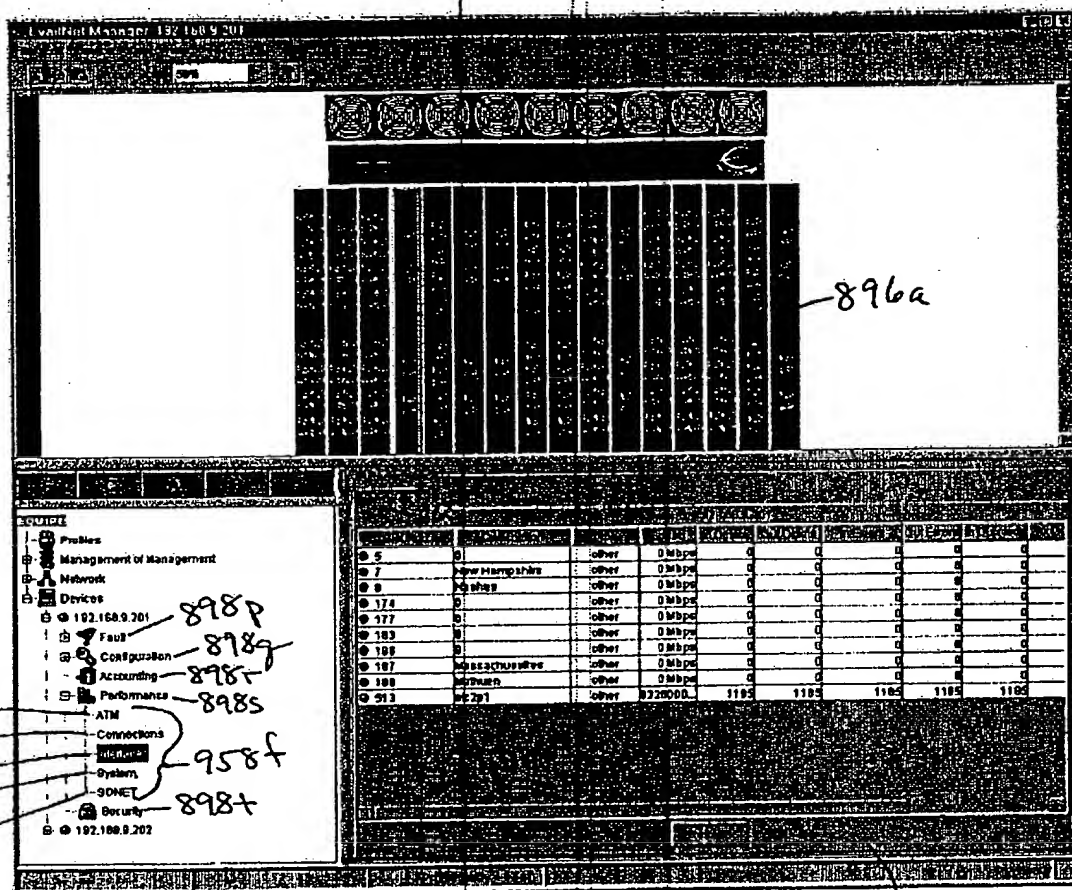
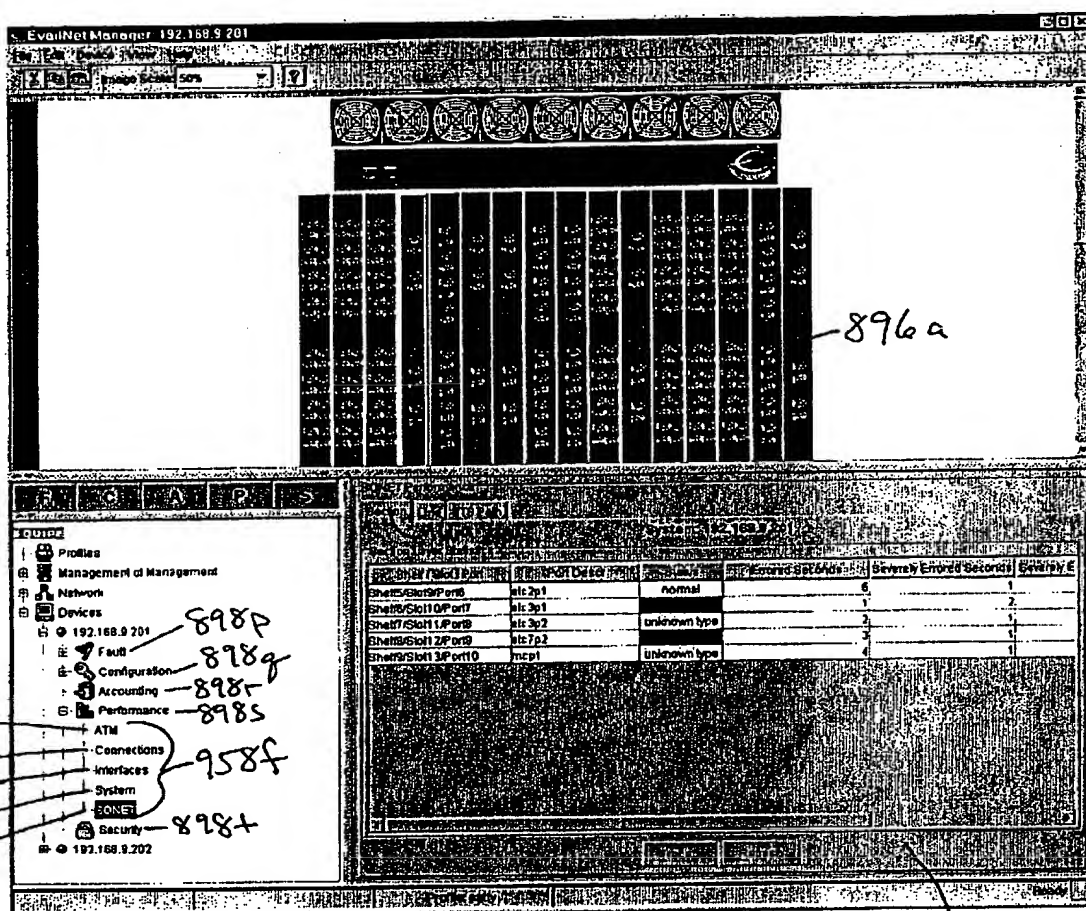


Fig. 7y

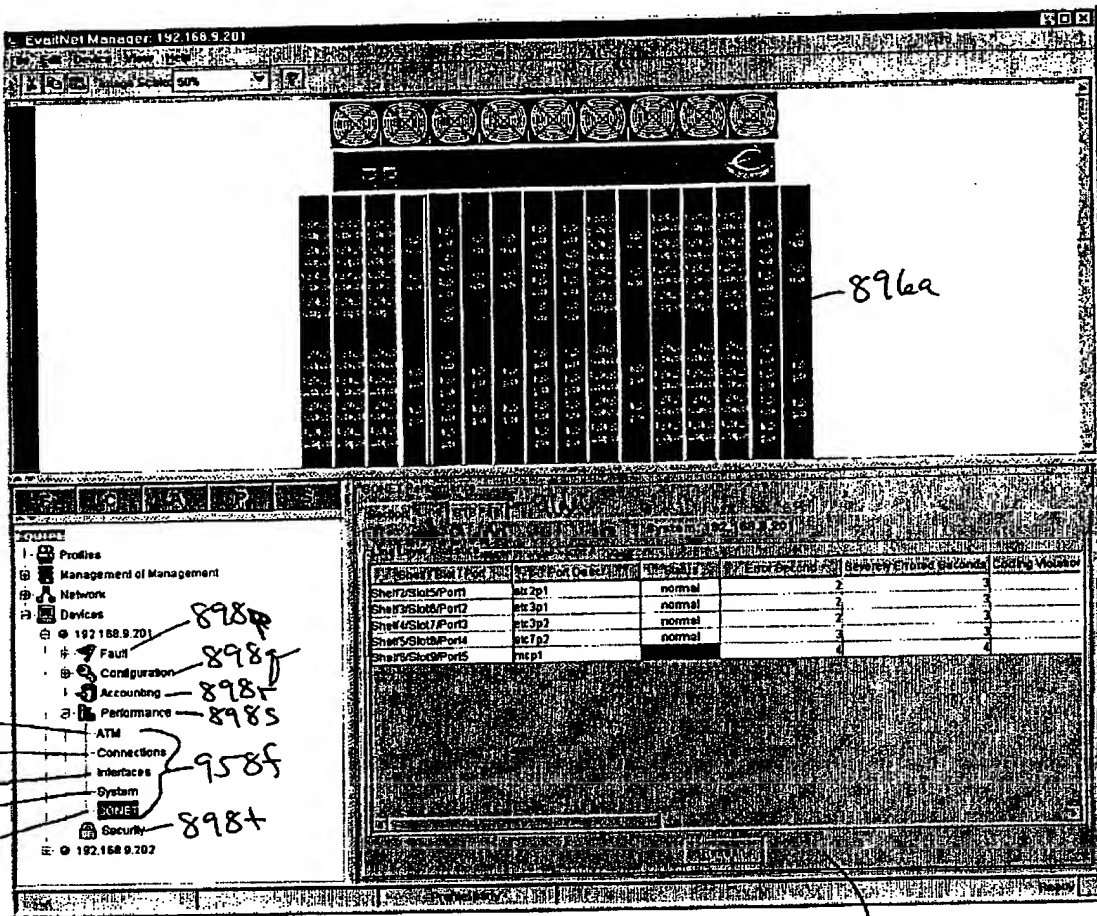
895



897

Fig. 8a

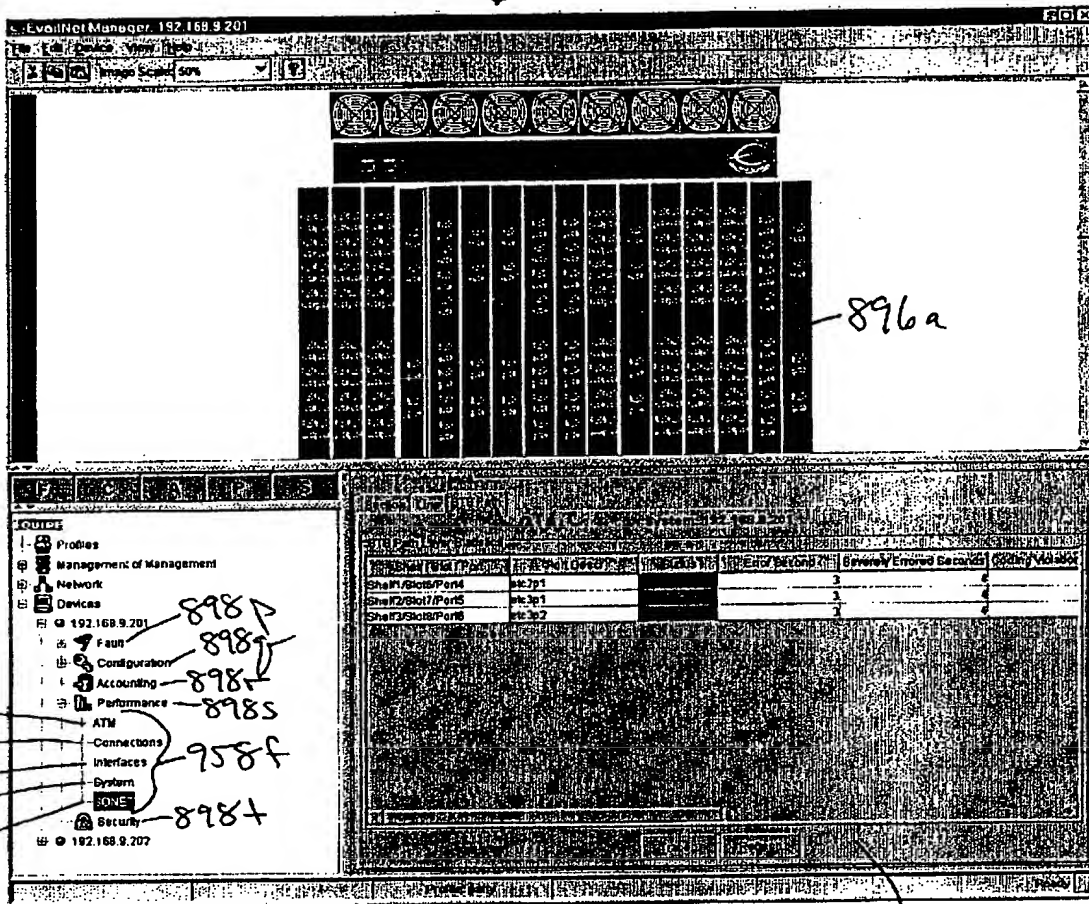
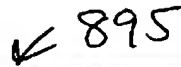
895



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Fig. 8b

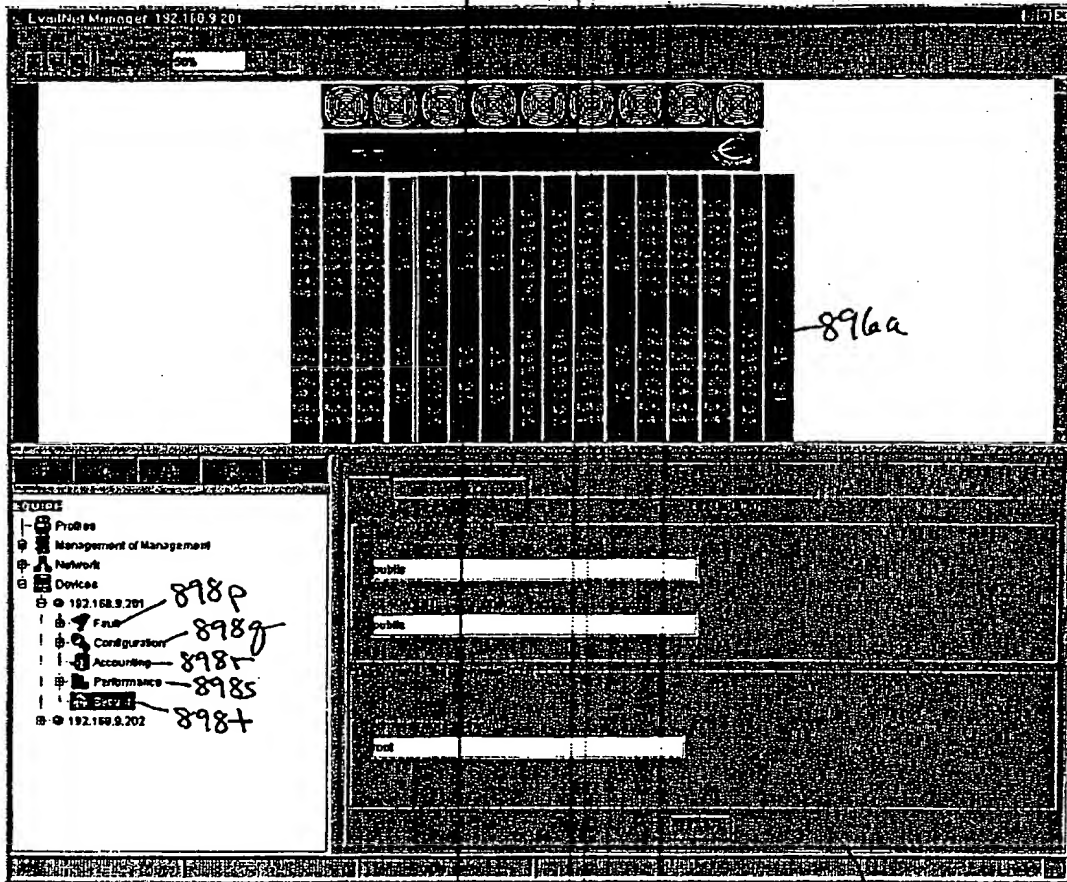




897

Fig. 8c

895



897

Fig. 8d

← 895

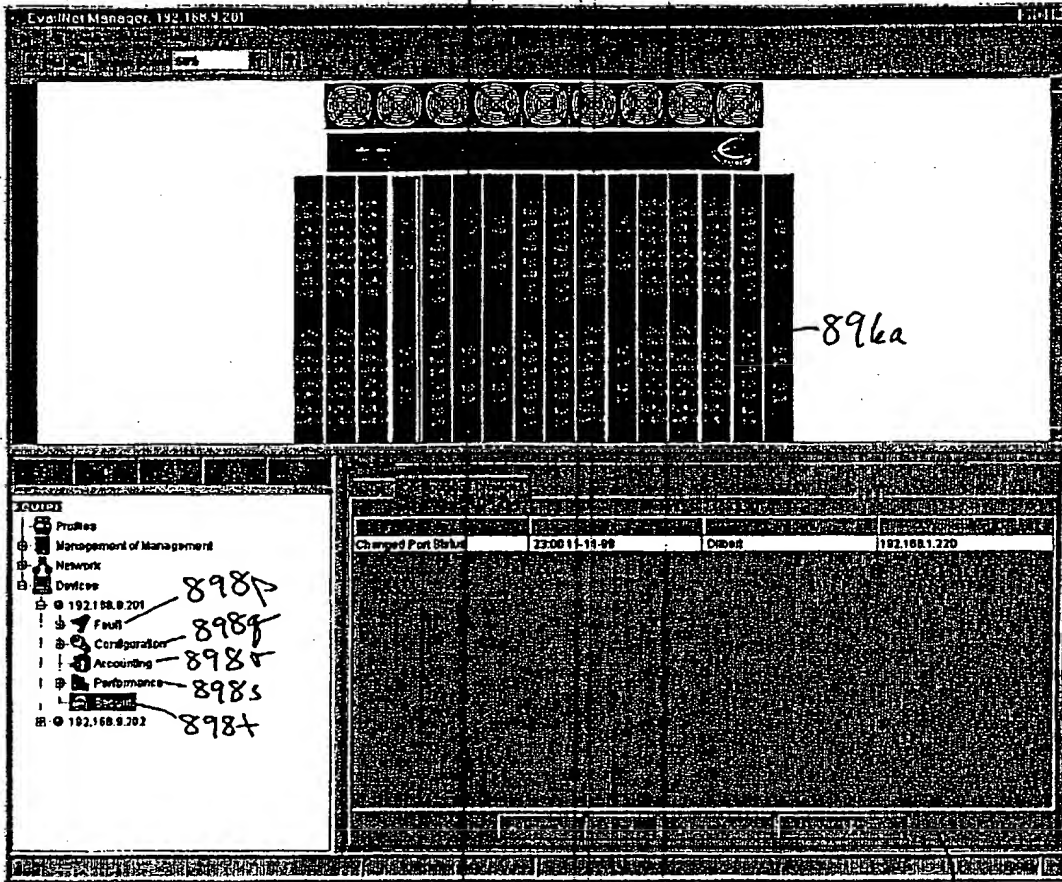


Fig. 8e

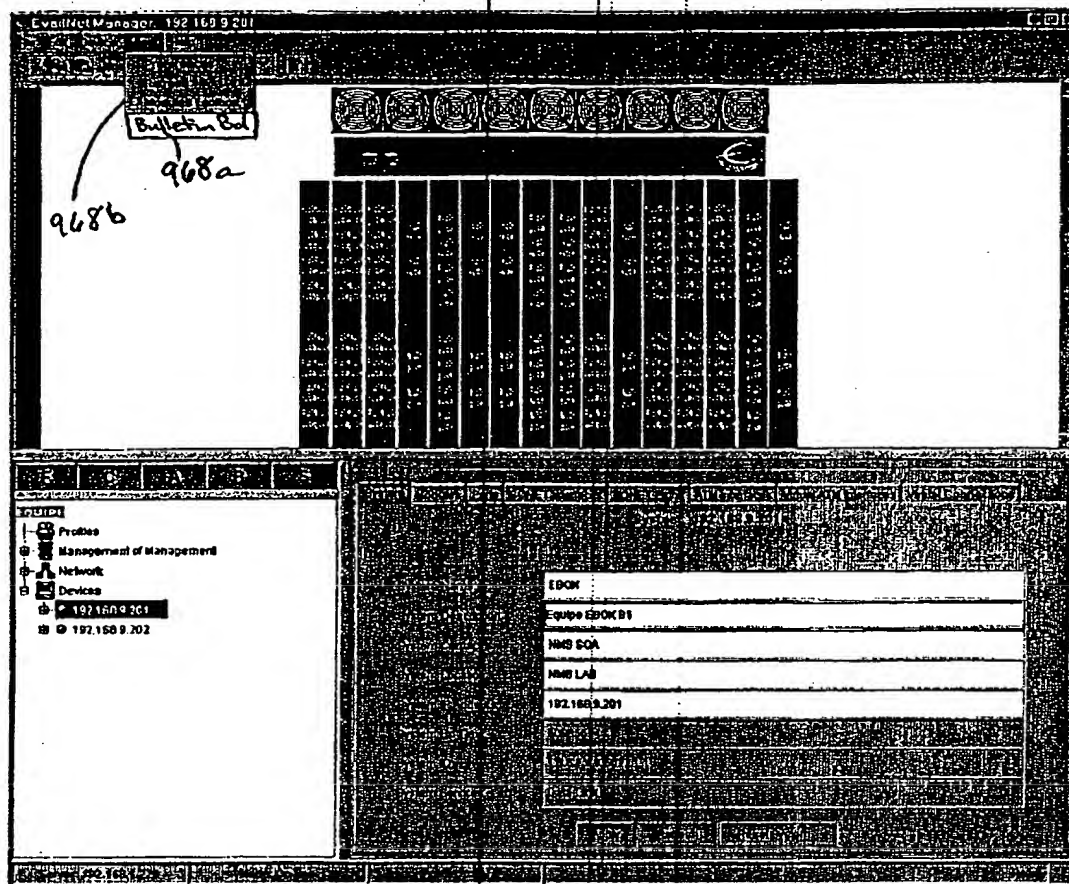


Fig. 9a

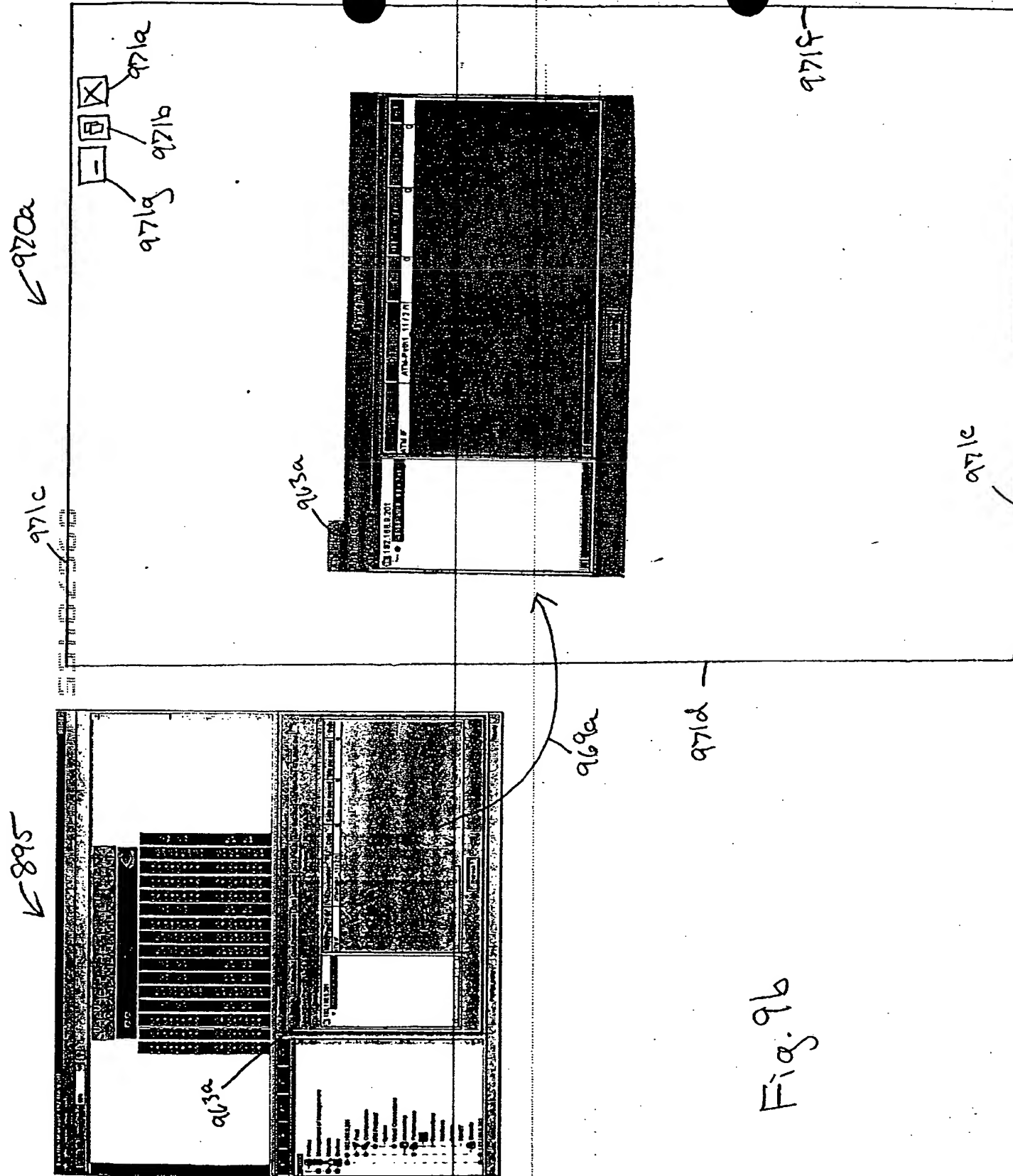
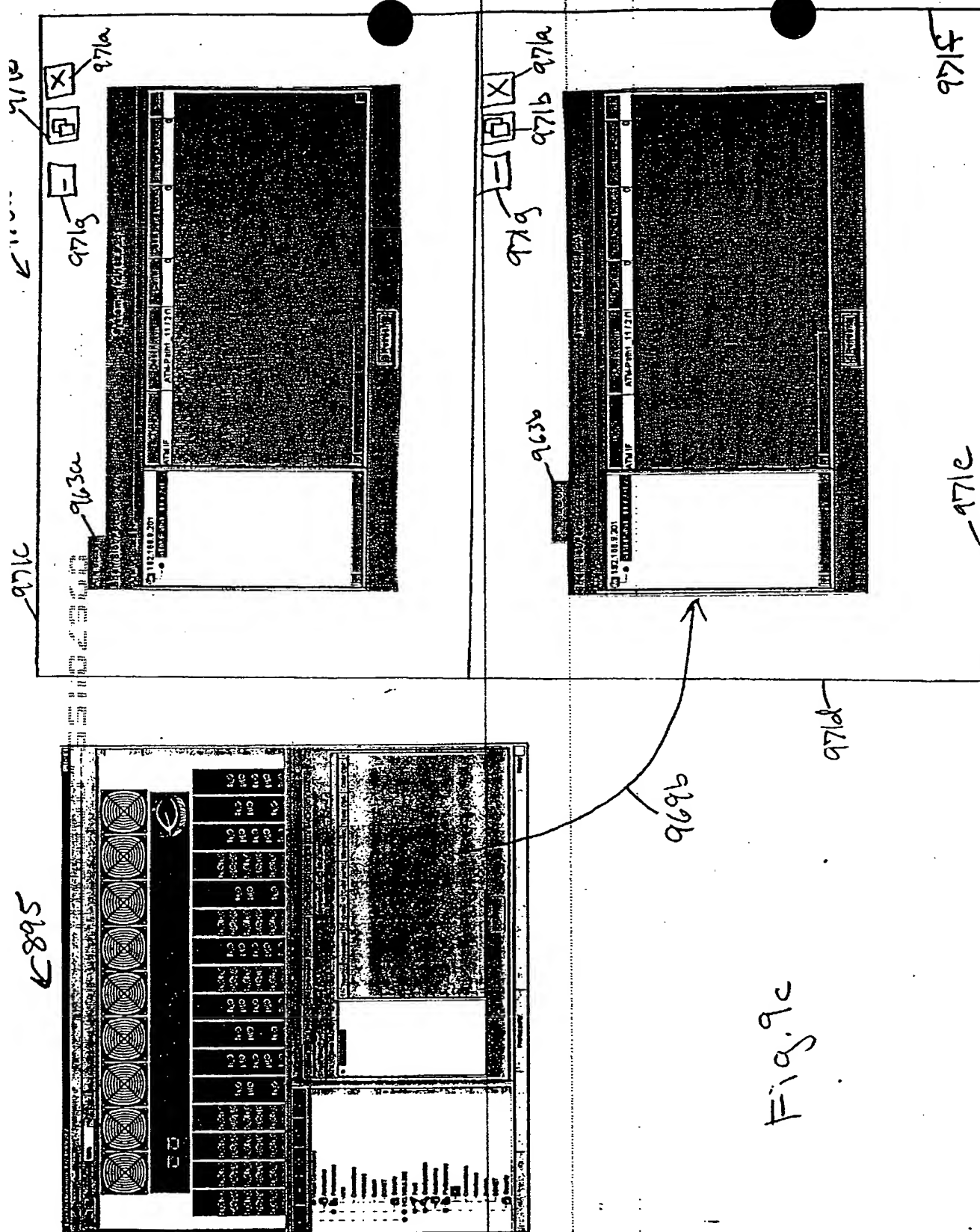
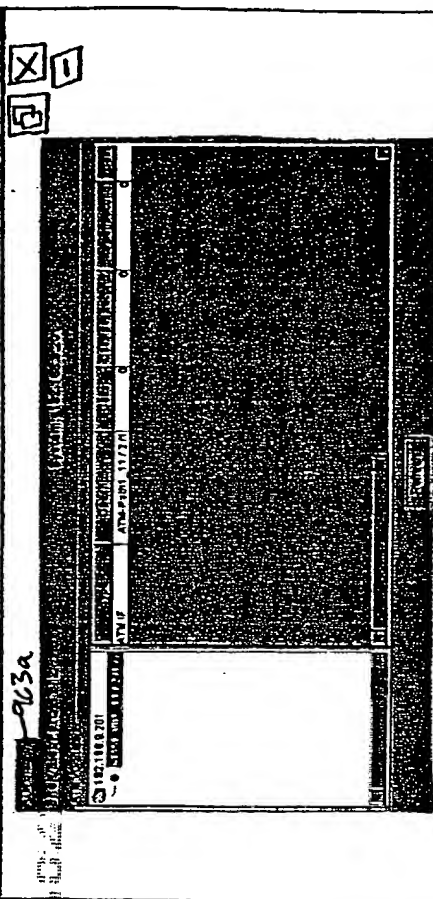


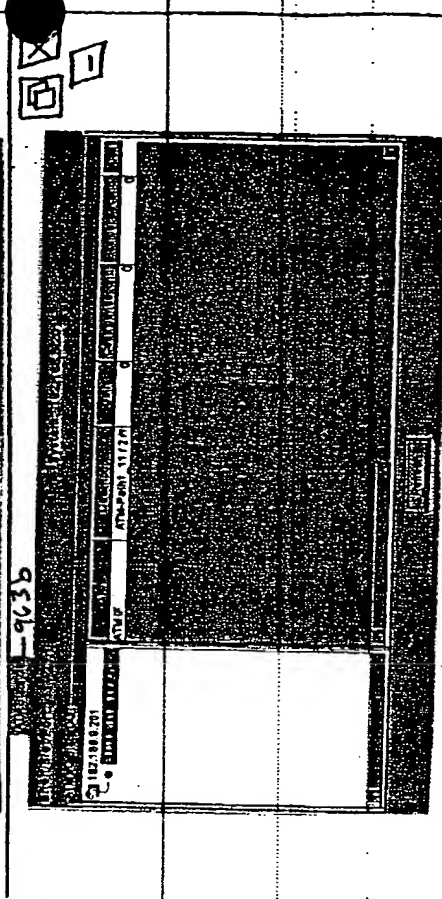
Fig. 9b



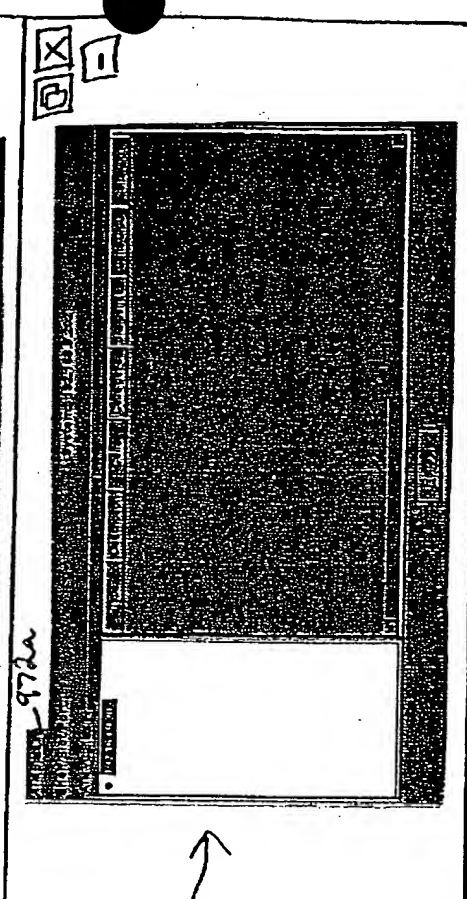
963a



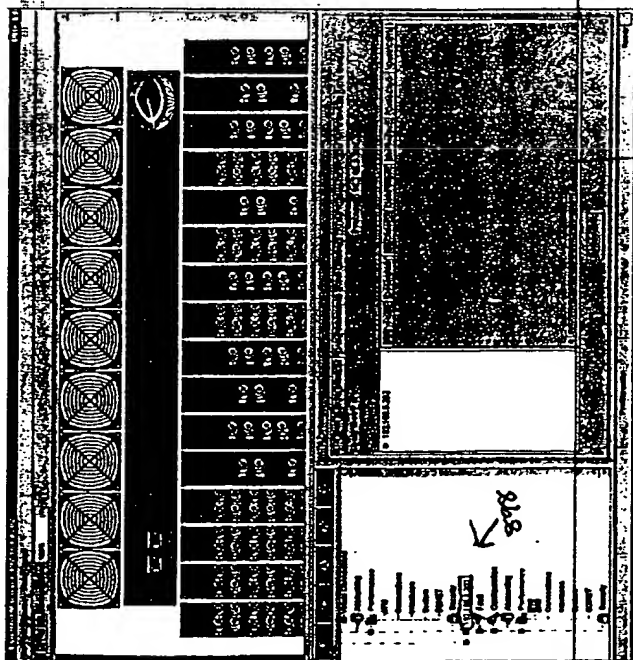
963b



972a



969c

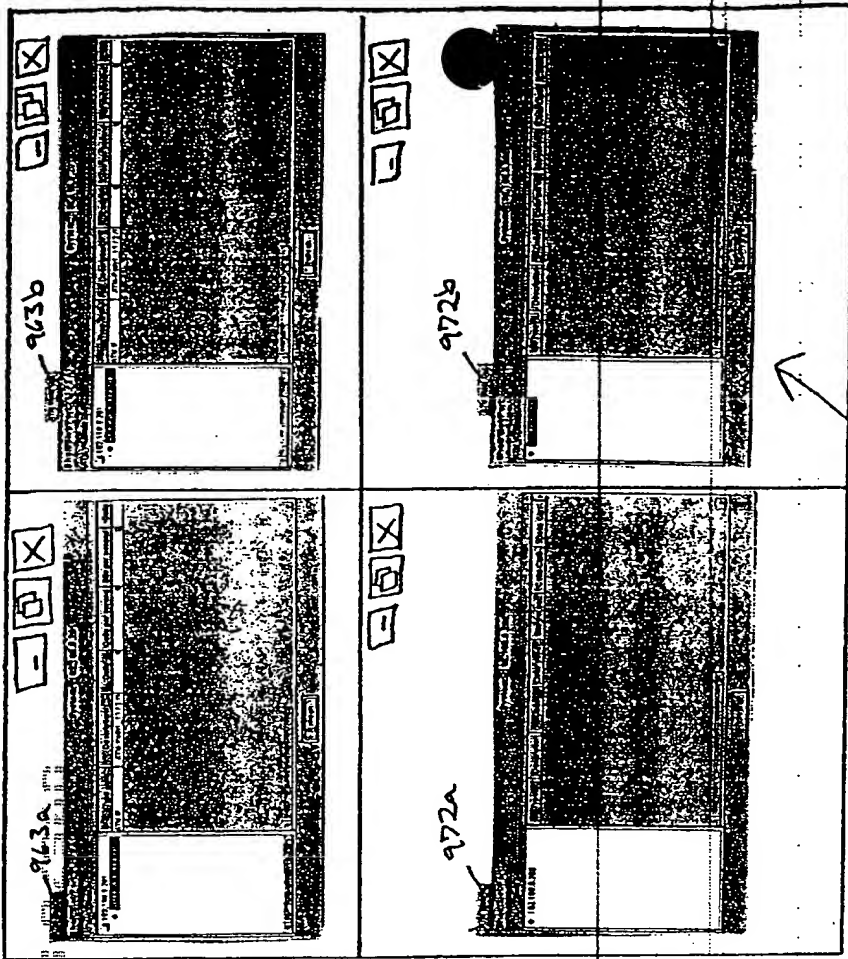


969c

Fig. 9d



← 963a



← 895

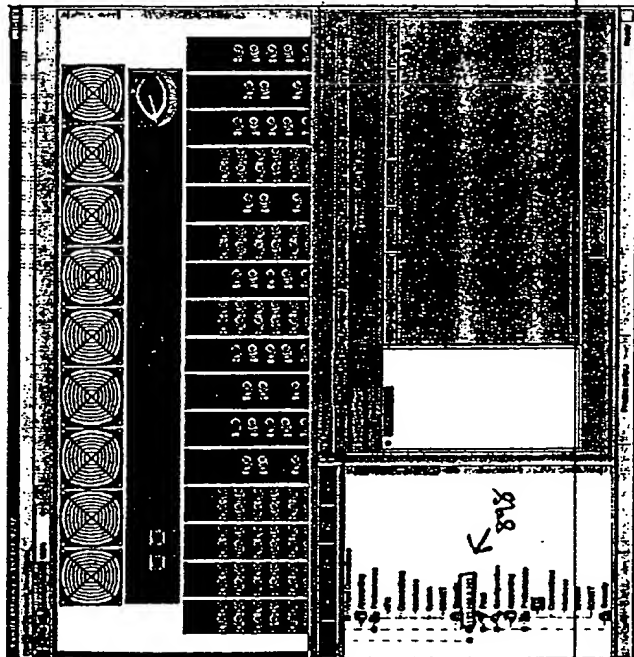


Fig. 9e

969d



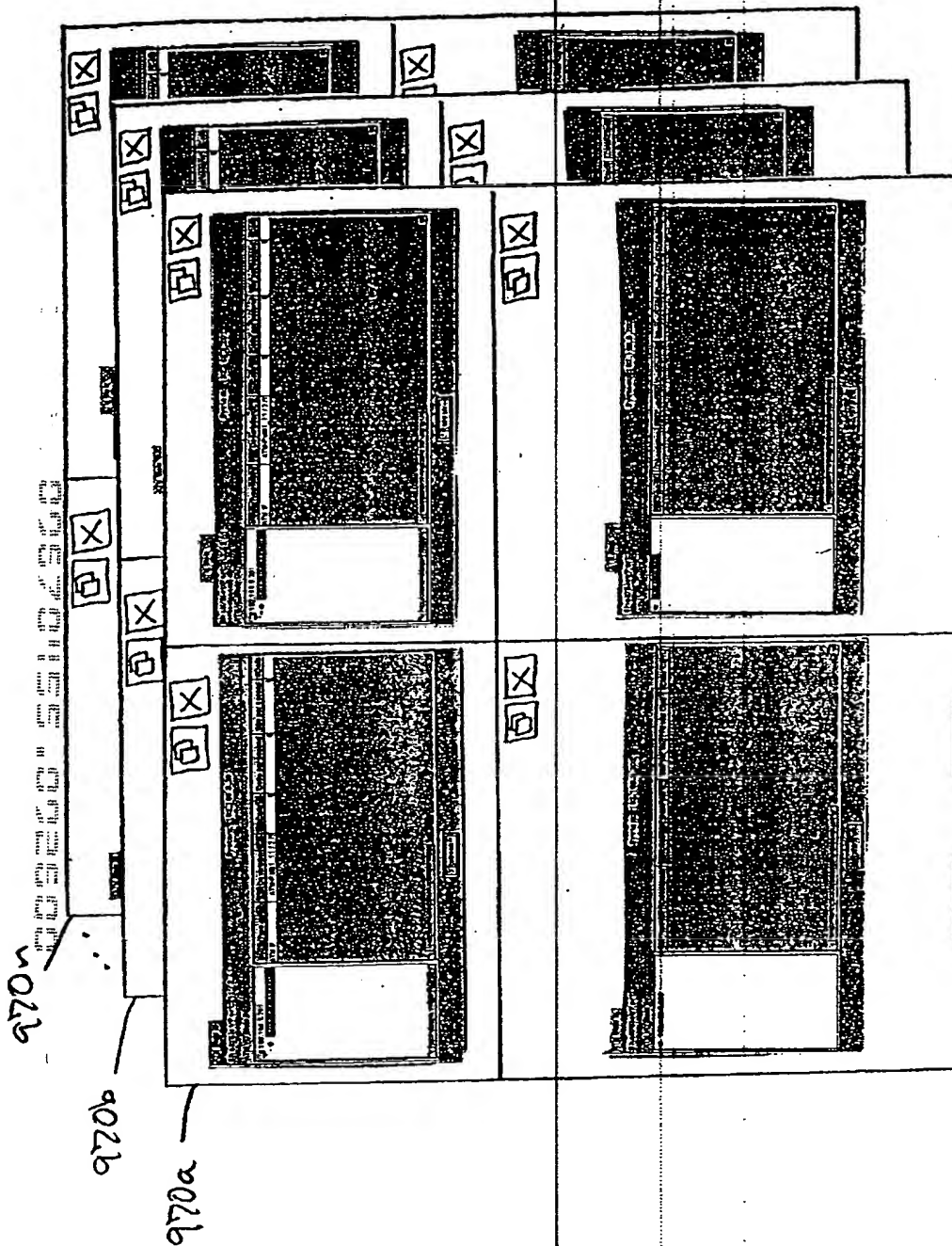
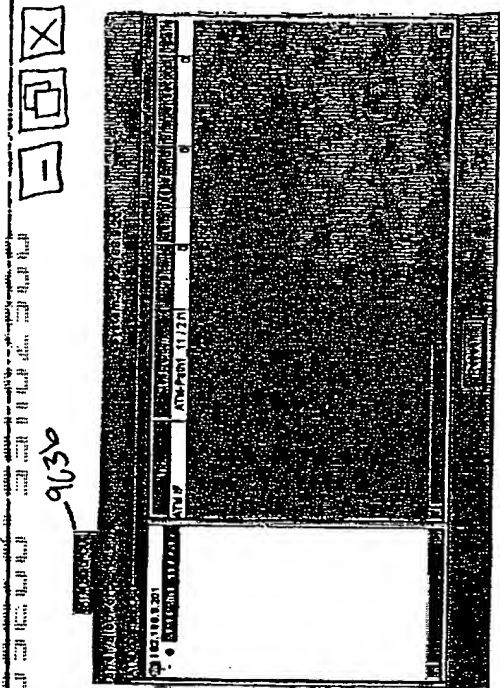


Fig. 9.98

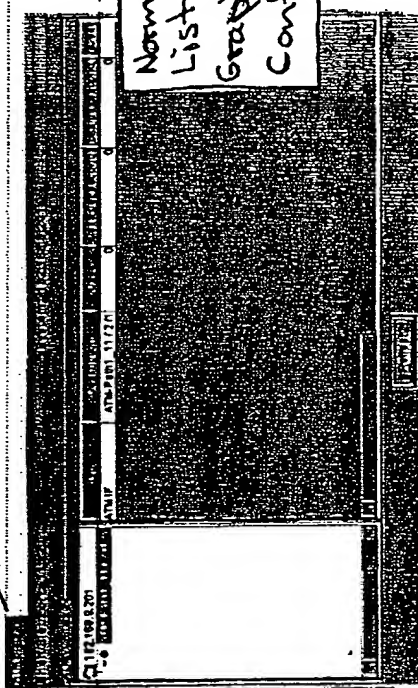
970a



963b



963a

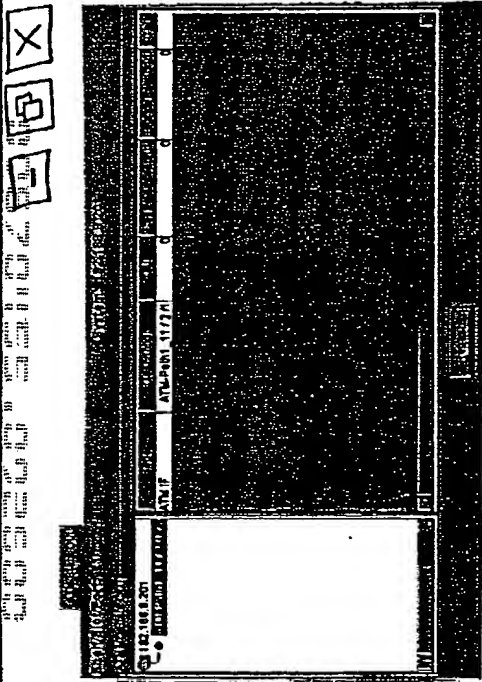


973

Normal — 973a  
List — 973b  
Graph — 973c  
Config — 973d

Fig. 9g

← 970a



System: 192.168.9.201

Type: ATM IF

Description: ATM-Path 1\_11/21

Cells:

Cells Per Second:

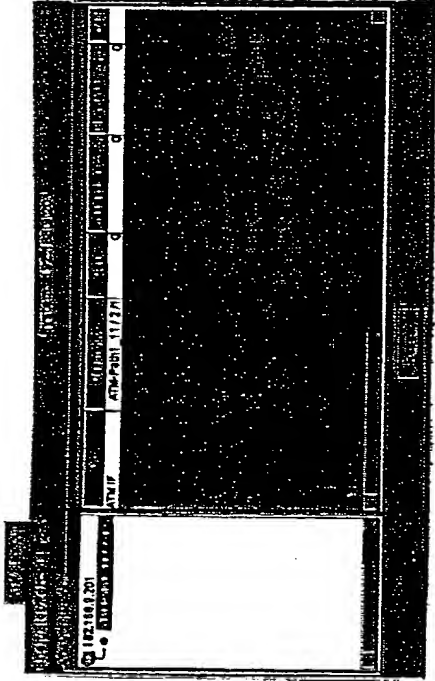
Bits Per Second:

•  
•  
•

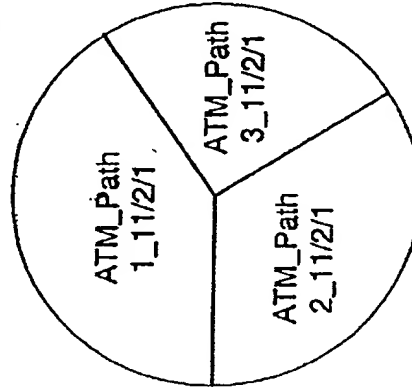
974a

Fig. 91h

↙ 110a



Cells Per Second



← 9746

Fig 9i

✓ 7/10a

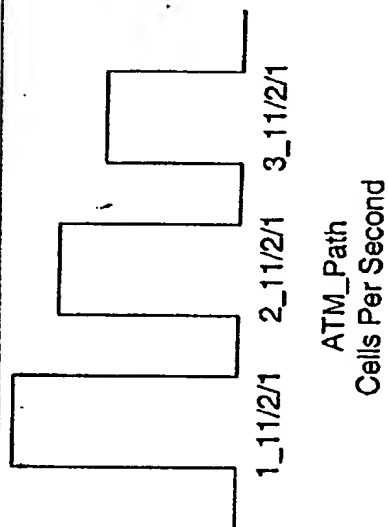
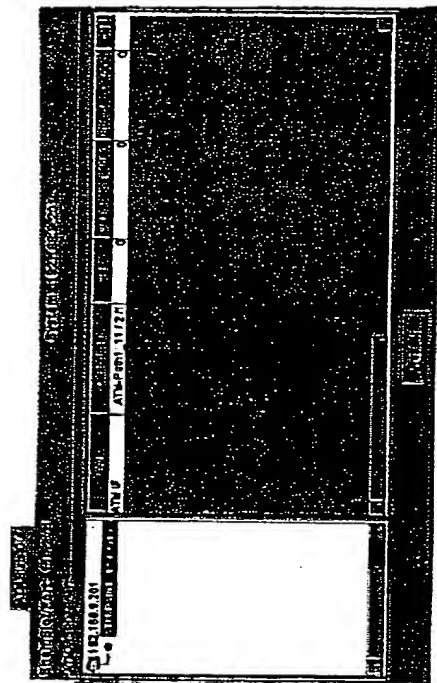
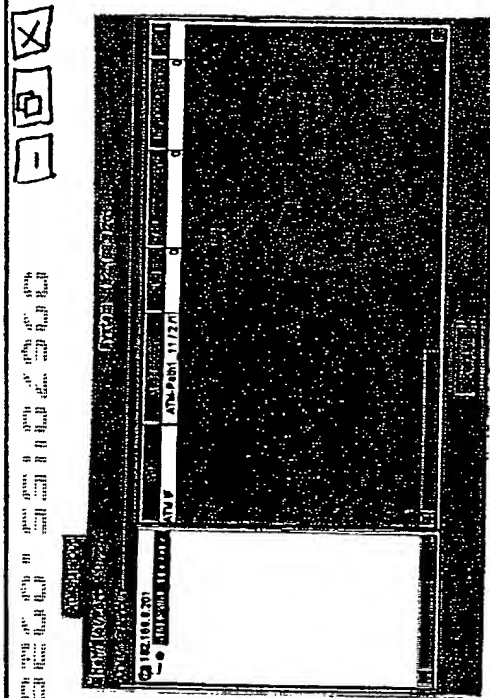
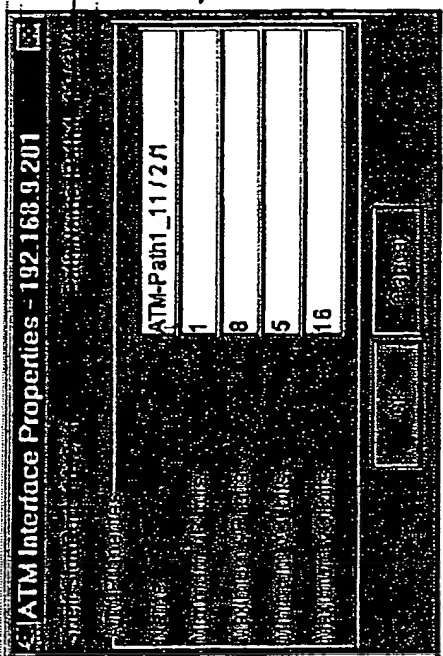


Fig. 9.1j

✓ y.10a

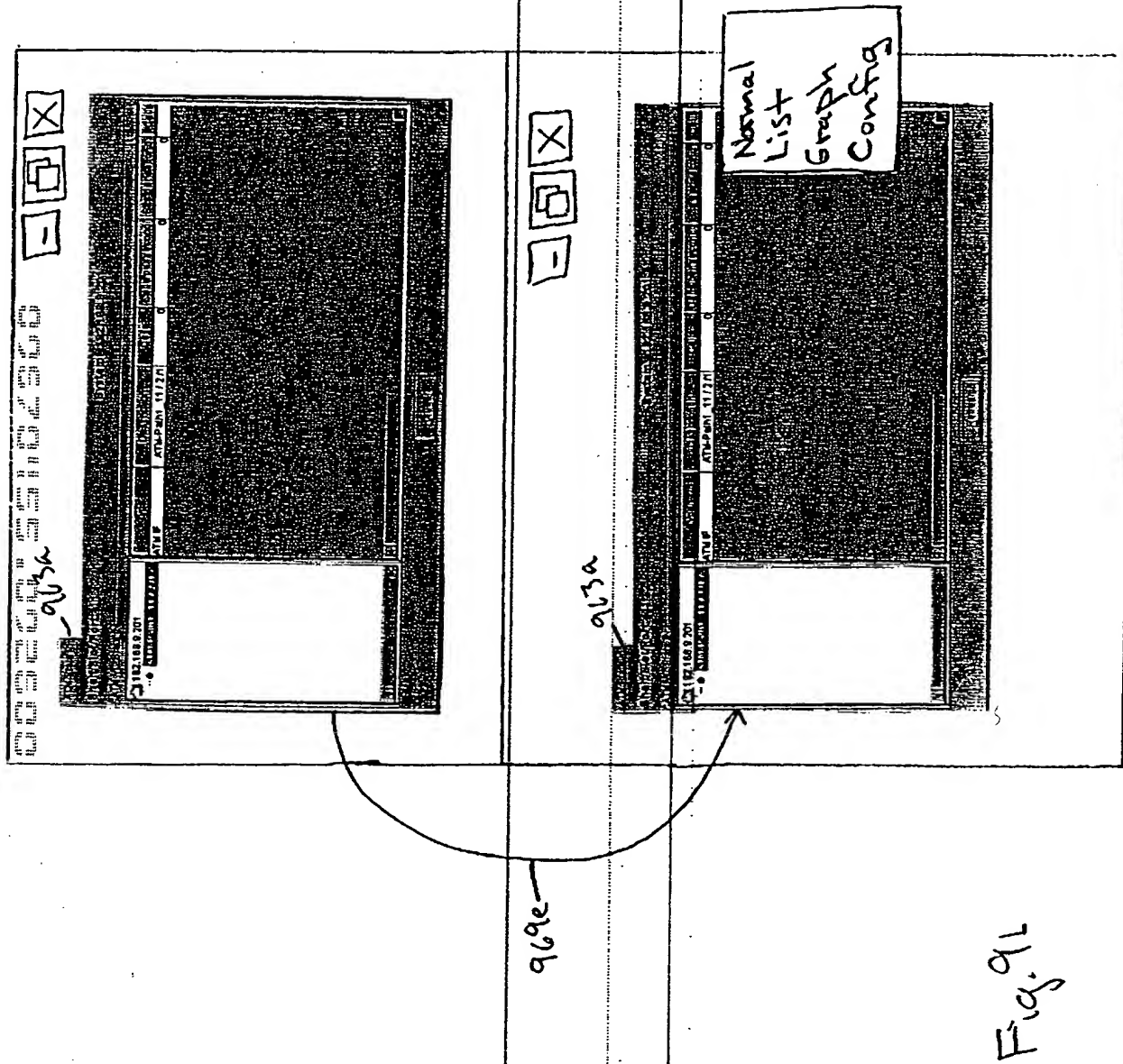


ATM Interface Properties - 192.168.3.201



974d

Fig. 9K



970a

System: 192.168.9.201

896a

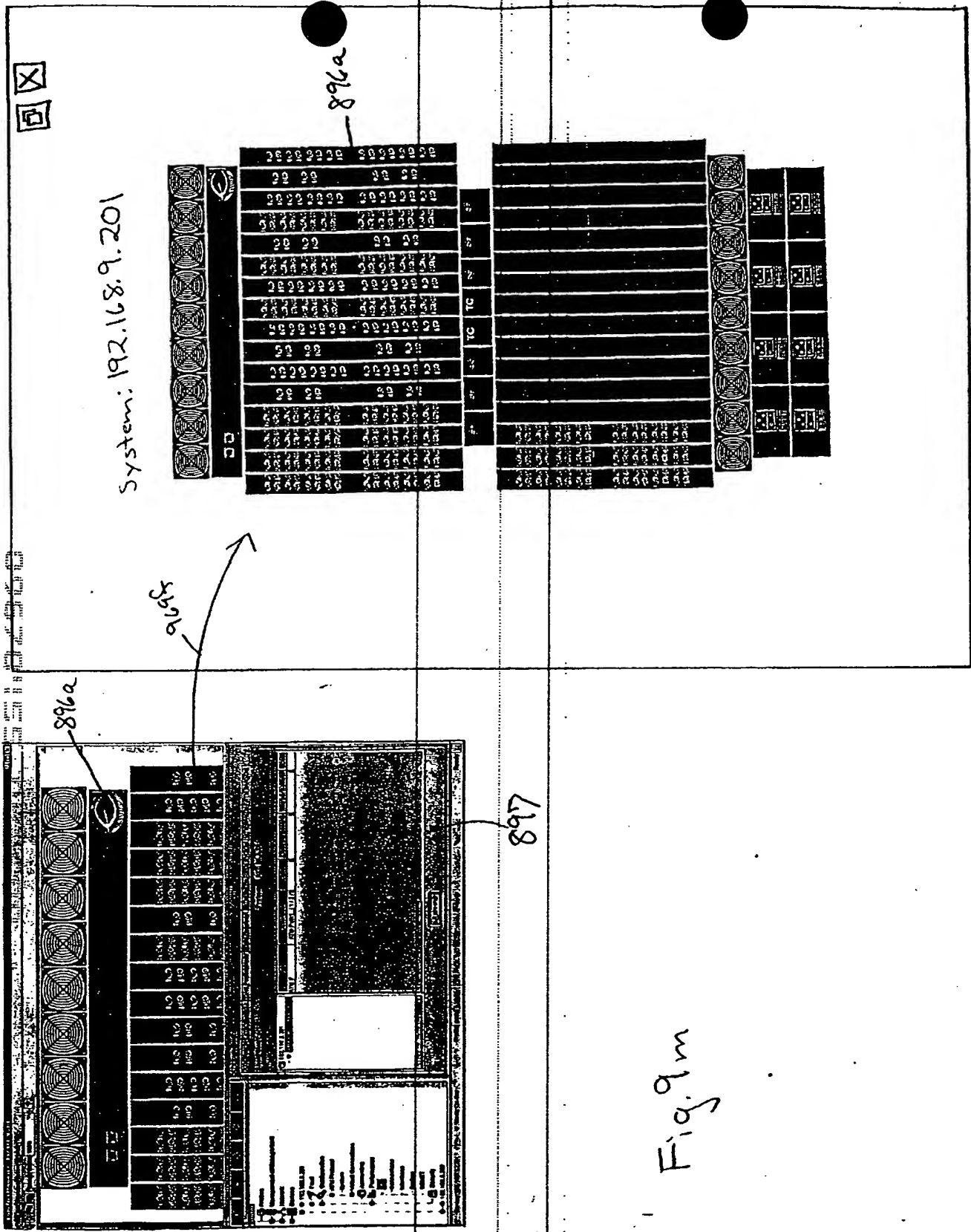
896b

896a

897

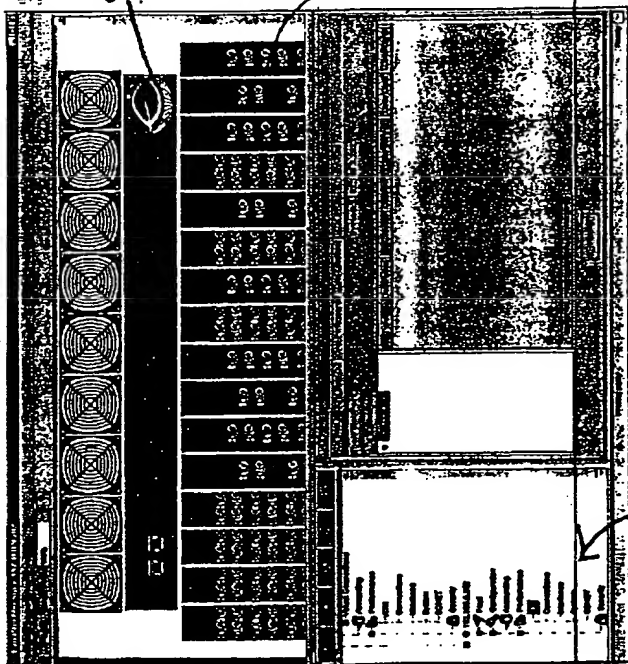
895

Fig. 9m





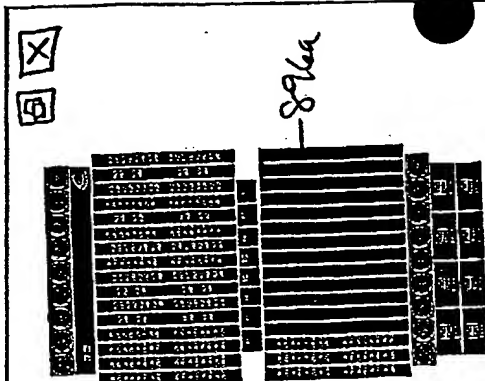
✓ 895



975

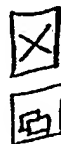
898

✓ 970a

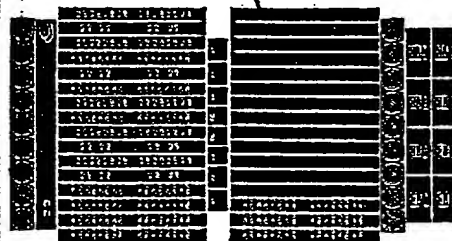


896a

System:  
192.168.9.201



System:  
192.168.9.202



975

Fig. 9n

969g

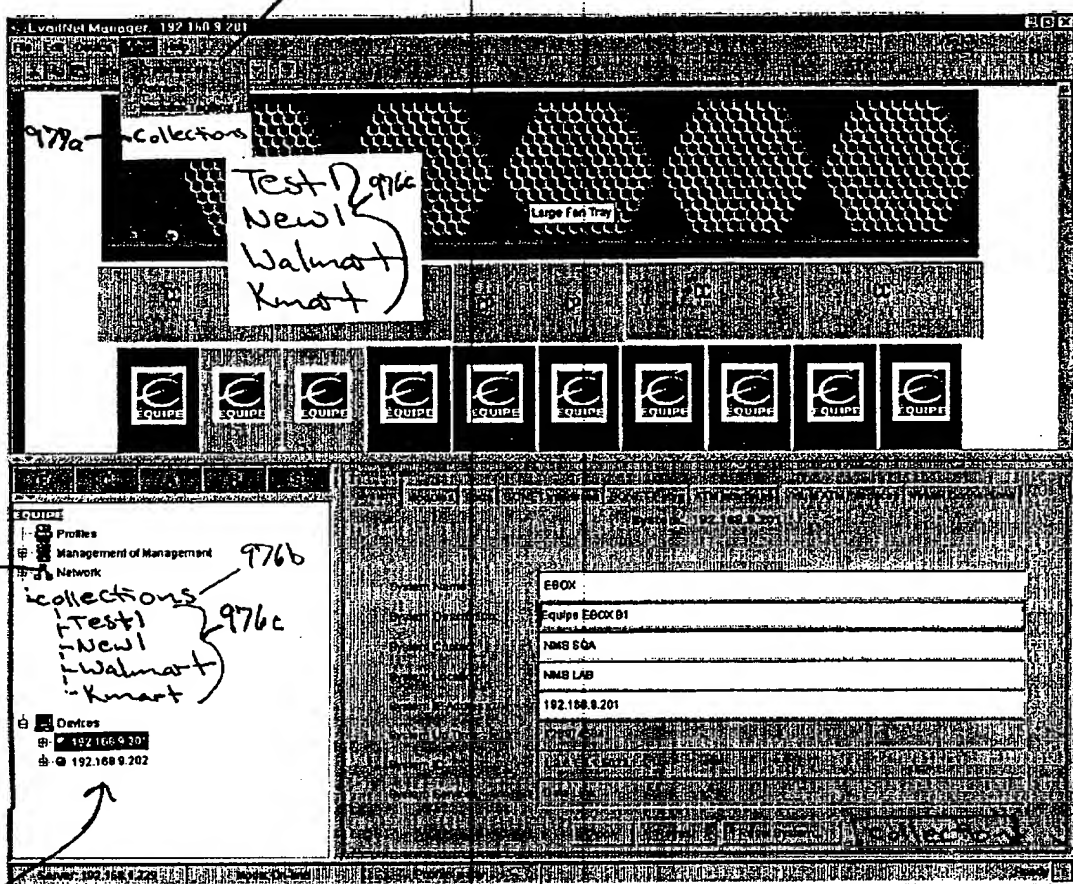


Fig. 10a

895

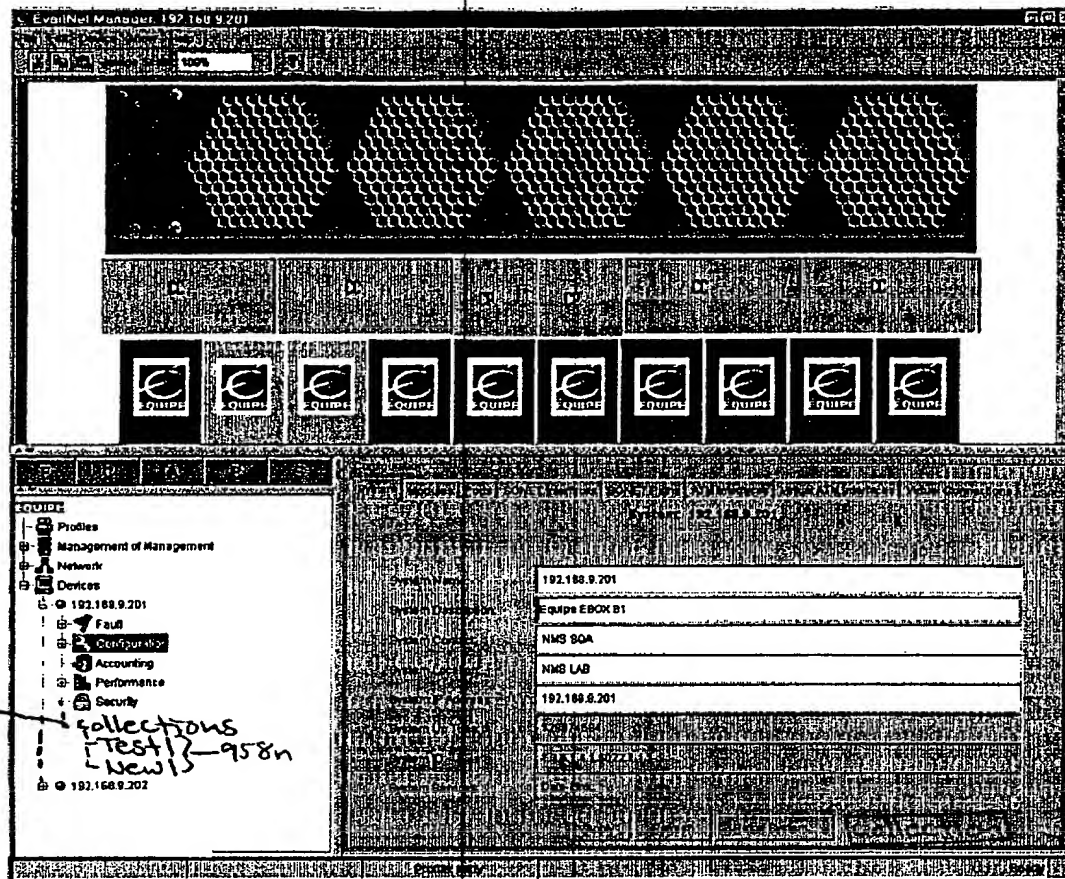


Fig. 10b

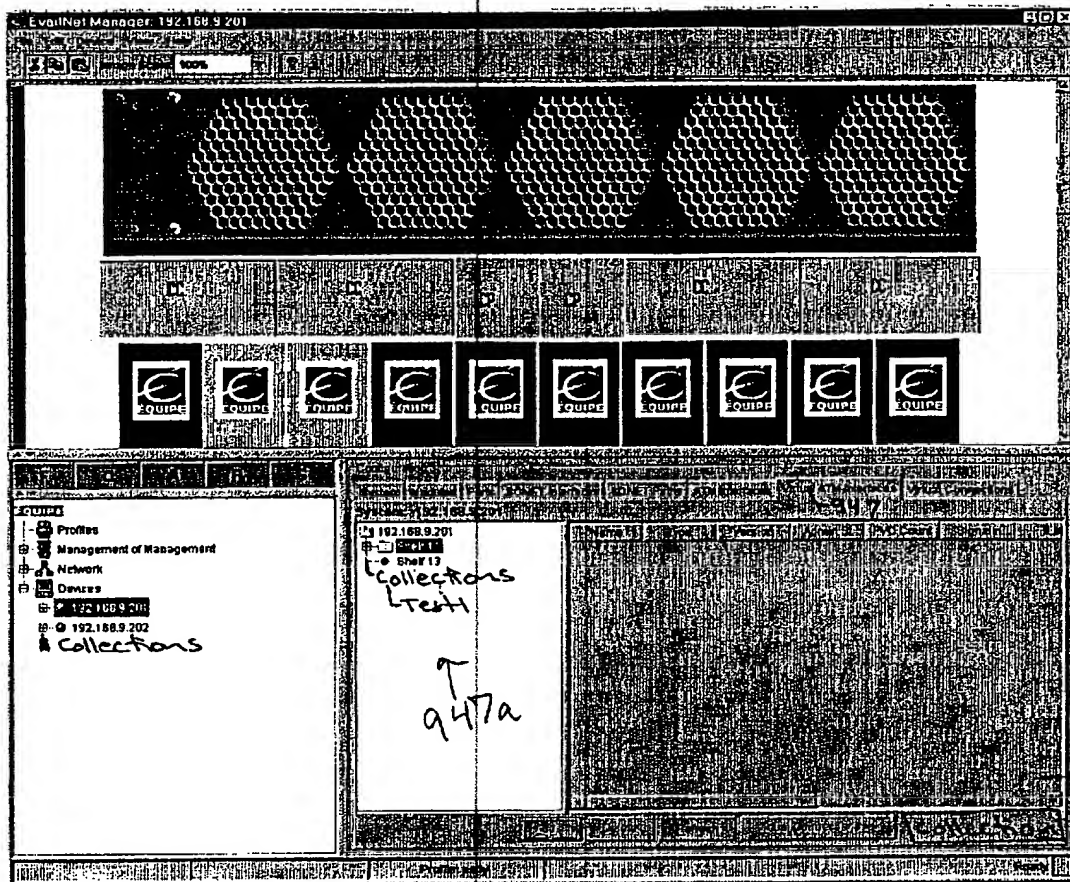
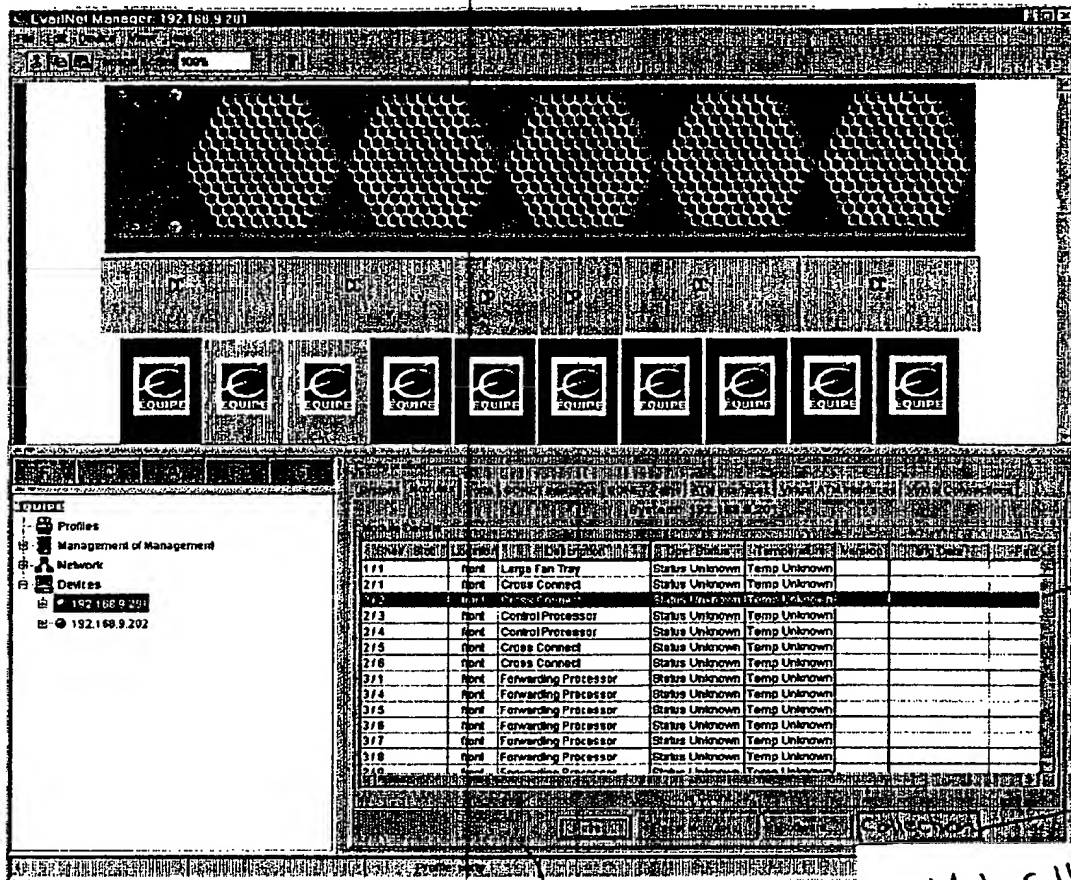


Fig. 10c

895



978a

979a  
979b

Add to Collection  
New Collection

979c

Fig. 10d

897

979d

Collection Name:

OK

Fig. 10e

979e

Existing Collections:

- Test1
- New1
- Walmart
- Kmart

OK

979f

Fig. 10f

979g

895

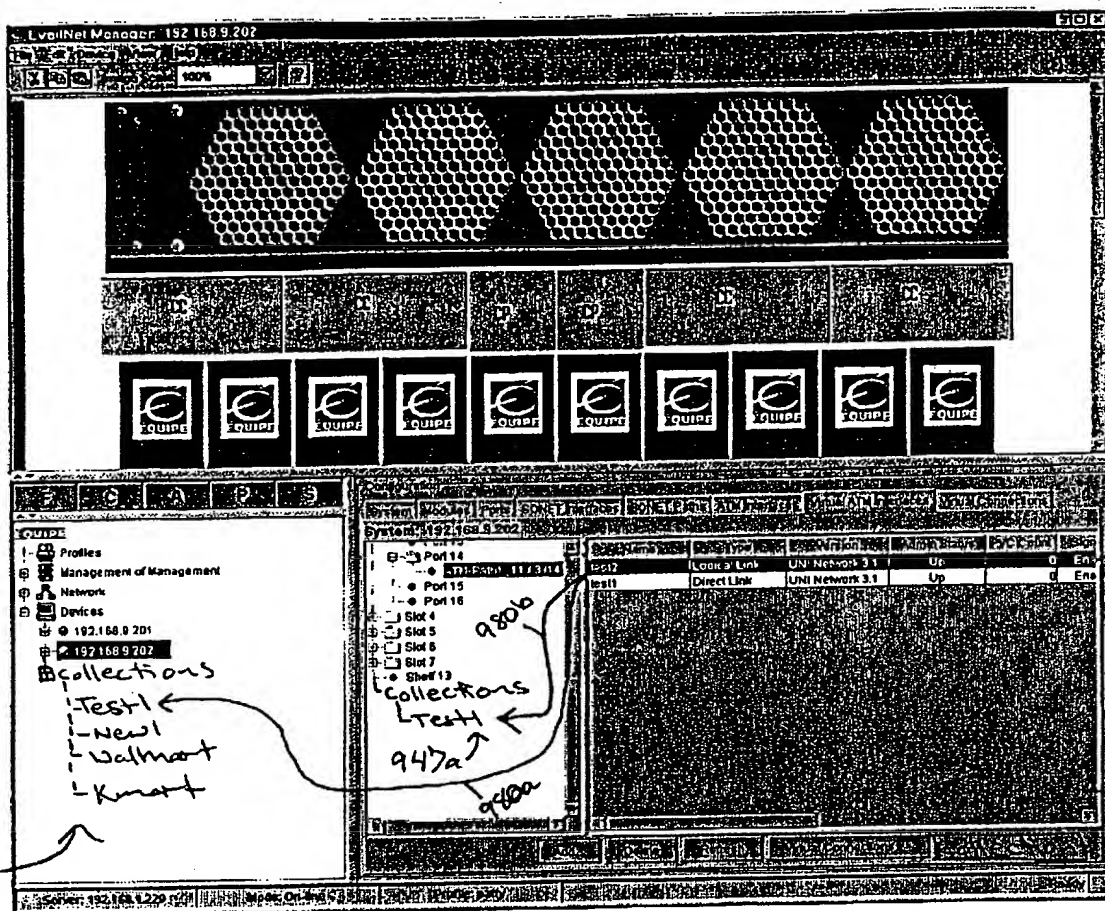


Fig. 109

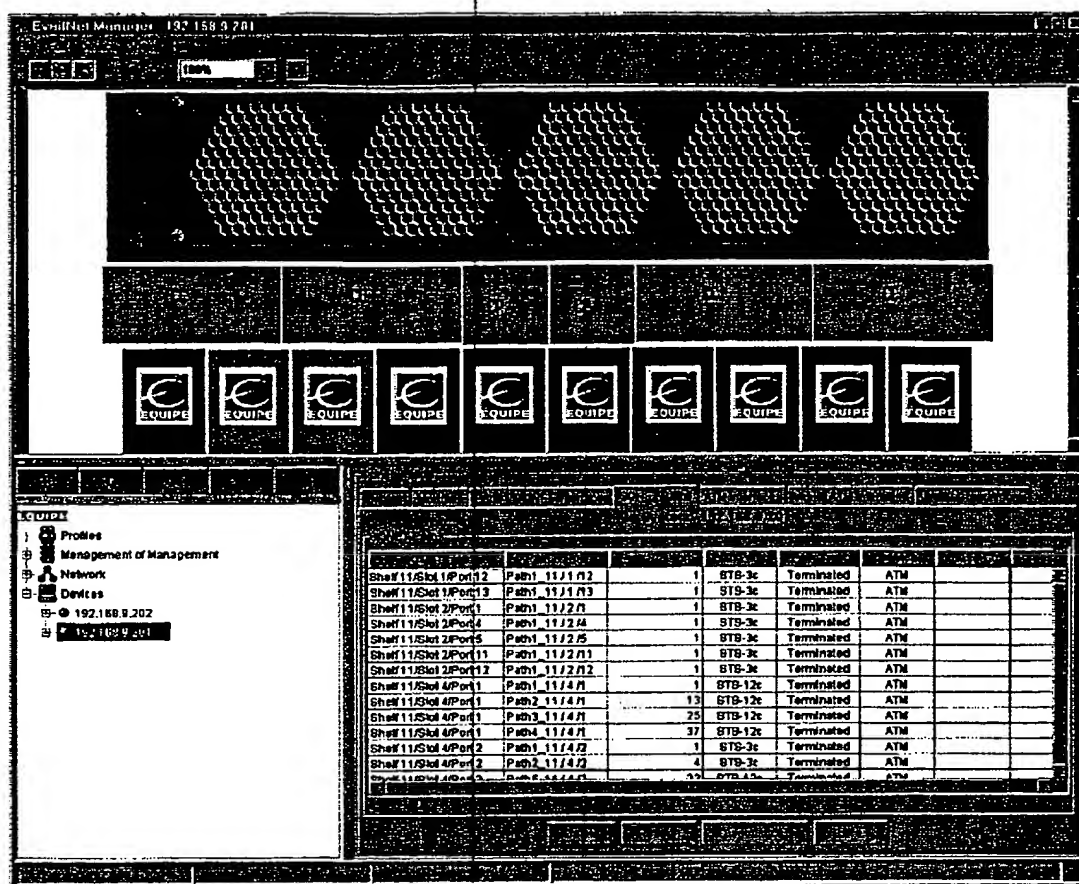


Fig. 10h



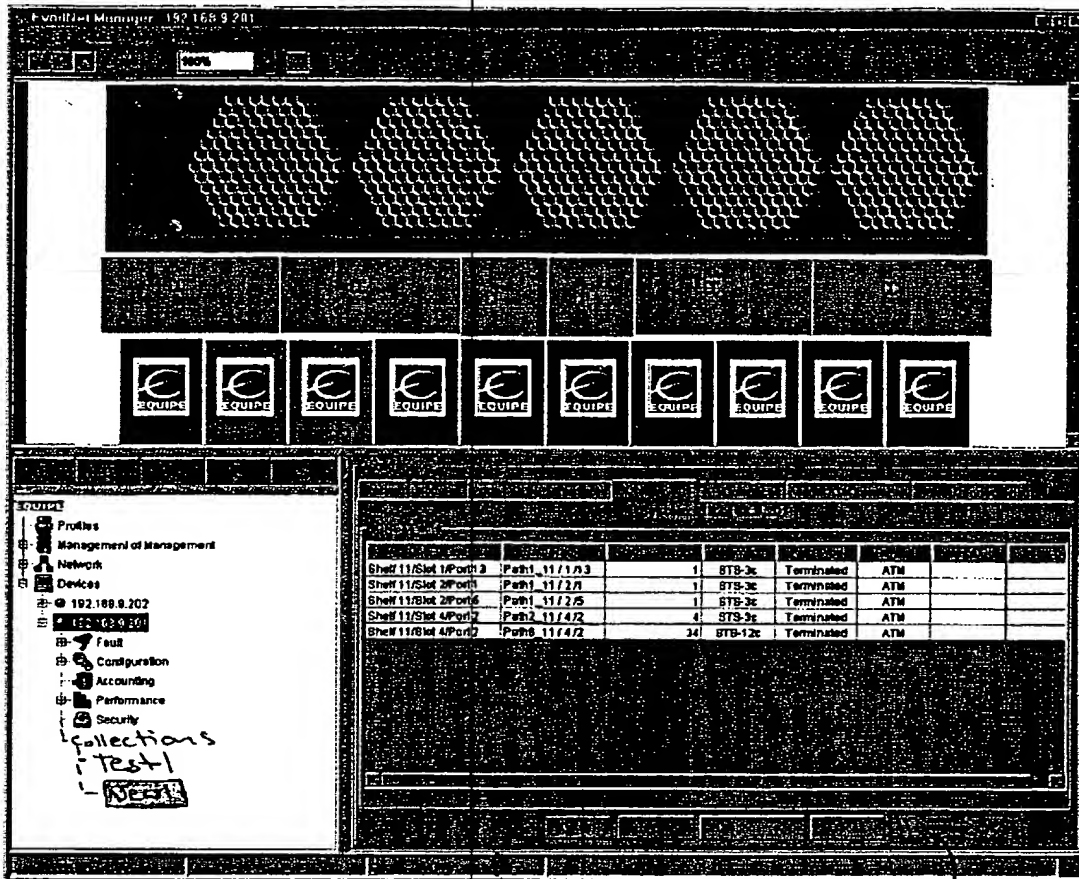


Fig. 10i

982



904

Name	Description	Security Level	Time out	Primary Server	Secondary Server
Joe	Joe Whitehouse	Admin	15	192.168.1.32	192.168.1.37
Wayne	Wayne Arena	Provisioner	15	TeamServer1:192.168...	TeamServer2:192.168.1.32

905

906

2587

Fig. 11a

107

**General**

Username: Kevin 908a

Description: Kevin Snow user account 908e

Group Name: Equipe 908f

Group Level Access: 908d

Password: \*\*\*\*\* 908b

Confirm Password: \*\*\*\*\* 908c

**Policies**

☒ User Cannot Change Password 908h

☐ Account Disabled 908i

☒ User Can Add Devices 908j

User Session Timeout: 15 Minutes 908k

**Servers**

Primary Server: 192.168.1.220 908l

Primary Server Port: 6500 908n

Secondary Server: 192.168.1.221 908m

Secondary Server Port: 6503 908o

**Devices**

IP Device	READ	READWRITE	Relay	Timeout
192.168.9.202	public	equipe	3	5
192.168.9.205	public	equipe	3	5
192.168.9.216	public	equipe	3	5

908g

908p 908q 908r 908s 908t

OK Cancel

Fig. 11b

Fig. C

General Policies Servers Devices

Username: Kevin

Description: Kevin Snow user account

Customer Name: Equipe

Group Level Access:

Password: \*\*\*\*\*

Confirm Password: \*\*\*\*\*

OK Cancel

General Policies Servers Devices

☒ User Cannot Change Password

☒ Account Disabled

☒ User Can Add Devices

User Session Timeout: 15 Minutes

OK Cancel

Fig. 11d

Fig. 11e

General Policies Servers Devices

Primary Server: 192.168.1.220

Primary Server Port: 6500

Secondary Server: 192.168.1.205

Secondary Server Port: 6503

OK Cancel

General Policies Servers Devices

Device	READ	READWRITE	Retry	Timeout	MaxConnections
192.168.9.202	public	equipe	3	5	162
192.168.9.205	public	equipe	3	5	162
192.168.9.216	public	equipe	3	5	5012

Add Delete

OK Cancel

Fig. 11f

904

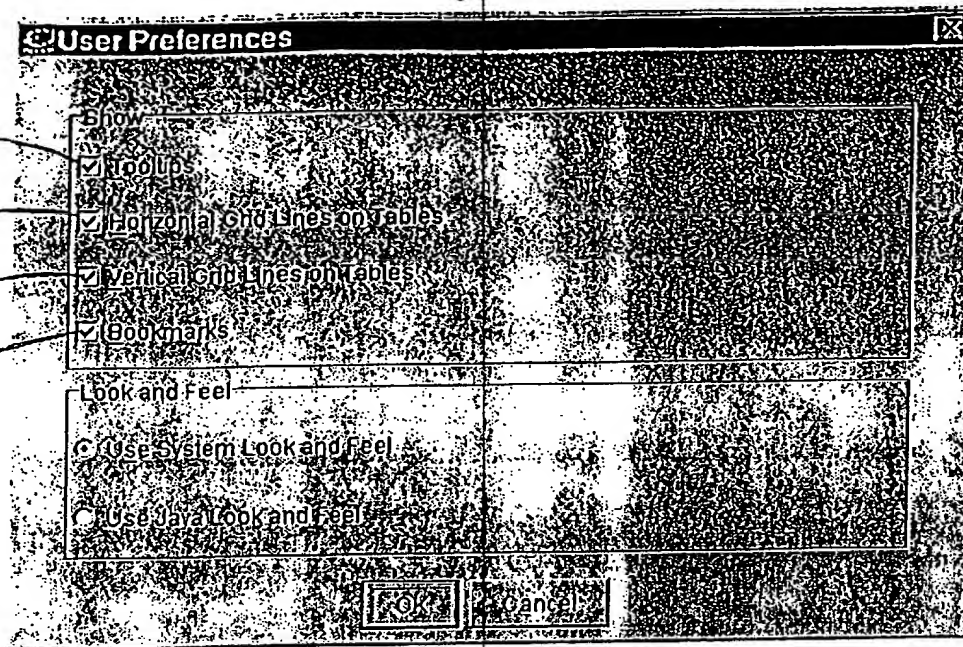
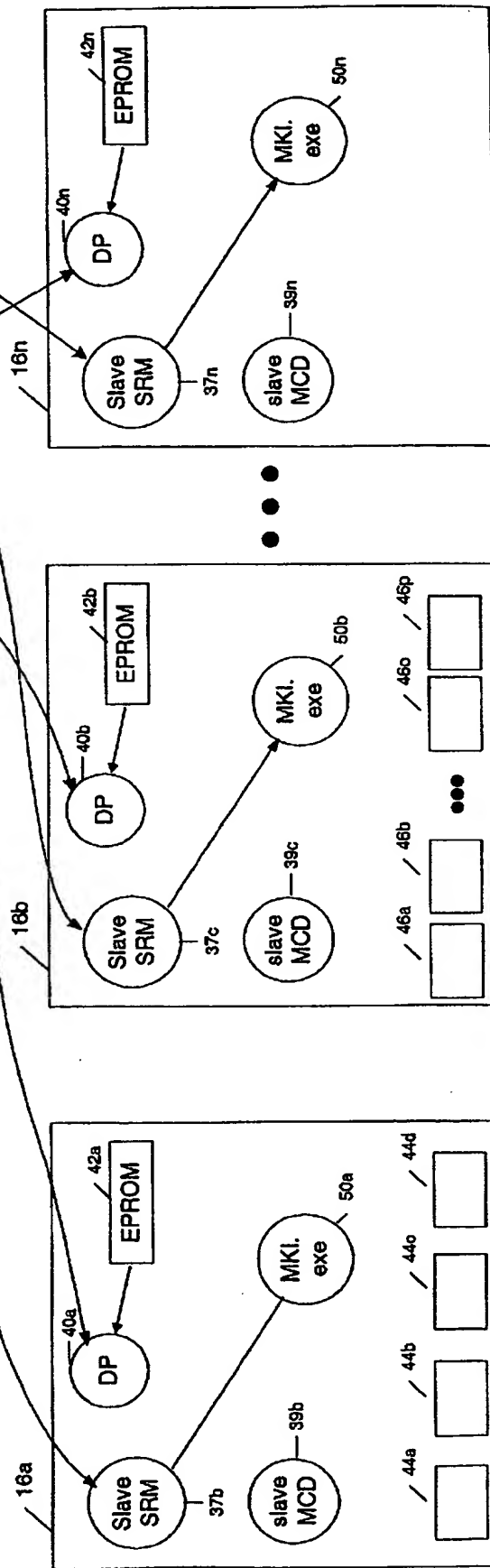
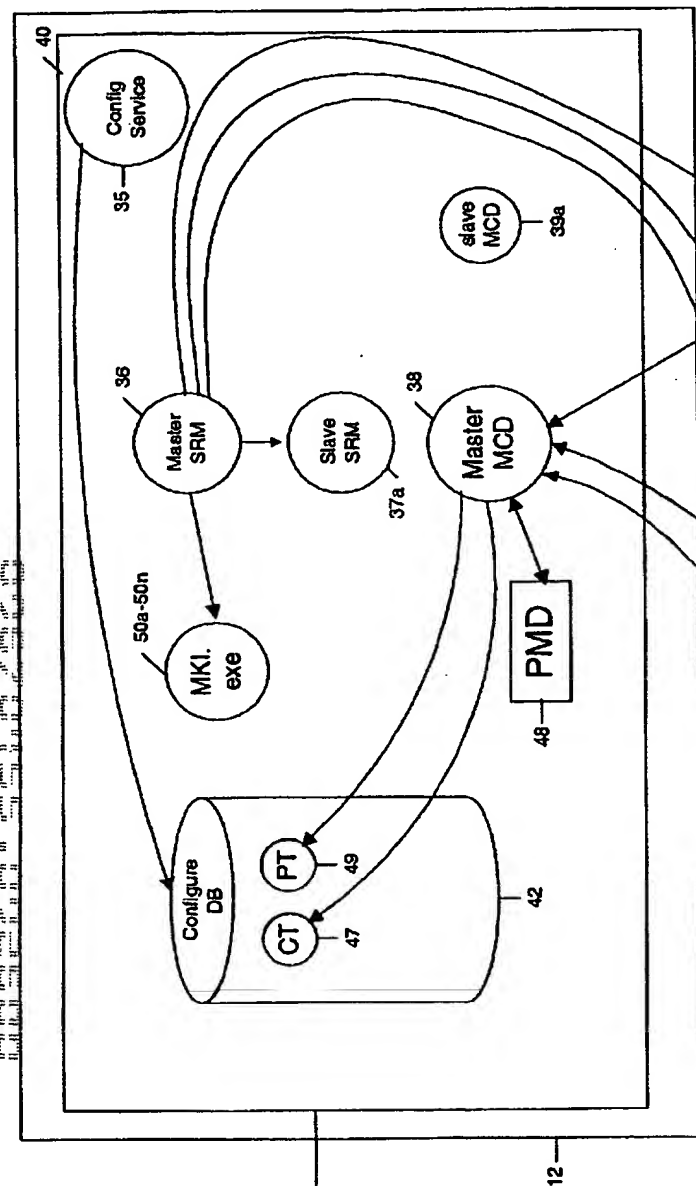
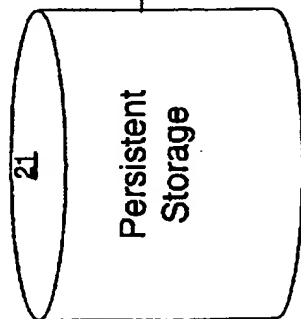


Fig. 11g

FIG. 12a

10



# CARD TABLE

47

PID	CWD TYPE	VERSION NO.	SLOT NO.	...
16 a — 500	0XF002	3	1	
16 b — 501	0XF002	4	2	
• • •	• • •	• • •	• • •	• • •
16 e — 505	0X6002	1	5	
• • •	• • •	• • •	• • •	• • •
16 n — 513	0XF002	1	12	
• • •	• • •	• • •	• • •	• • •

FIG. 12b

PORT TABLE

49

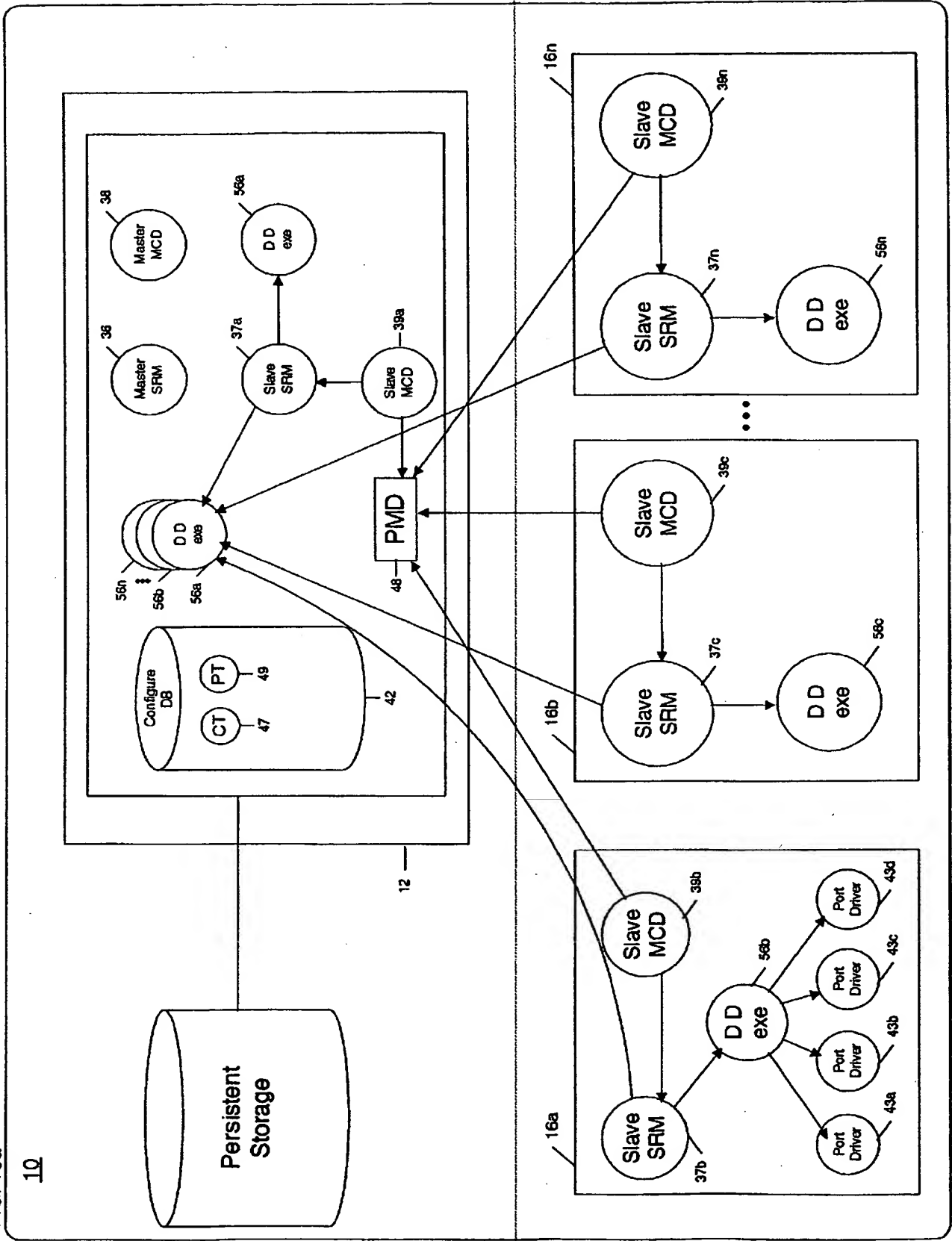
PID	PORT TYPE	VERSION NO.	SLOT NO.	...
44a — 1500	00620	1	1	
44b — 1501	00620	1	1	
44c — 1502	00620	1	1	
44d — 1503	00620	1	1	
44a — 1504	00820			
46a — :	:	:	:	:
1600	00620	1	8	
:	:	:	:	:

FIG. 12c



FIG. 13a

10



10

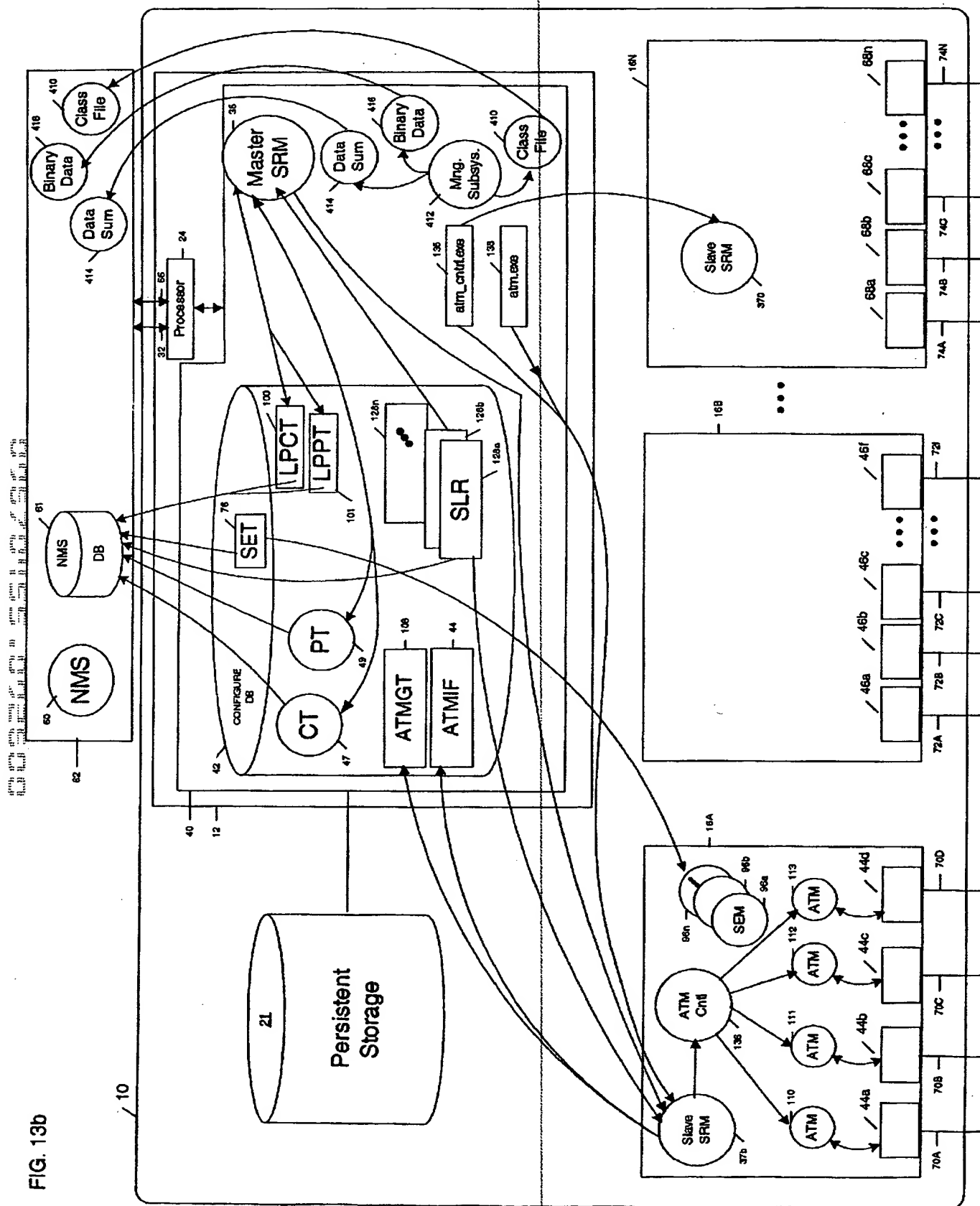


Fig. 13c

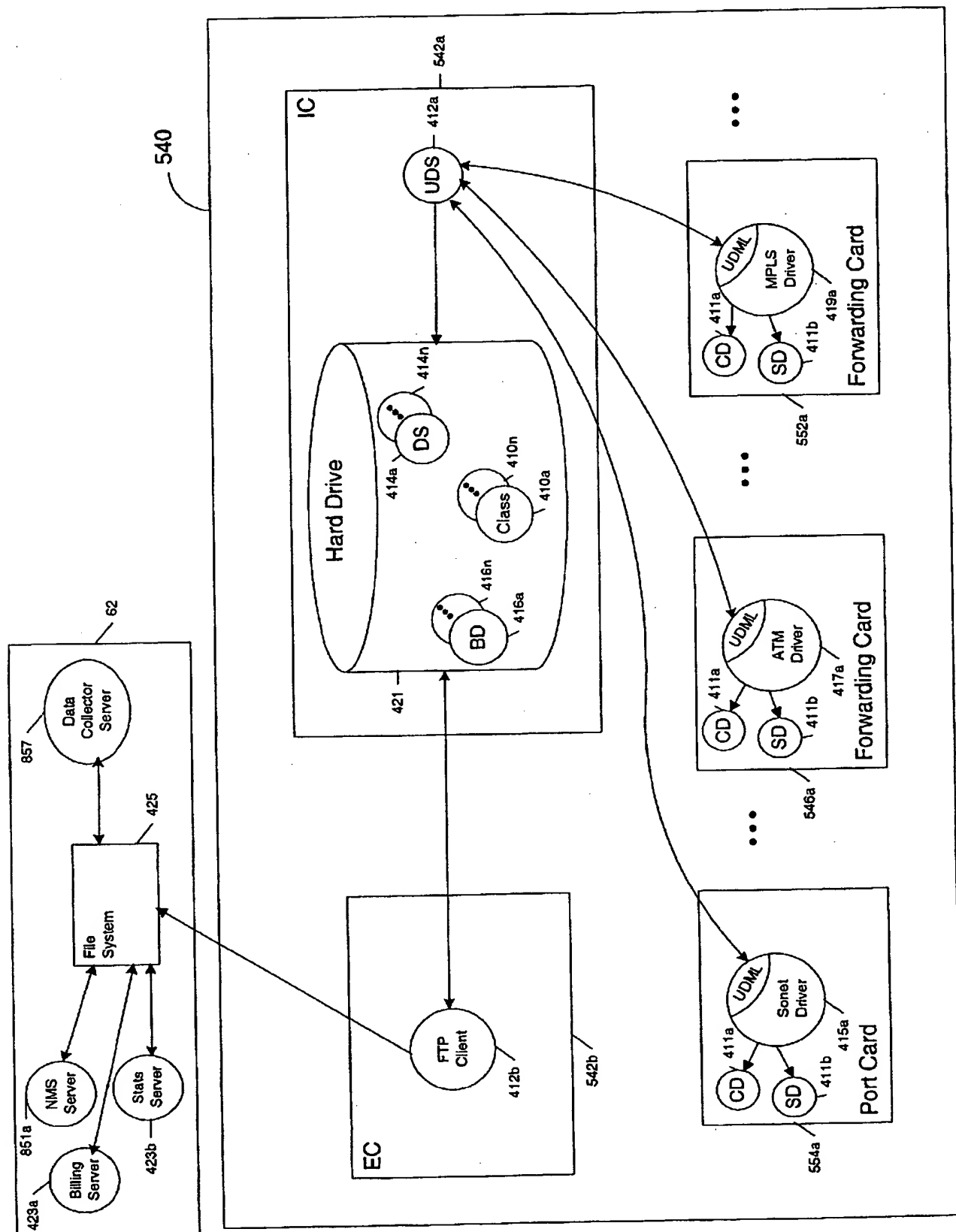


FIG. 13d is a block diagram of a system architecture for a network management system.

Fig. 13d

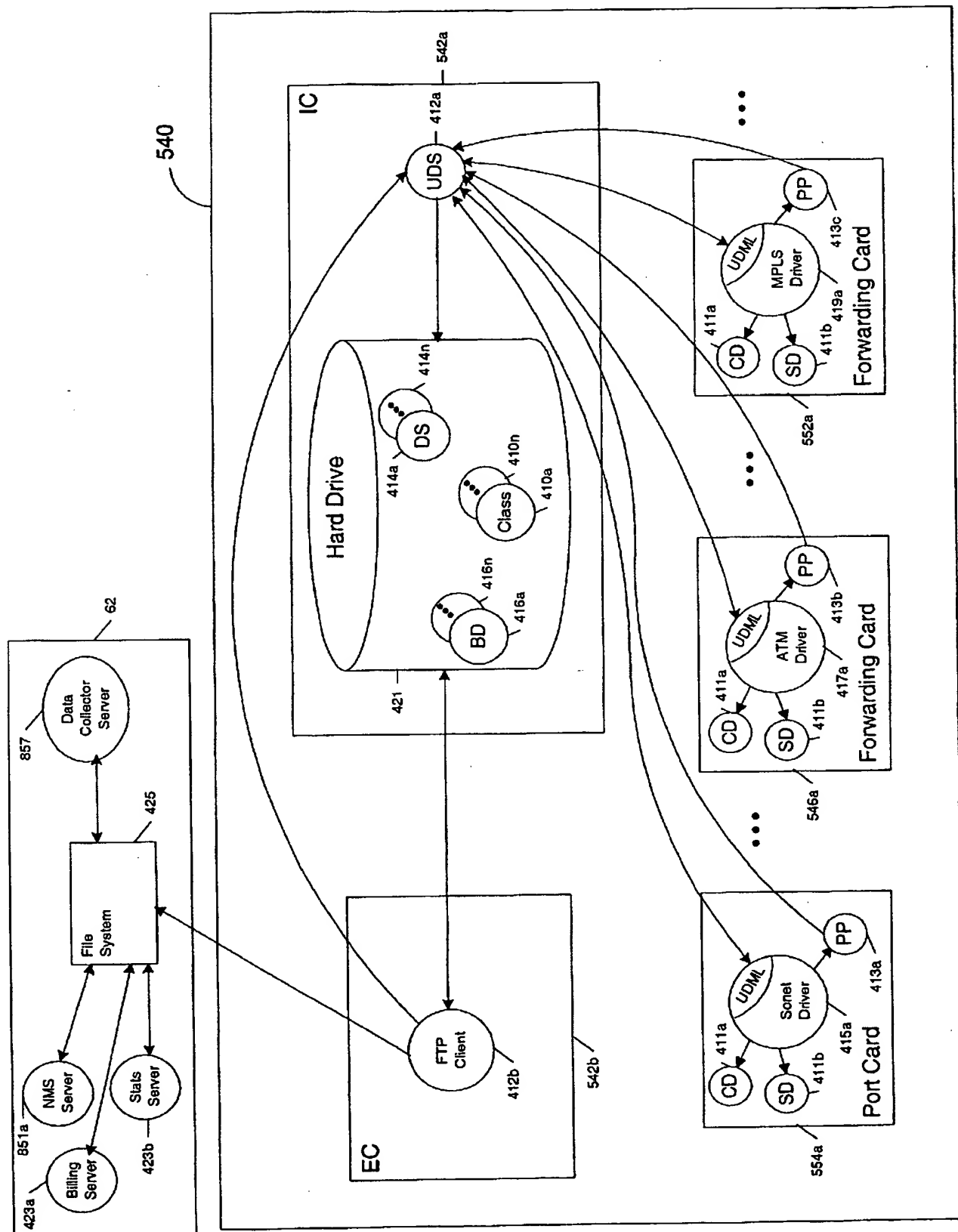


FIG. 14a

Service Endpoint Table 76

	Service Endpoint #	Port PID
78	1	1500
80	2	1501
82	3	1501
84	4	1501
86	5	1502
88	6	1502
90	7	1503
92	8	1503
94	9	1503
168	10	1502
	⋮	⋮

FIG. 14b

Logical to Physical Card Table 100

	LID	Primary PID	Back-up PID
106	30	500	513
109	31	501	513
	⋮	⋮	⋮

FIG. 14c

Logical to Physical Port Table 101

	LID	Primary PID	Back-up PID
107	40	1500	1600
	⋮	⋮	⋮

FIG. 14d

ATM Group Table 108

Group #	Card LID	...
1	30	
2	30	
3	30	
4	30	

FIG. 14e

ATM Interface Table 114

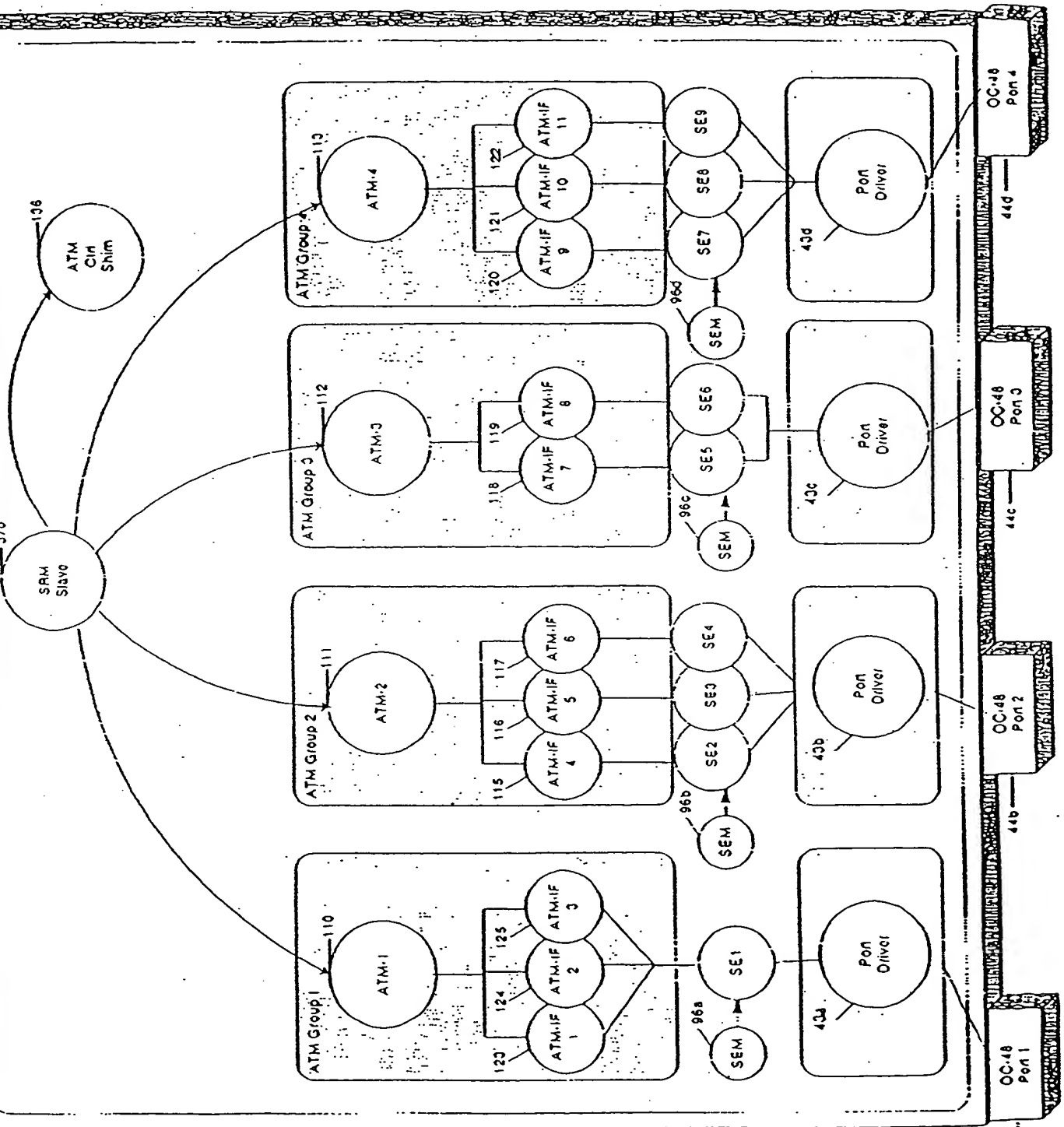
ATM IF	ATM Group	SE	...
1	1	1	
2	1	1	
3	1	1	
4	2	2	
5	2	3	
6	2	4	
⋮	⋮	⋮	⋮
12	3	10	
⋮	⋮	⋮	⋮

FIG. 14f

Software Load Record 128a

130	Control Shim	LID	132
134	atm-cntl.exe	30	

FIG. 15



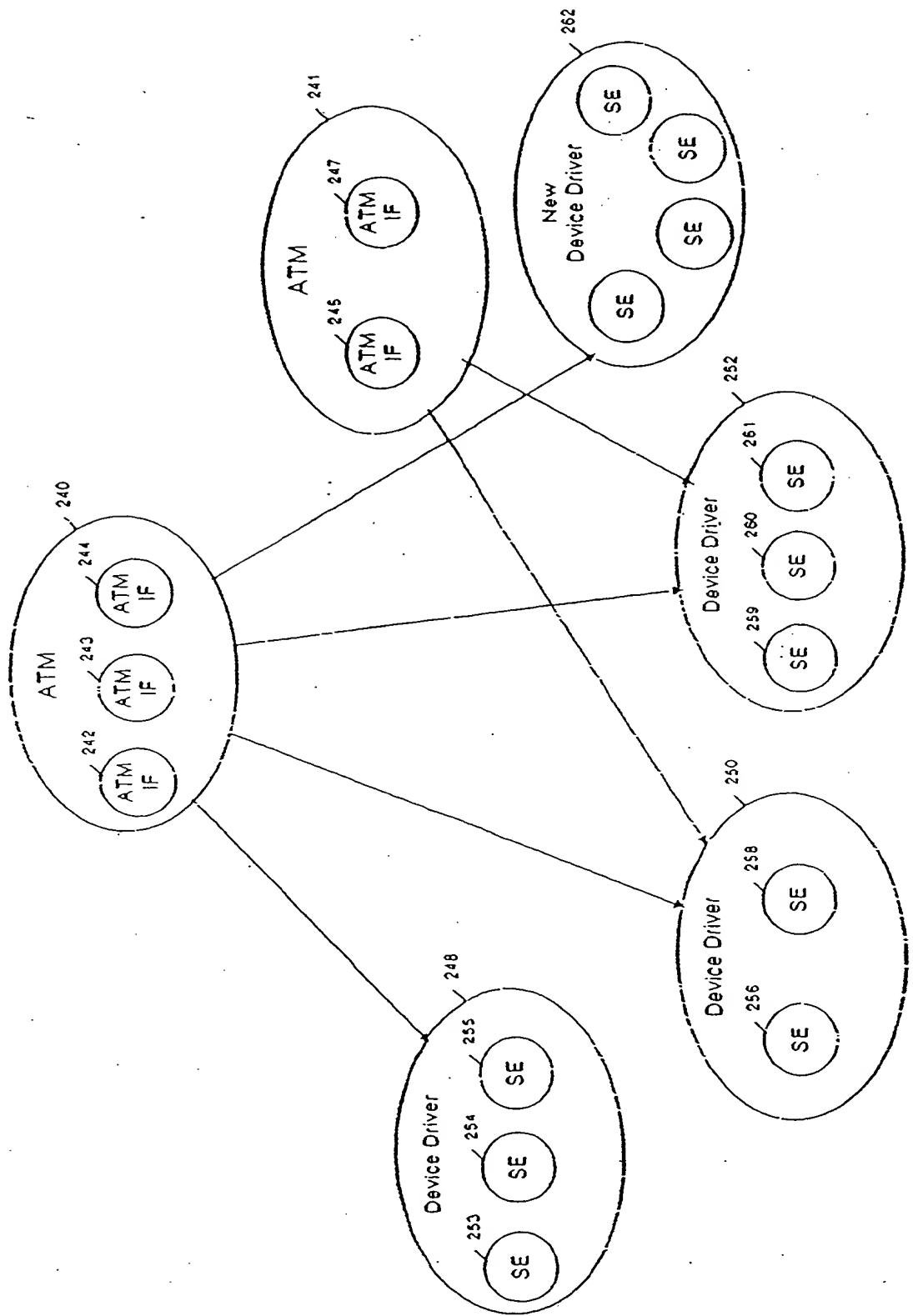




FIG. 16b

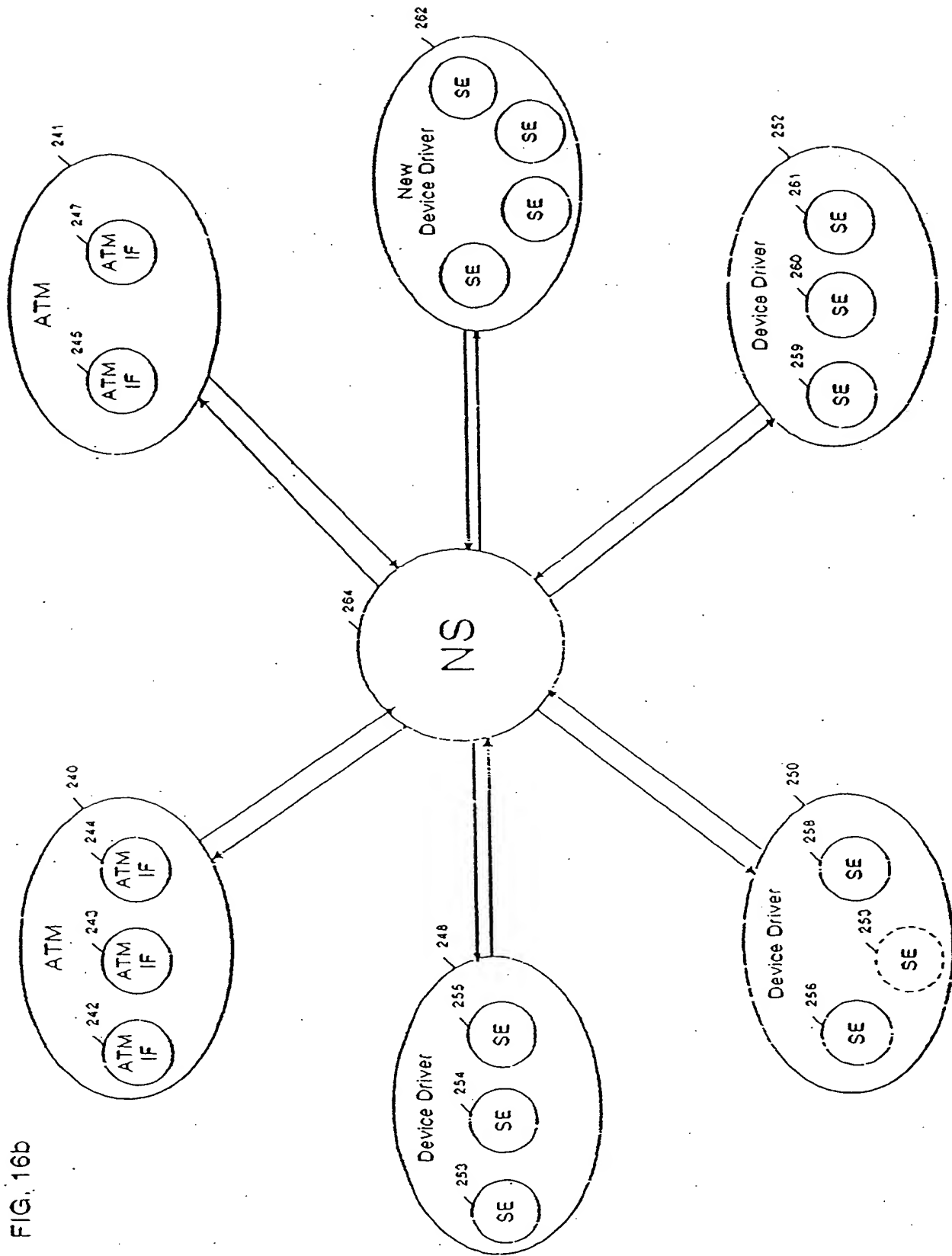
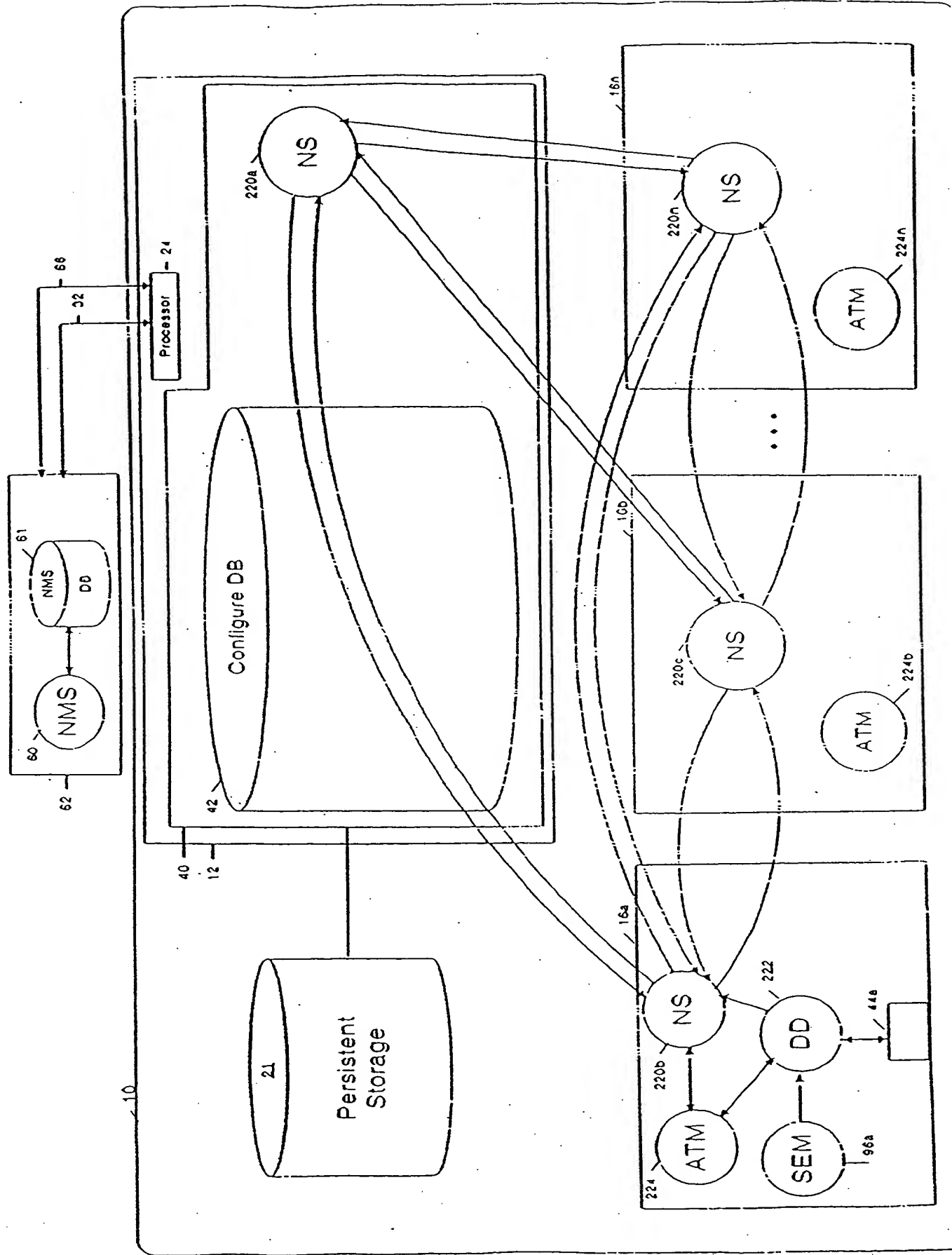


FIG. 16c



7



FIG. 17

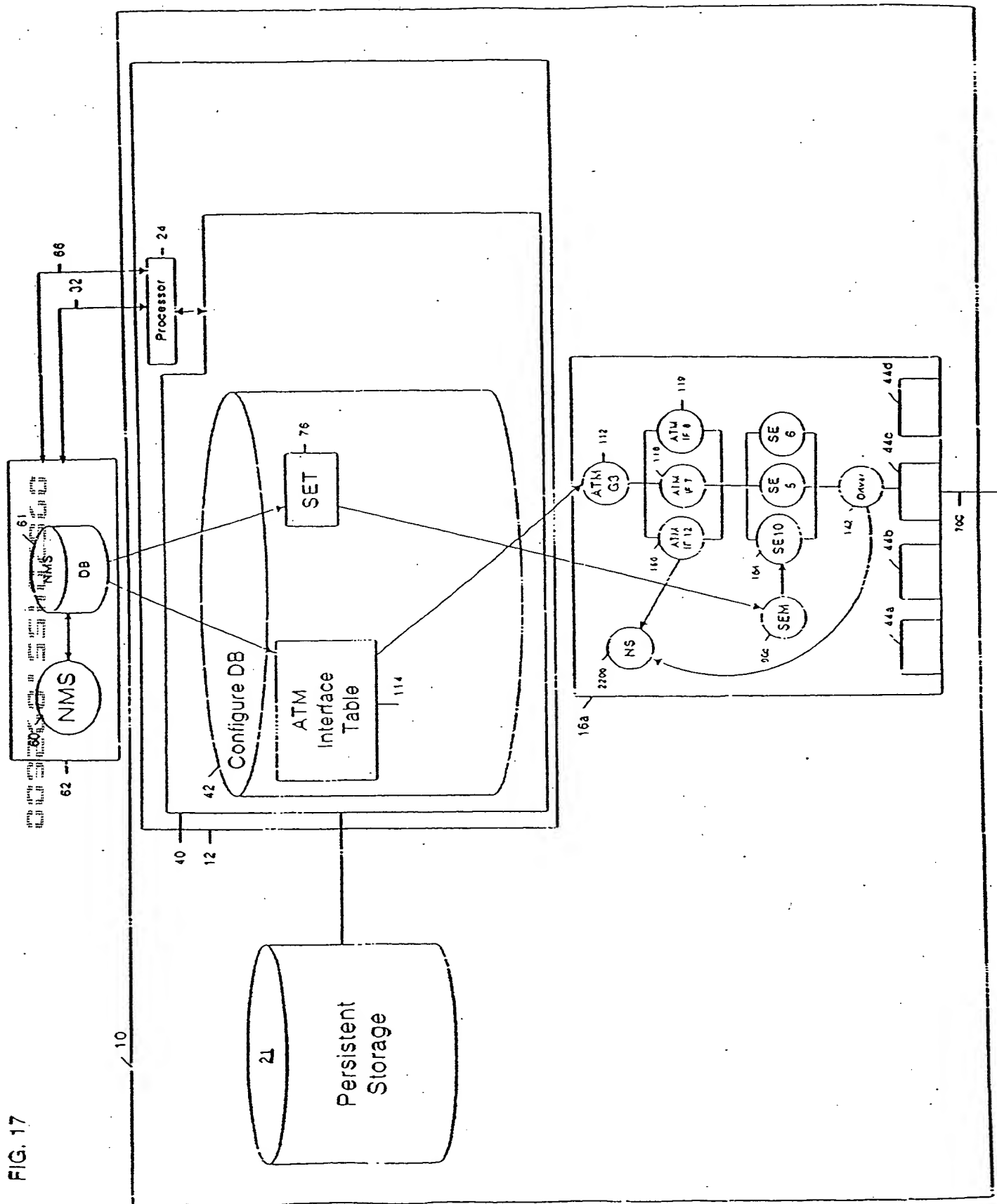


FIG. 18

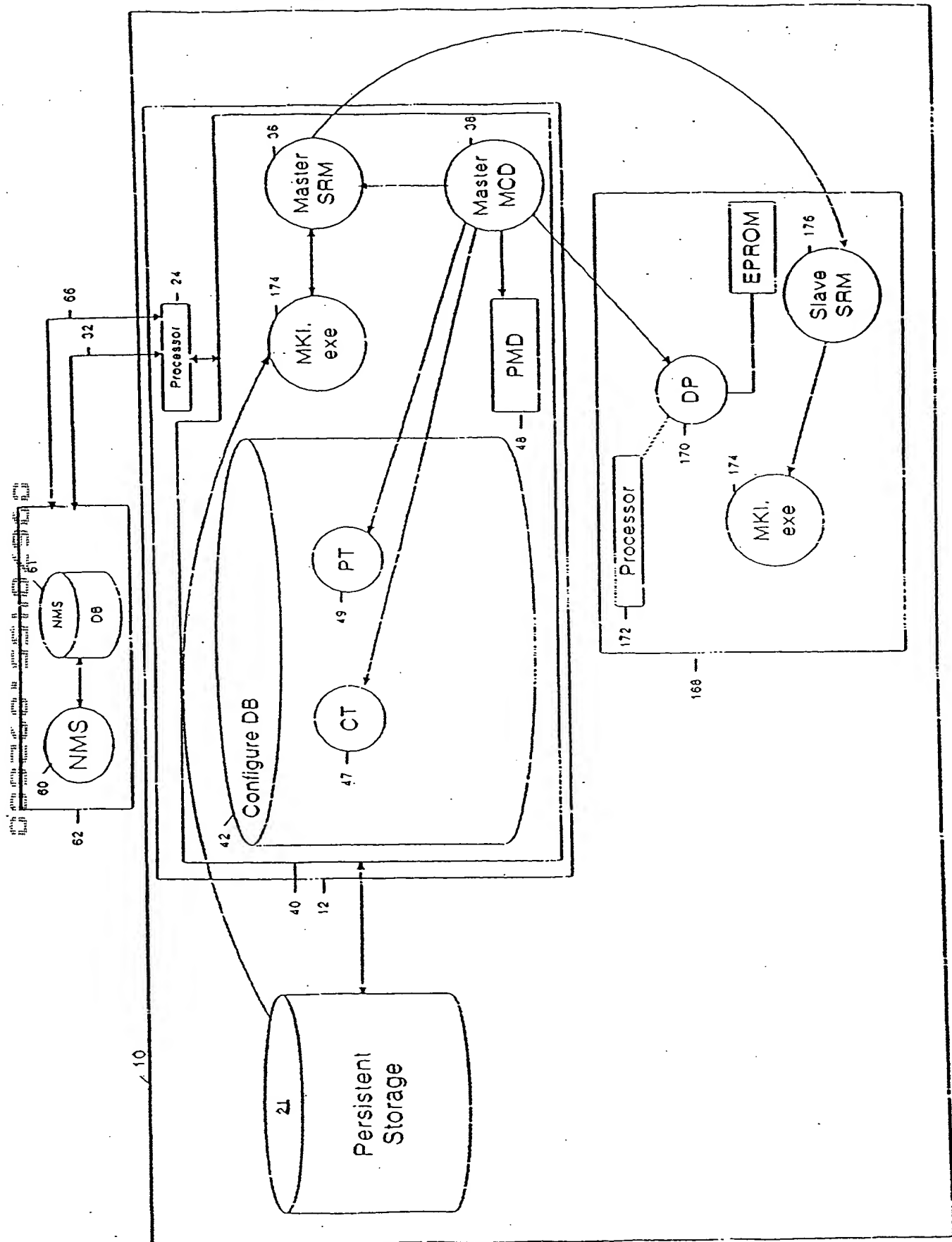


FIG. 19

FIG. 19 is a block diagram of a system architecture. The system includes a Persistent Storage unit (21) connected to a Processor (24) via a bus (12). The Processor (24) is connected to a Network Management System (NMS) (60) and a Database (DB) (61) via a bus (32). The NMS (60) is connected to a Master MCD (38) and a Slave MCD (178) via a bus (40). The Master MCD (38) is connected to a Master SRM (36) and a DD exe (180) via a bus (48). The Slave MCD (178) is connected to a Slave SRM (176) and a DD exe (180) via a bus (168). The Master MCD (38) is also connected to a CT (47) via a bus (48). The CT (47) is connected to the Config DB (42) via a bus (40). The Config DB (42) is connected to the Processor (24) via a bus (12).

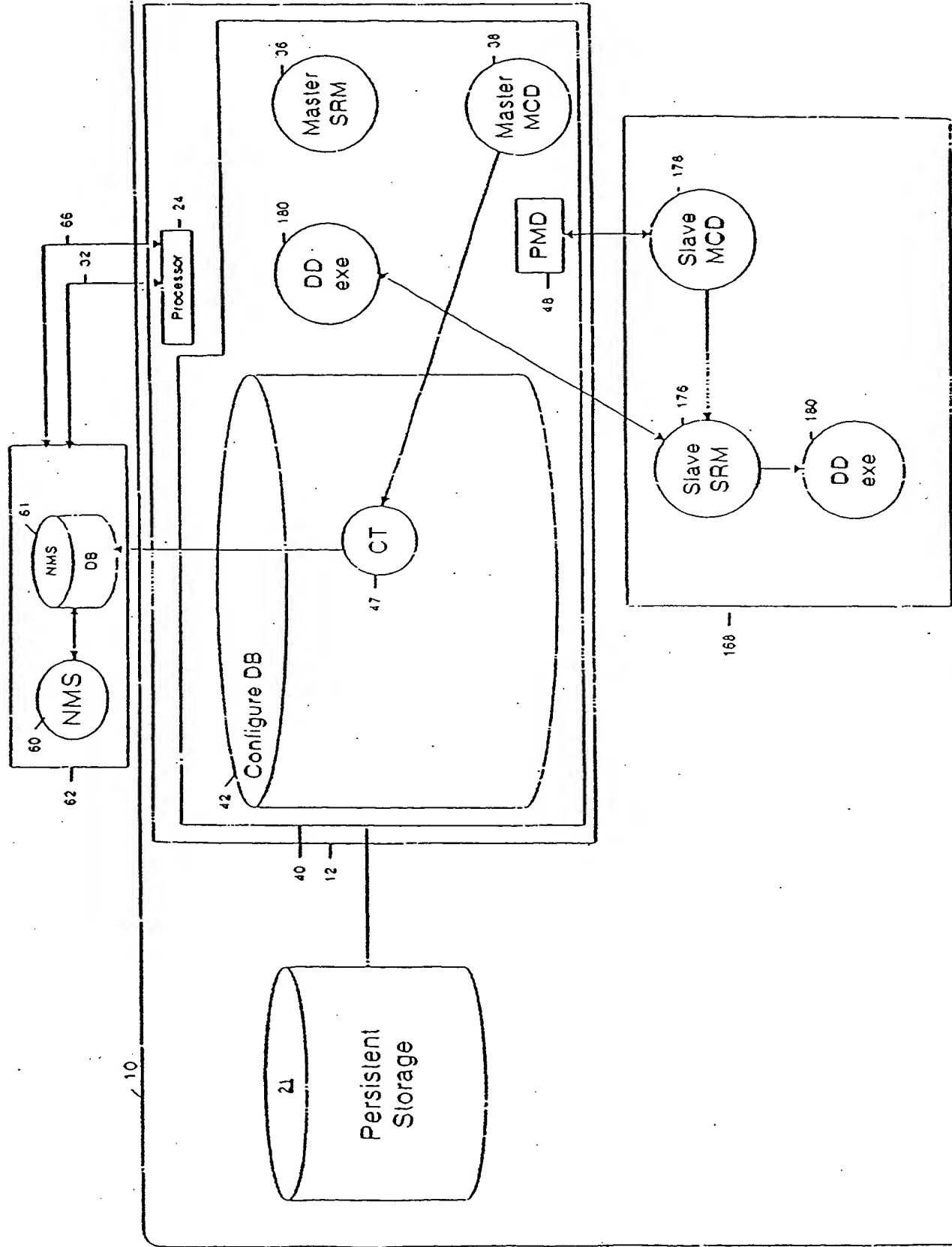


FIG. 20

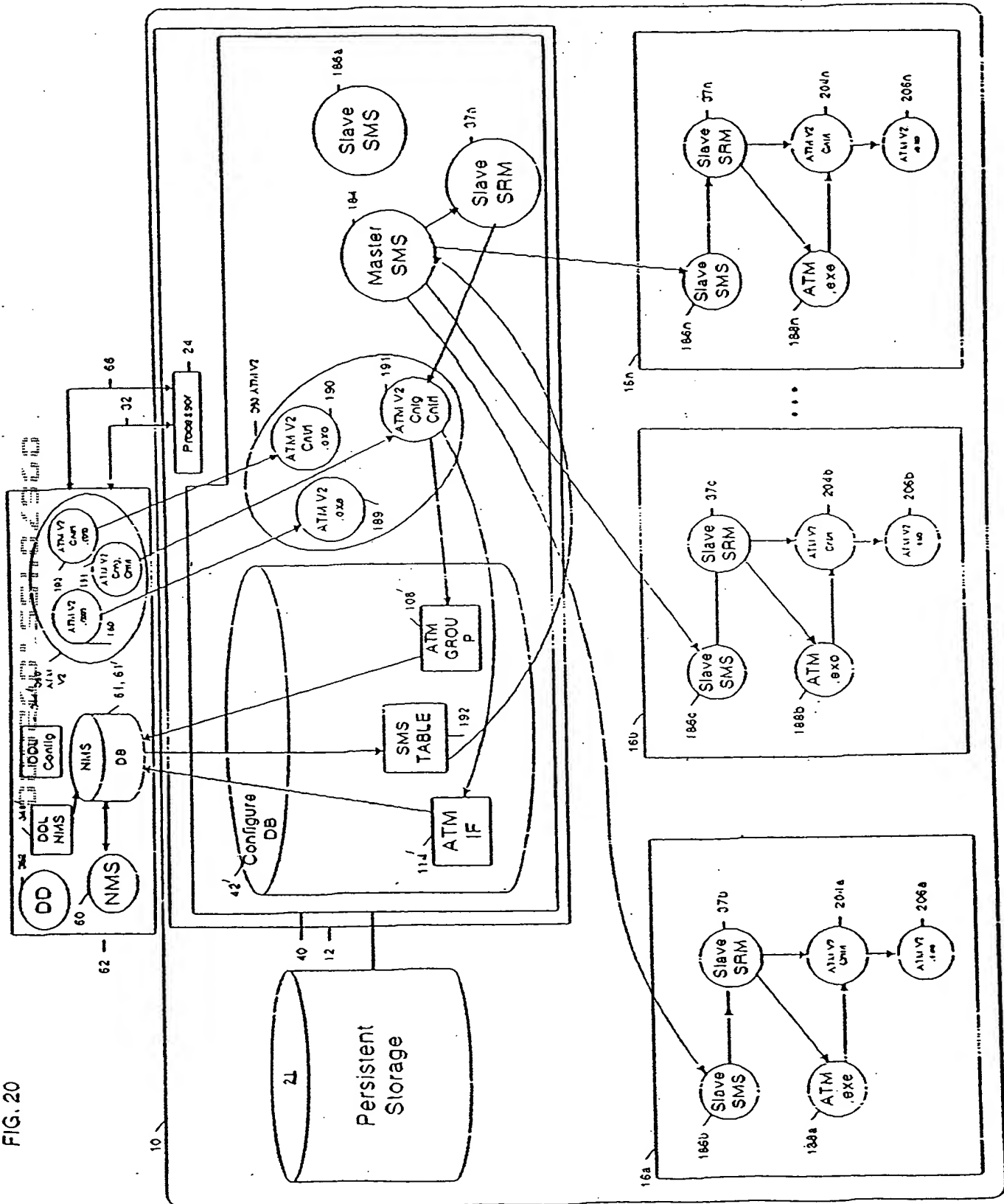


FIG. 21

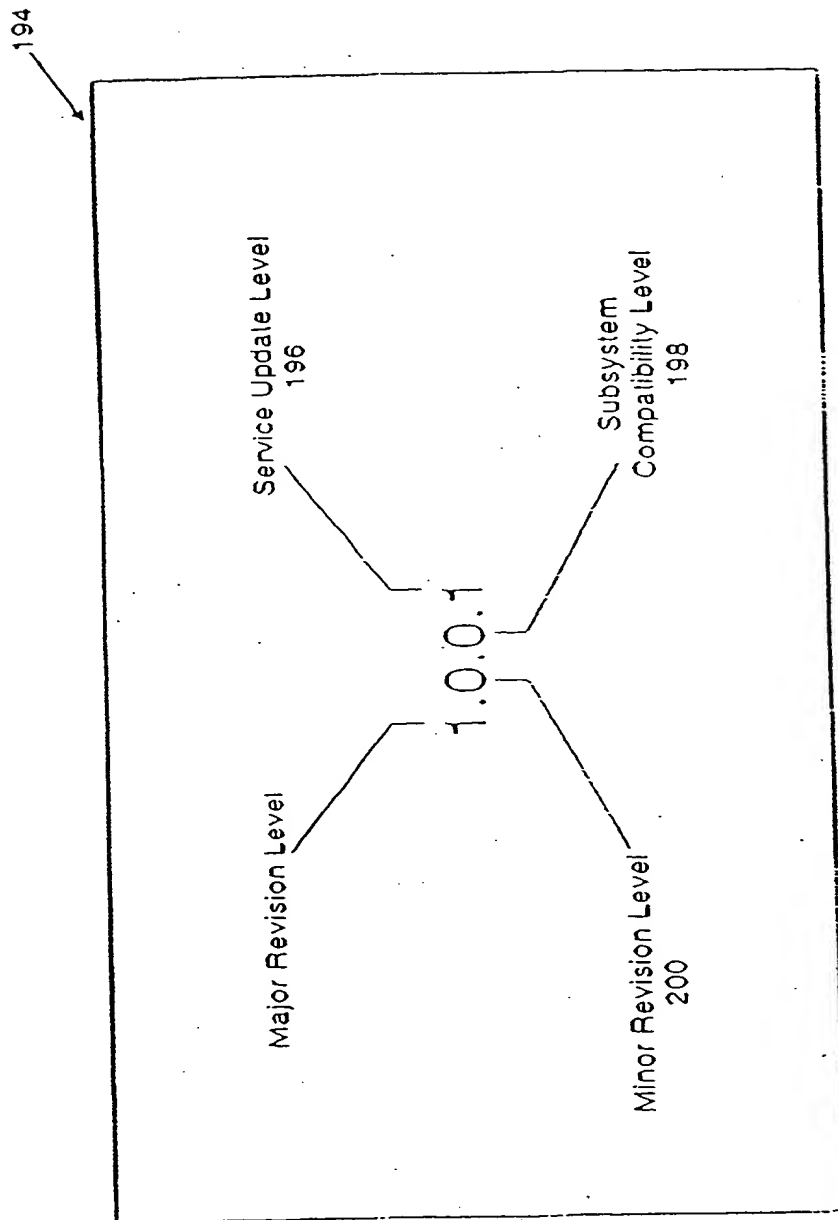




FIG. 22

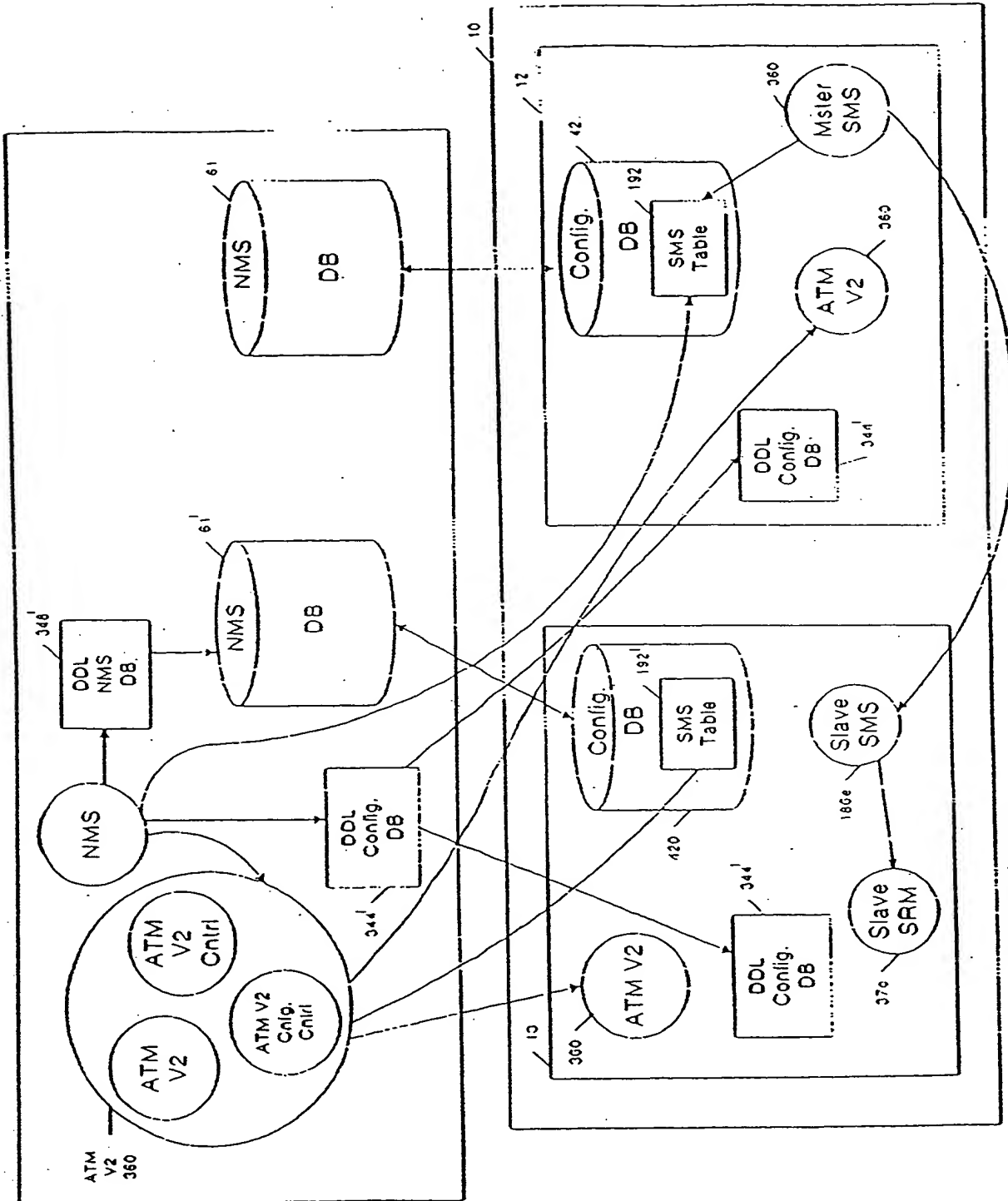


FIG. 23

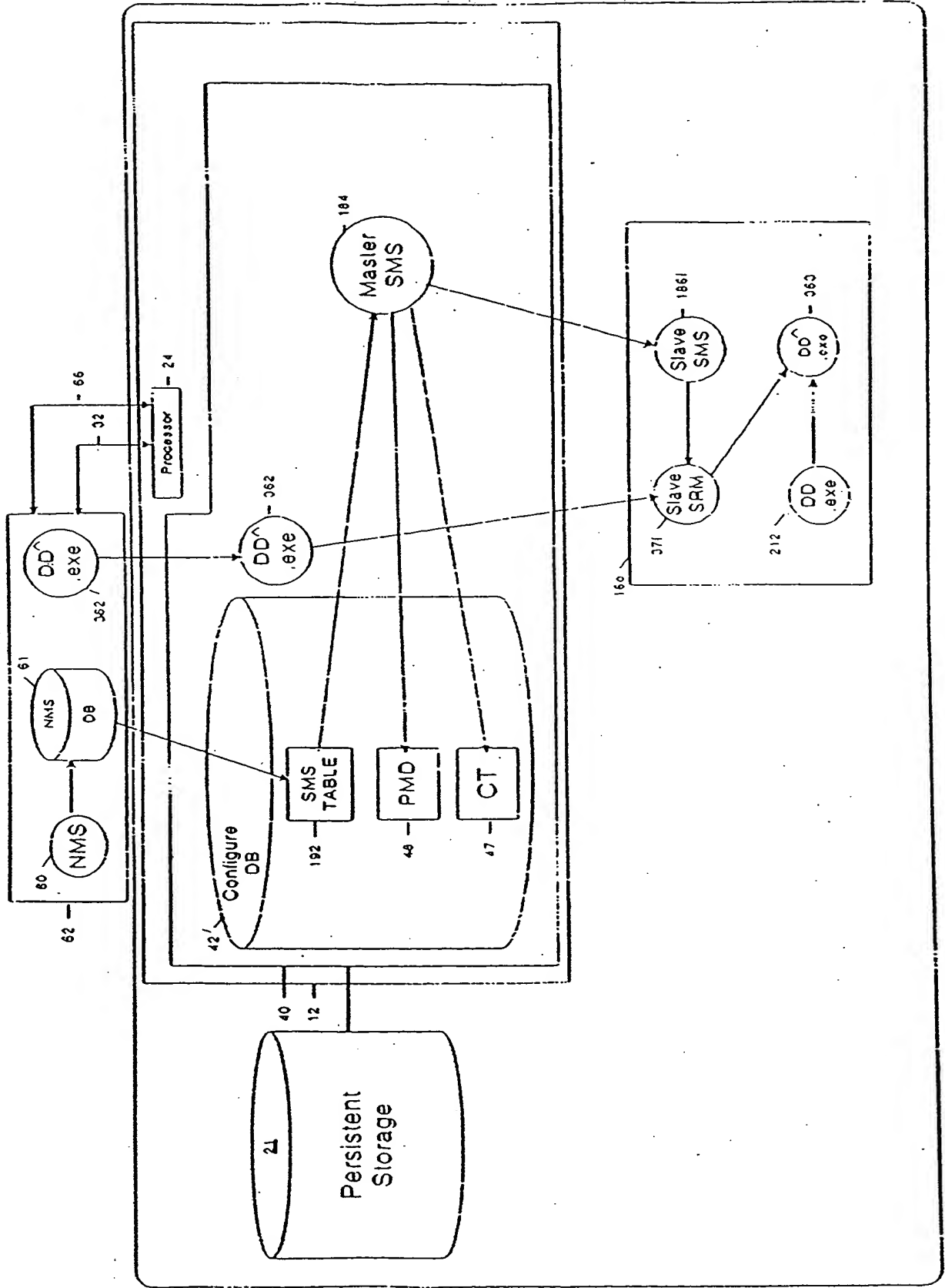


FIG. 24

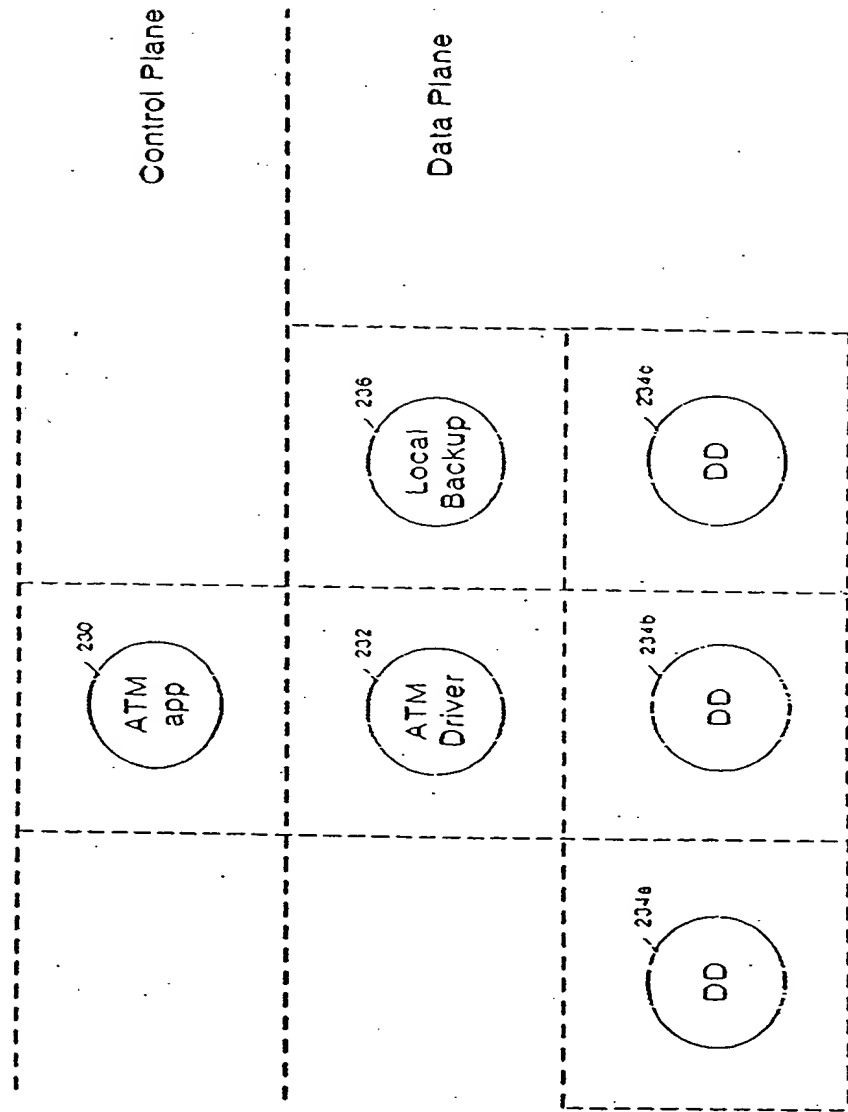


FIG. 25

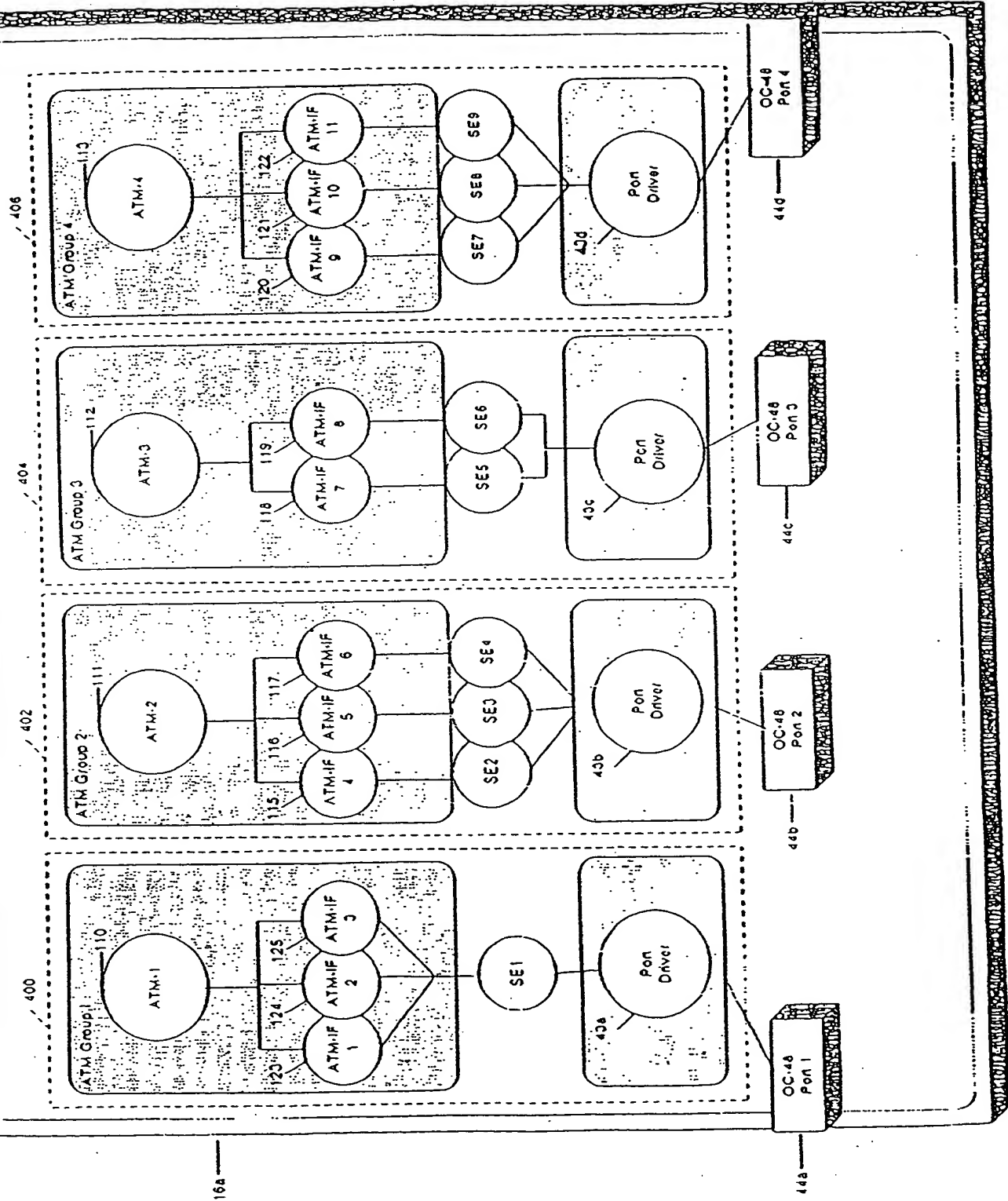


FIG. 26

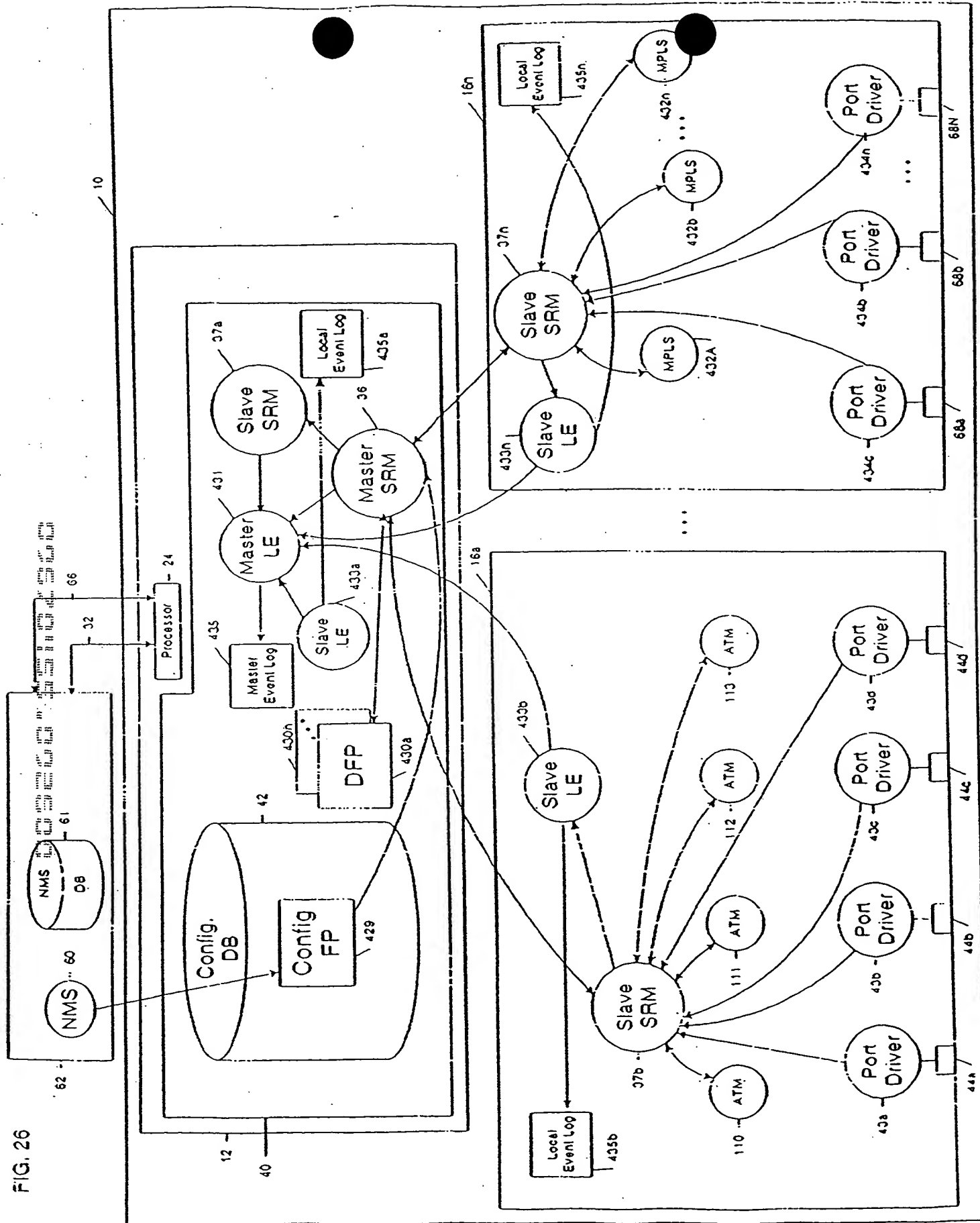


FIG. 27

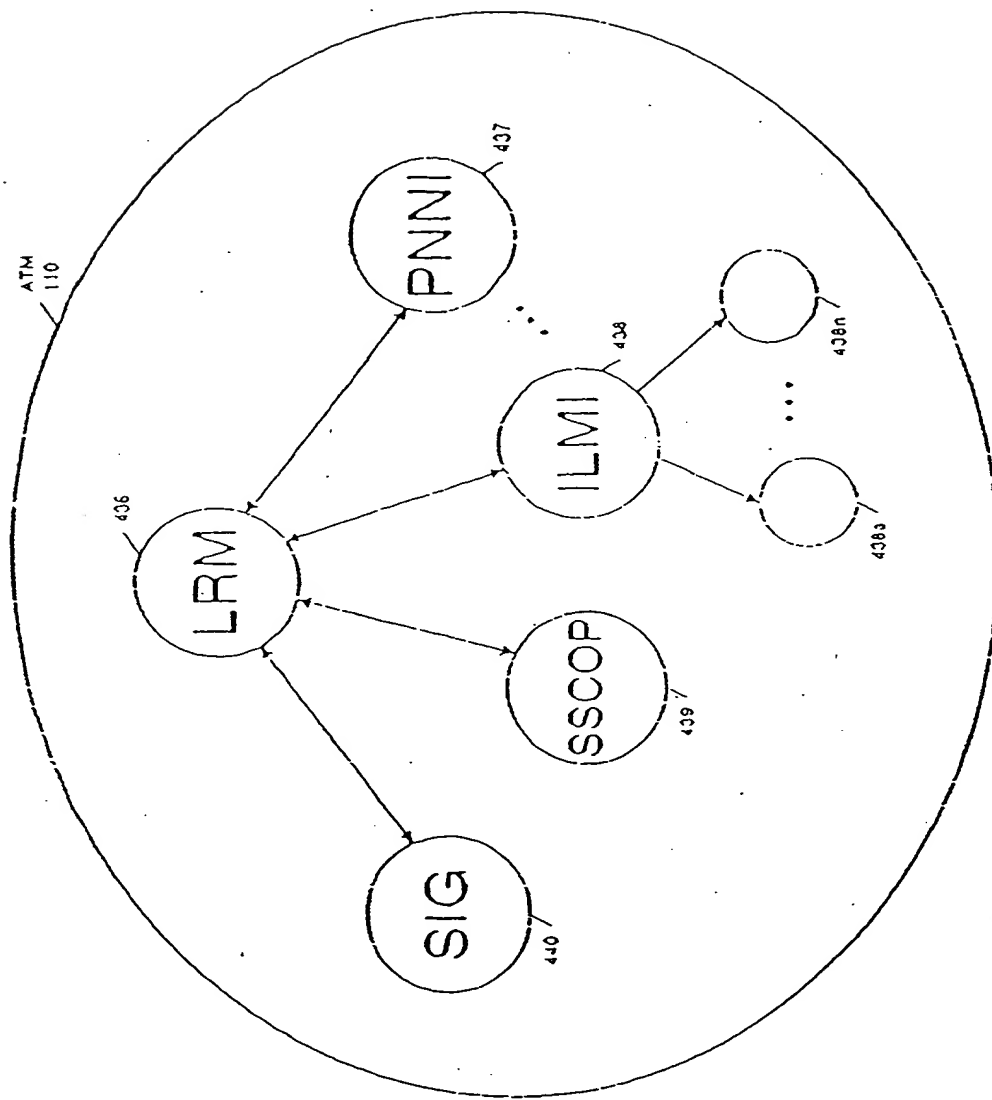
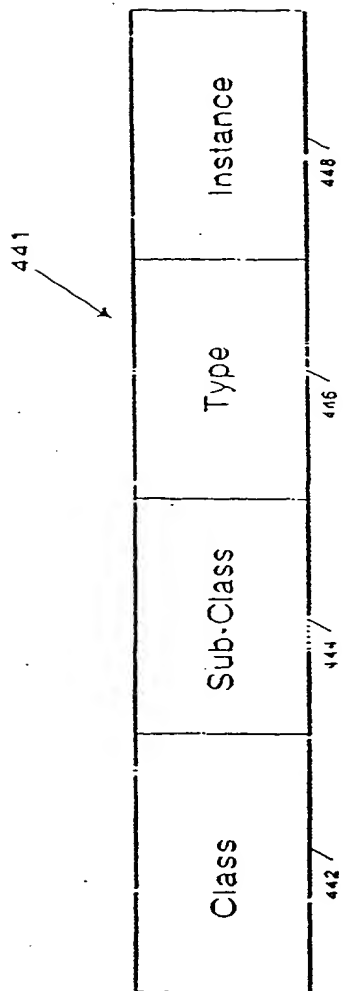
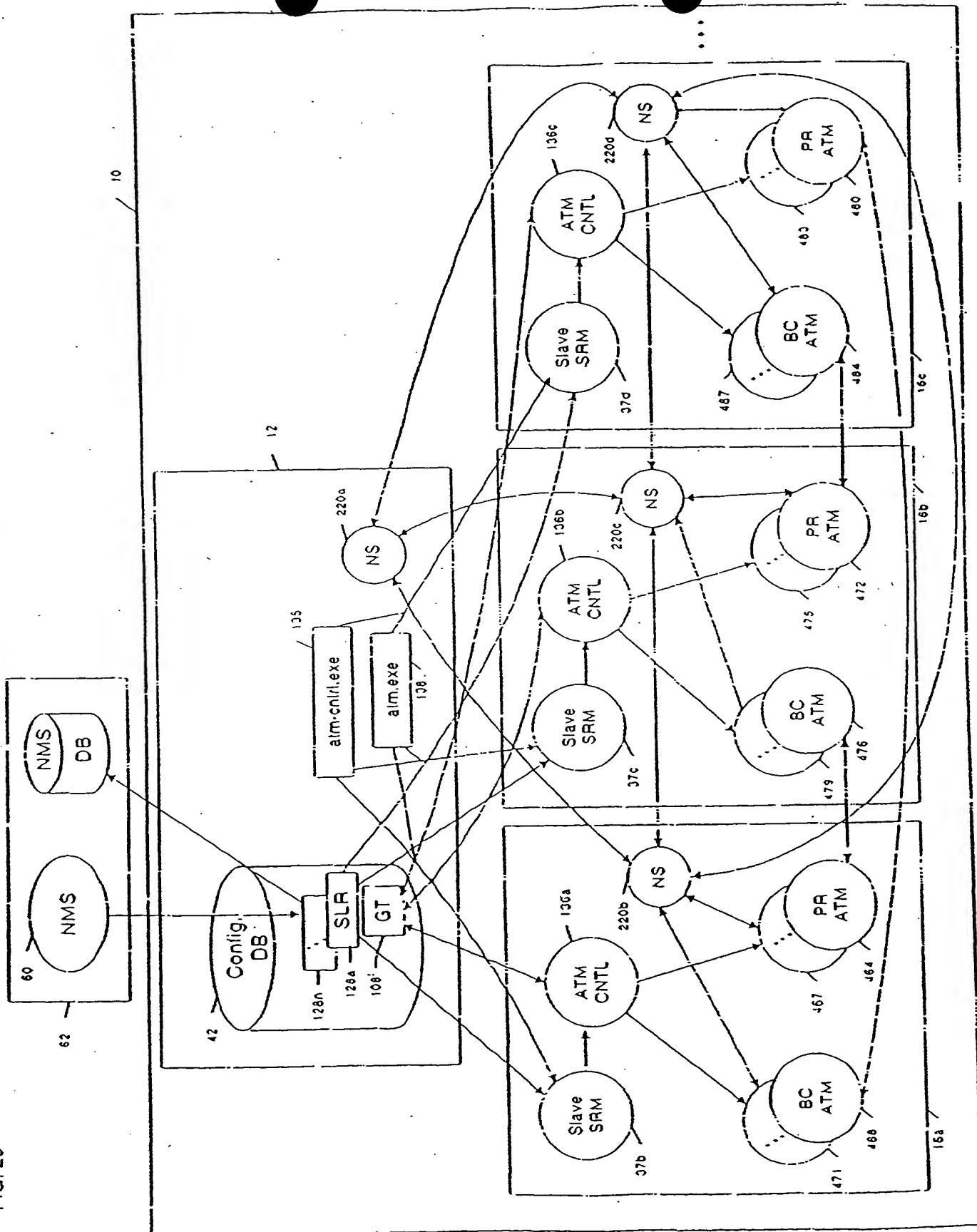


FIG. 28



[illegible]



Group Table 108'

	Group #	Primary Card LID	Backup Card LID	...
450	1	30	31	
451	2	30	31	
452	3	30	31	
453	4	30	31	
454	5	31	32	
455	6	31	32	
456	7	31	32	
457	8	31	32	
458	9	32	30	
459	10	32	30	
460	11	32	30	
461	12	32	30	
	⋮	⋮	⋮	⋮

Fig. 31a

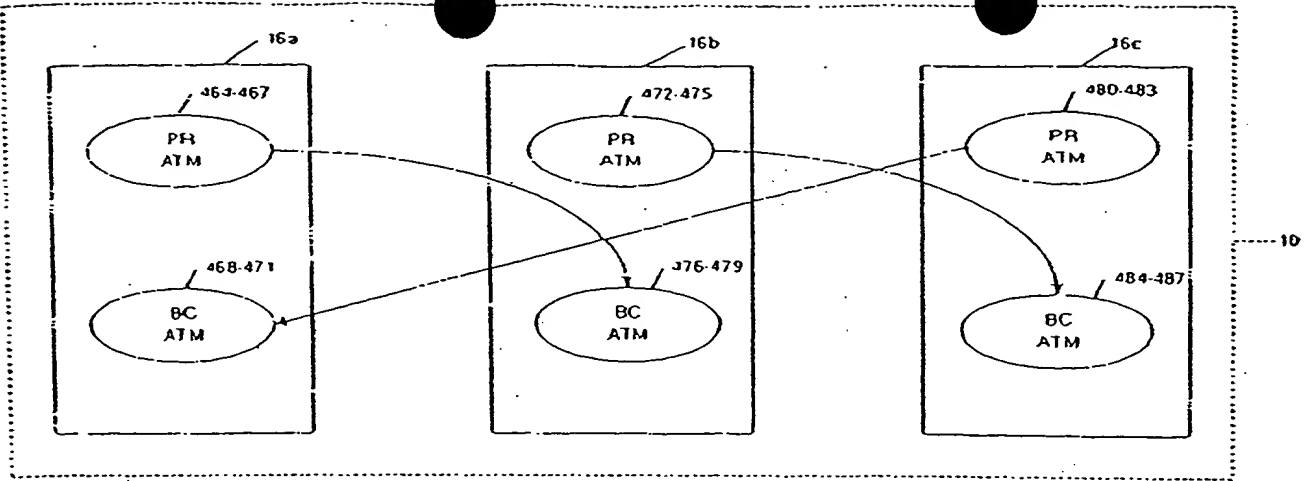


Fig. 31b

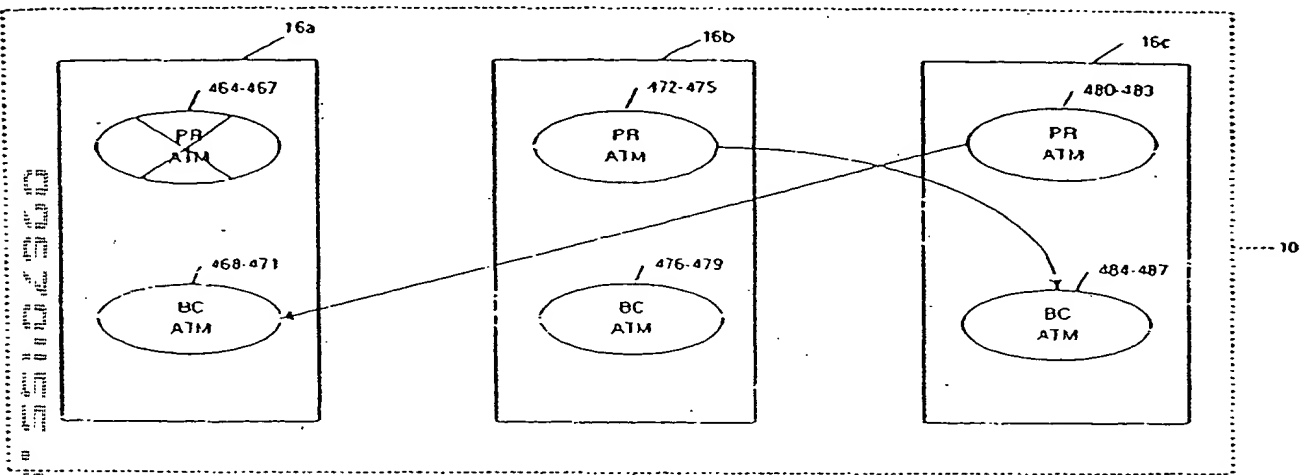


Fig. 31c

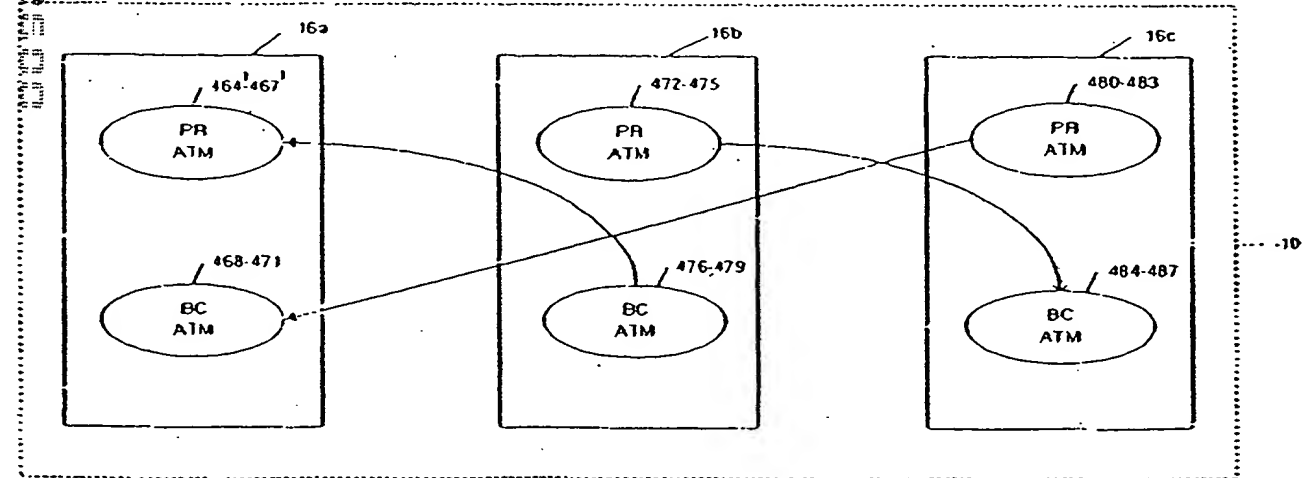


Fig. 32a

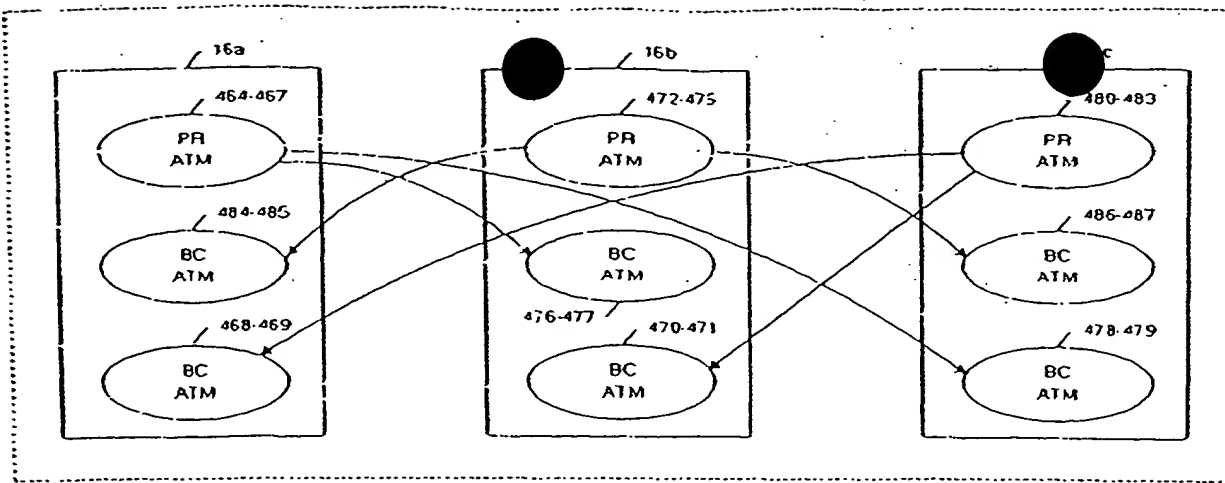


Fig. 32b

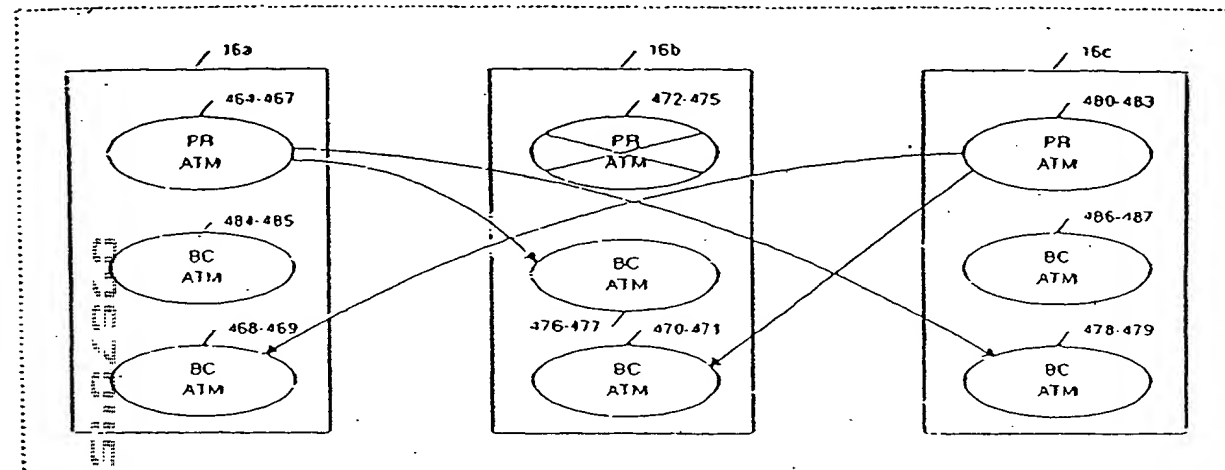


Fig. 32c

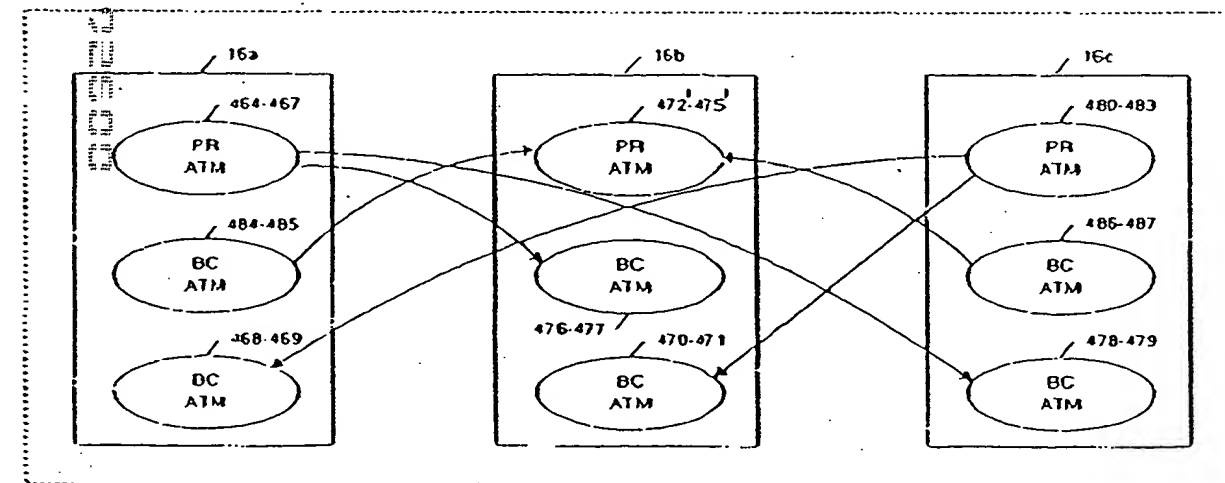


FIG. 33a

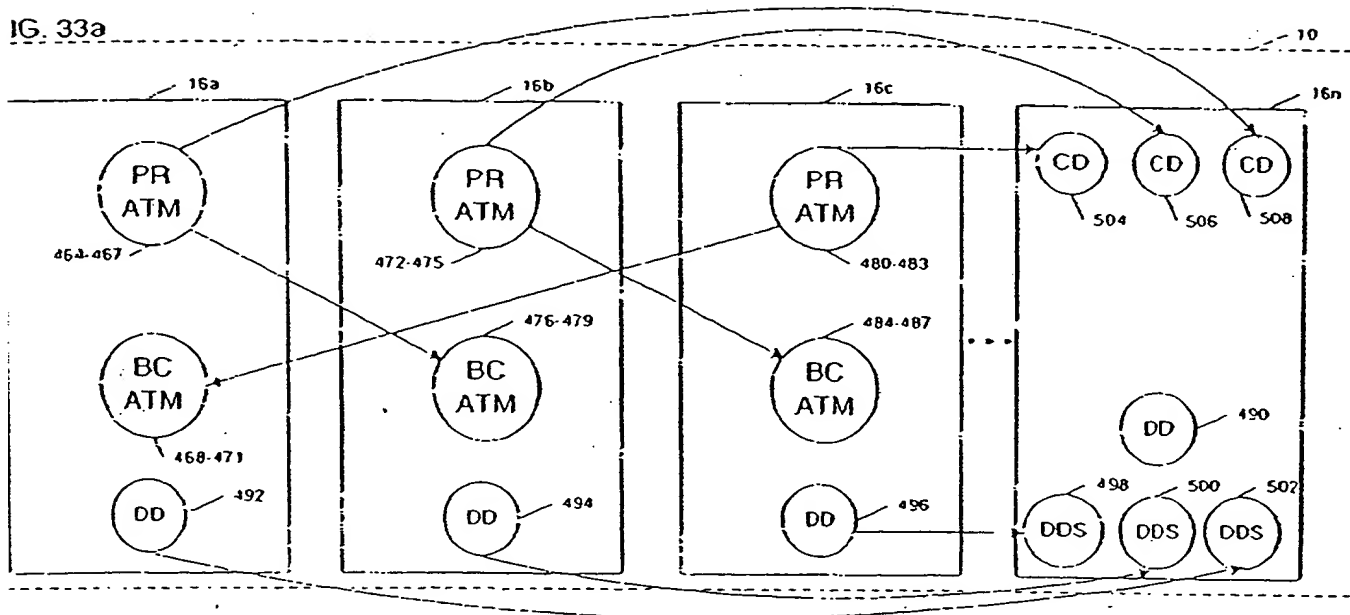
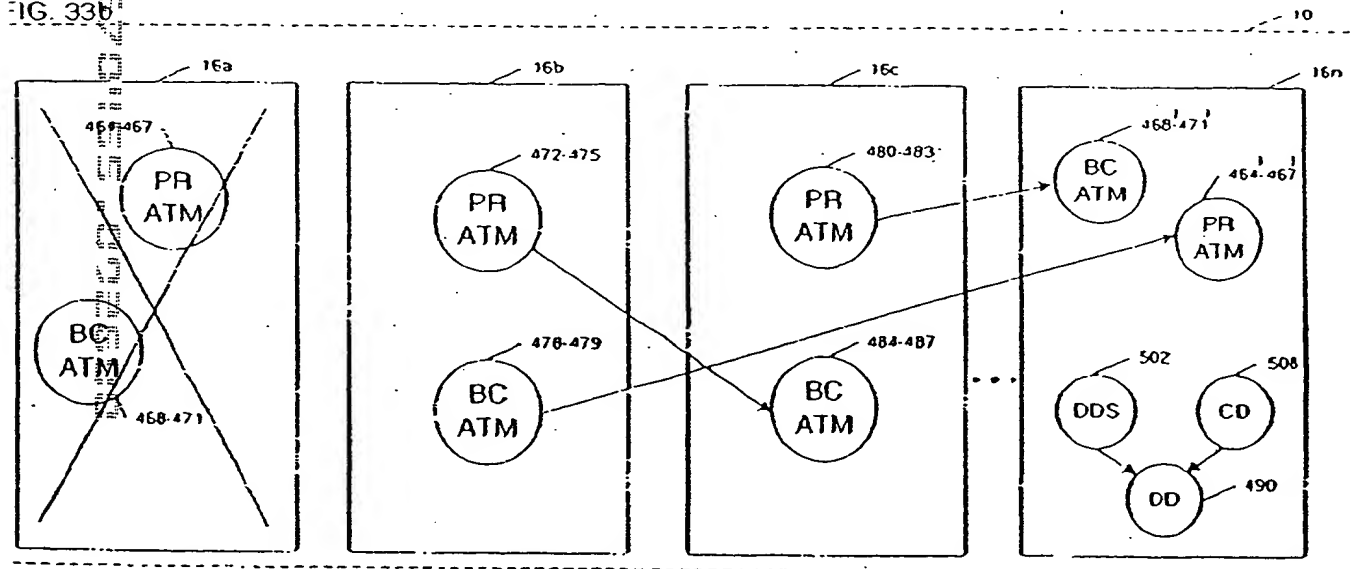
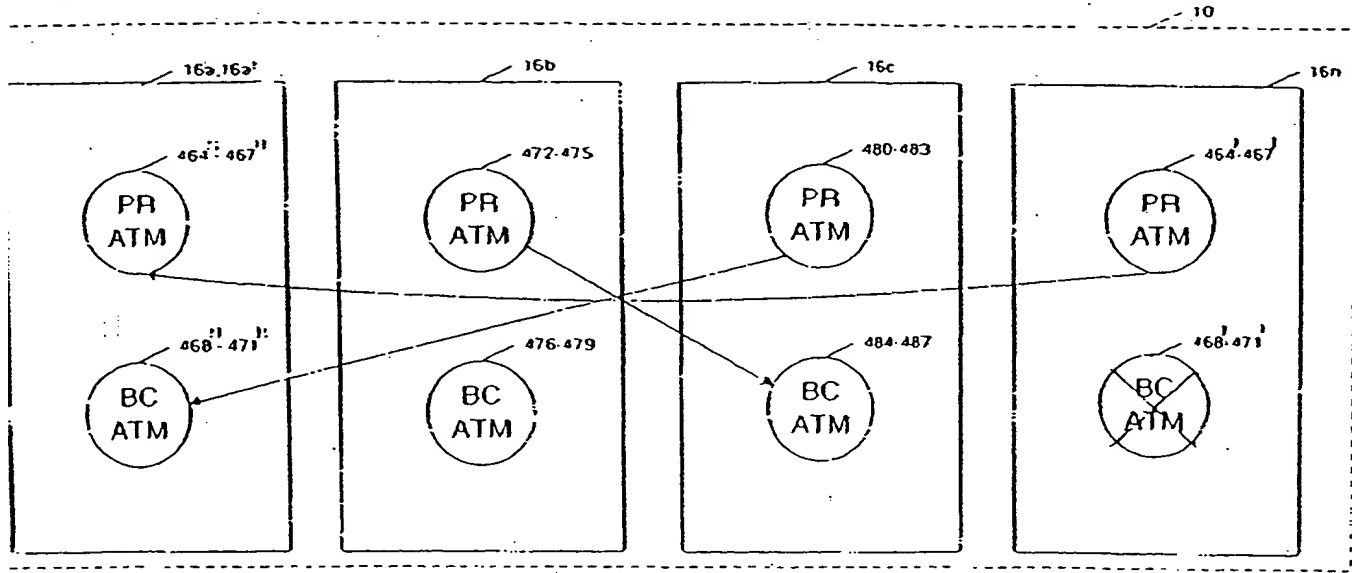


FIG. 33b



3. 33c



3. 33d

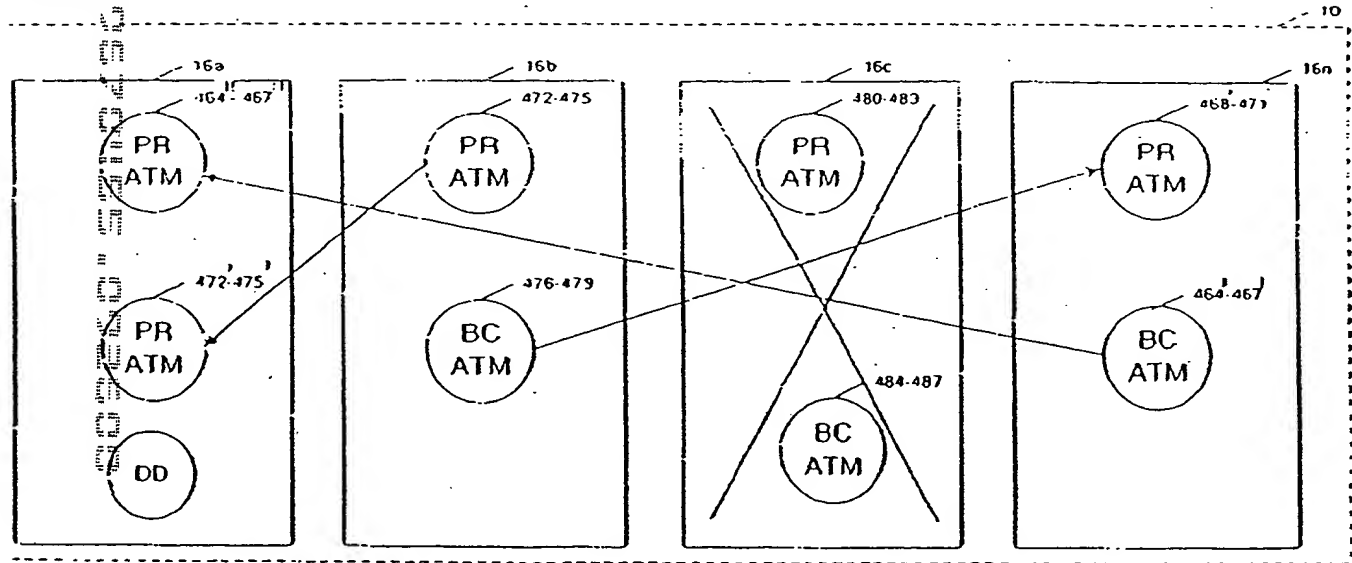


Fig. 34a

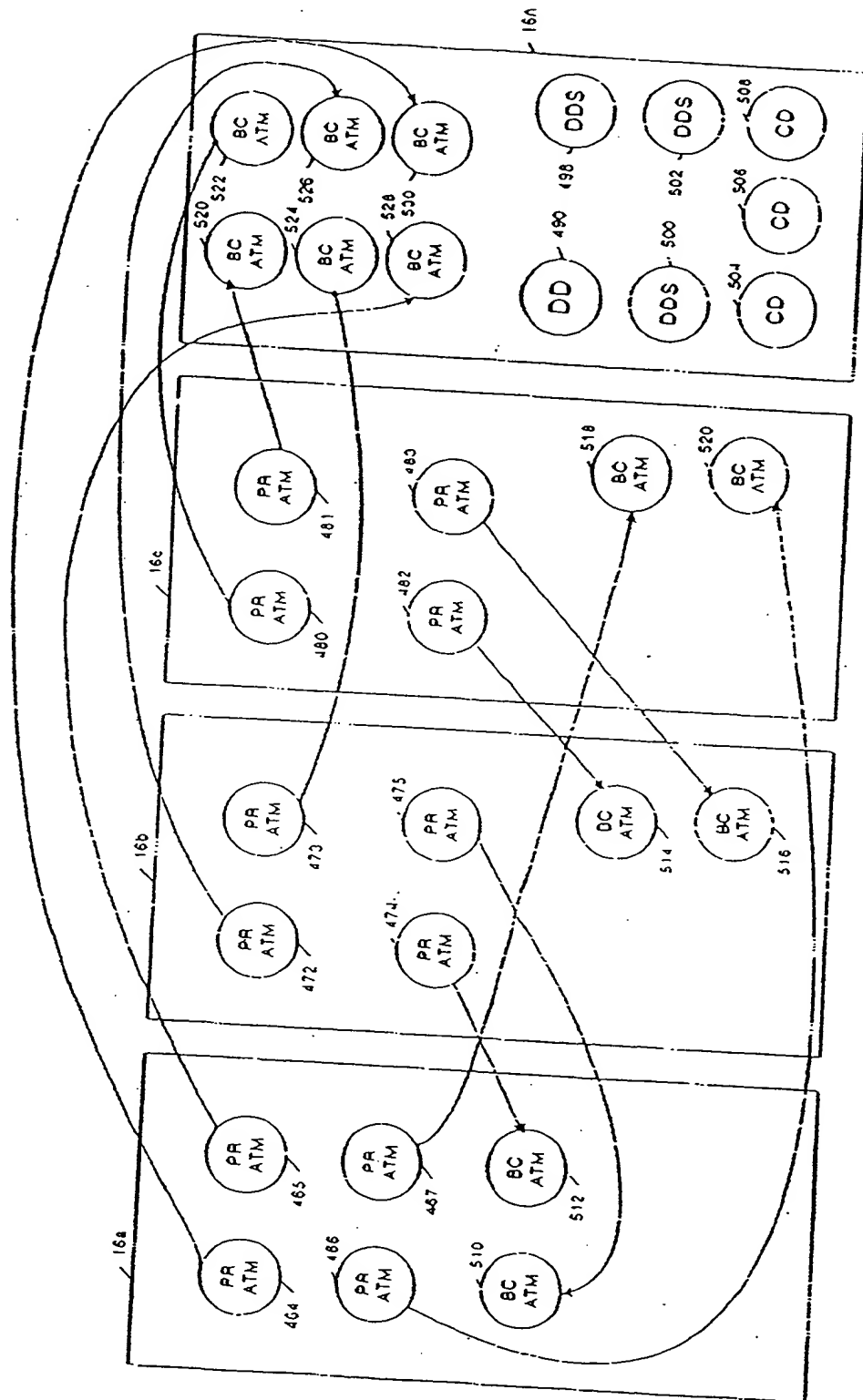
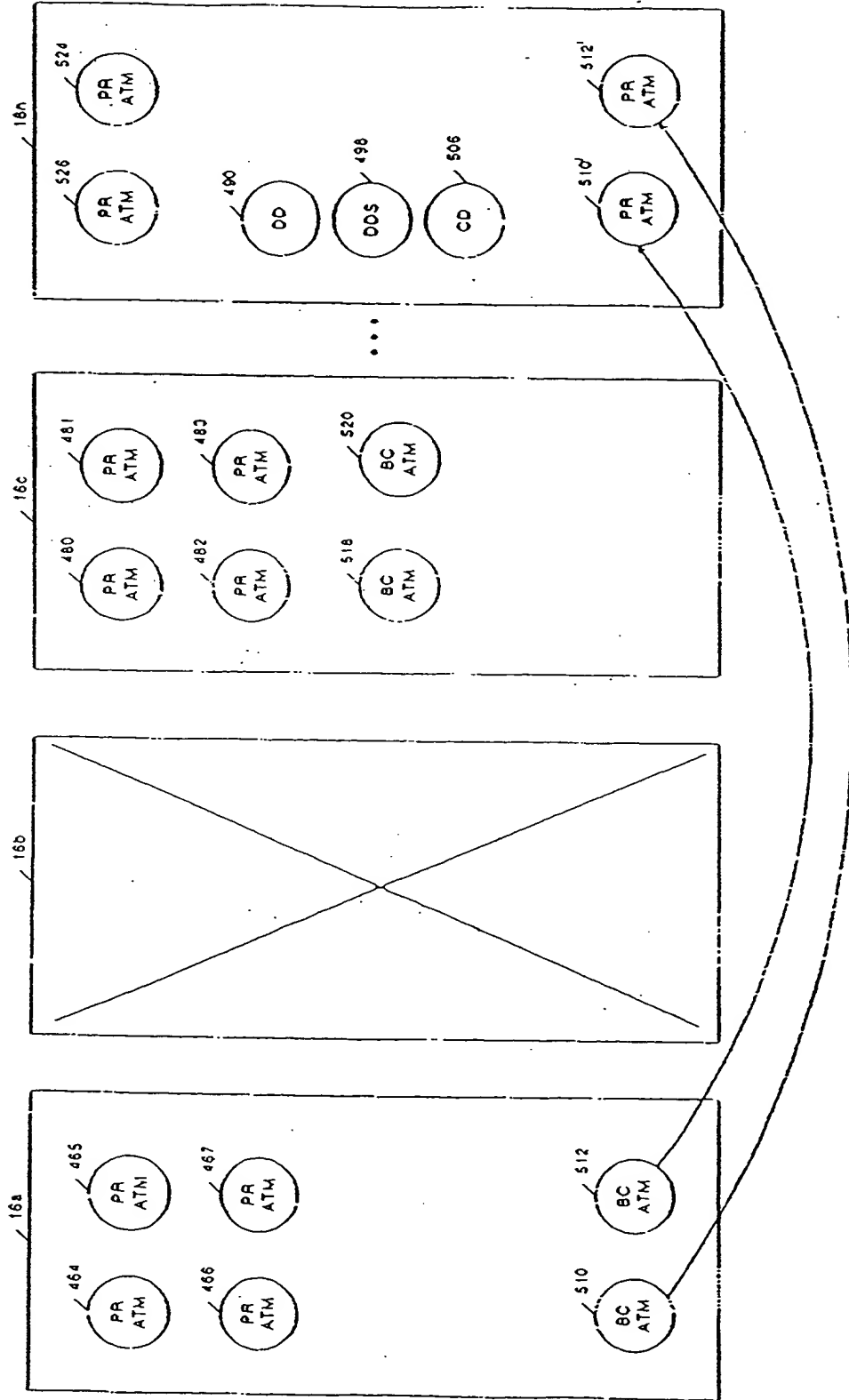
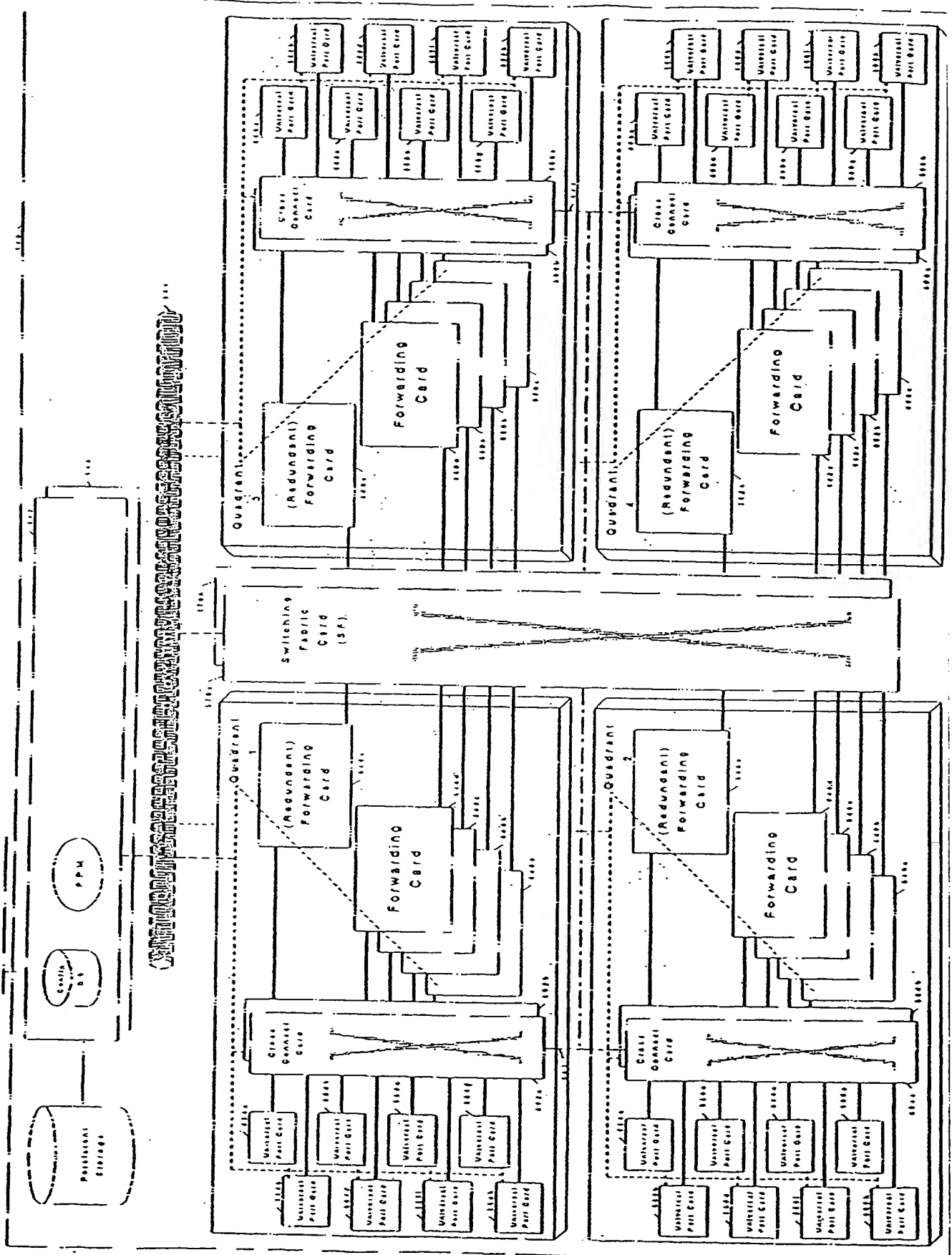


FIG. 34b



11-9





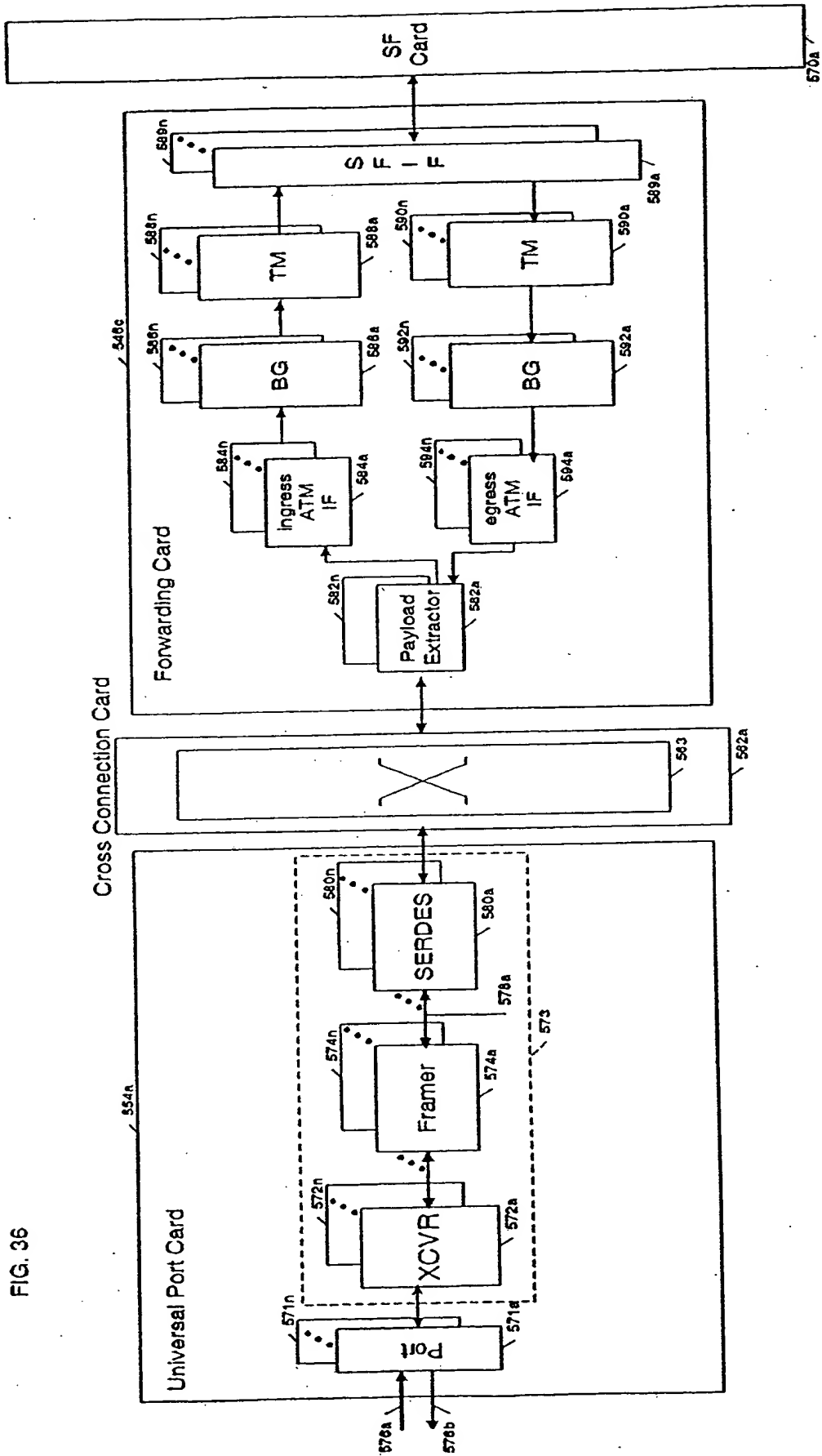


FIG. 36

FIG. 37

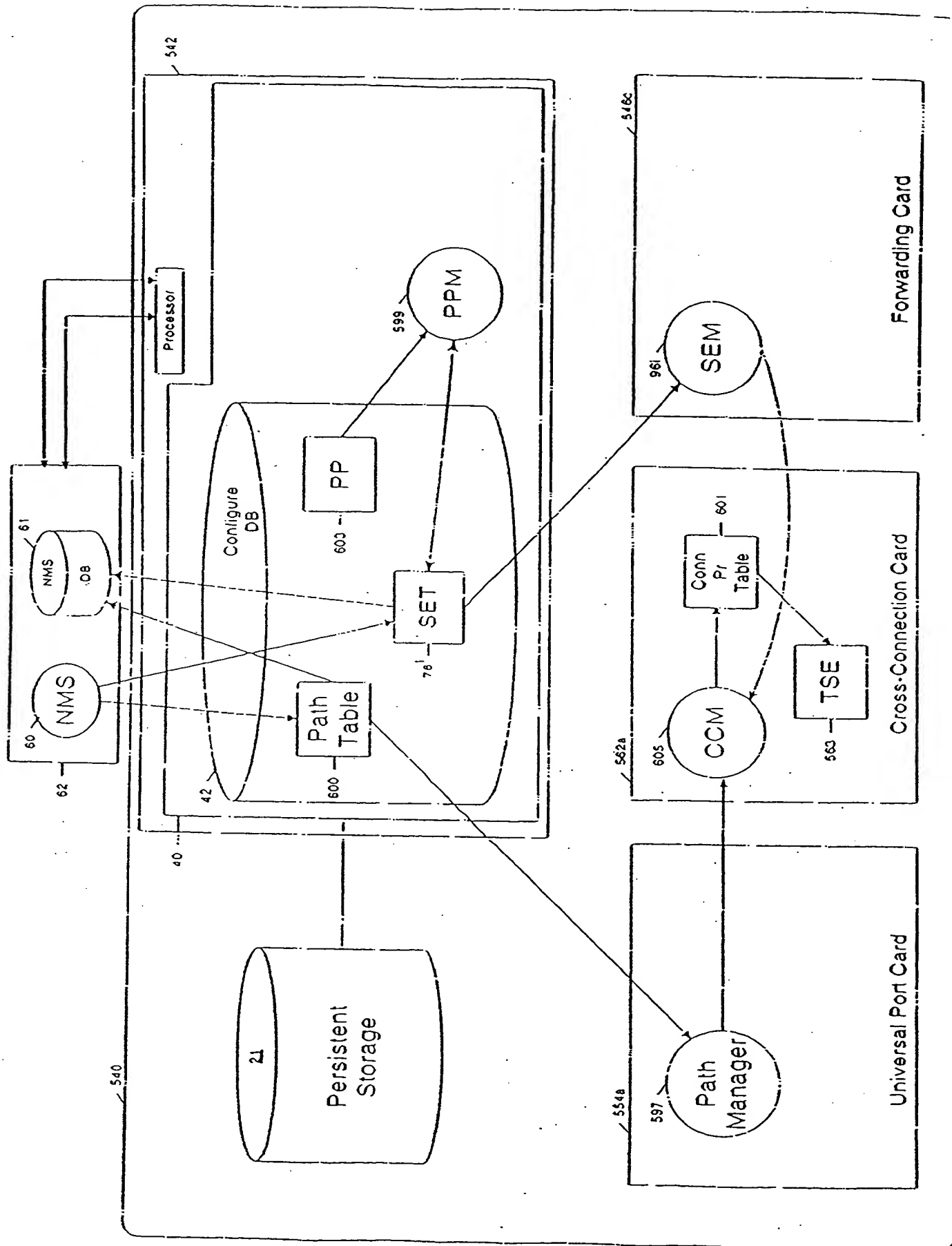


FIG. 38

Path Table 600

602

Path LID	UP Port LID	Time Slot	# of Time Slots	...
1666	1231	4	3	
...	...	...	...	...

FIG. 39

Service End Point Table 76'

SE #	Q #	FC LID	FC Slice	FC Time Slot	Path PID	...
878	1				1666	
:	:	:	:	:	:	:
:	:	:	:	:	:	:
:	:	:	:	:	:	:

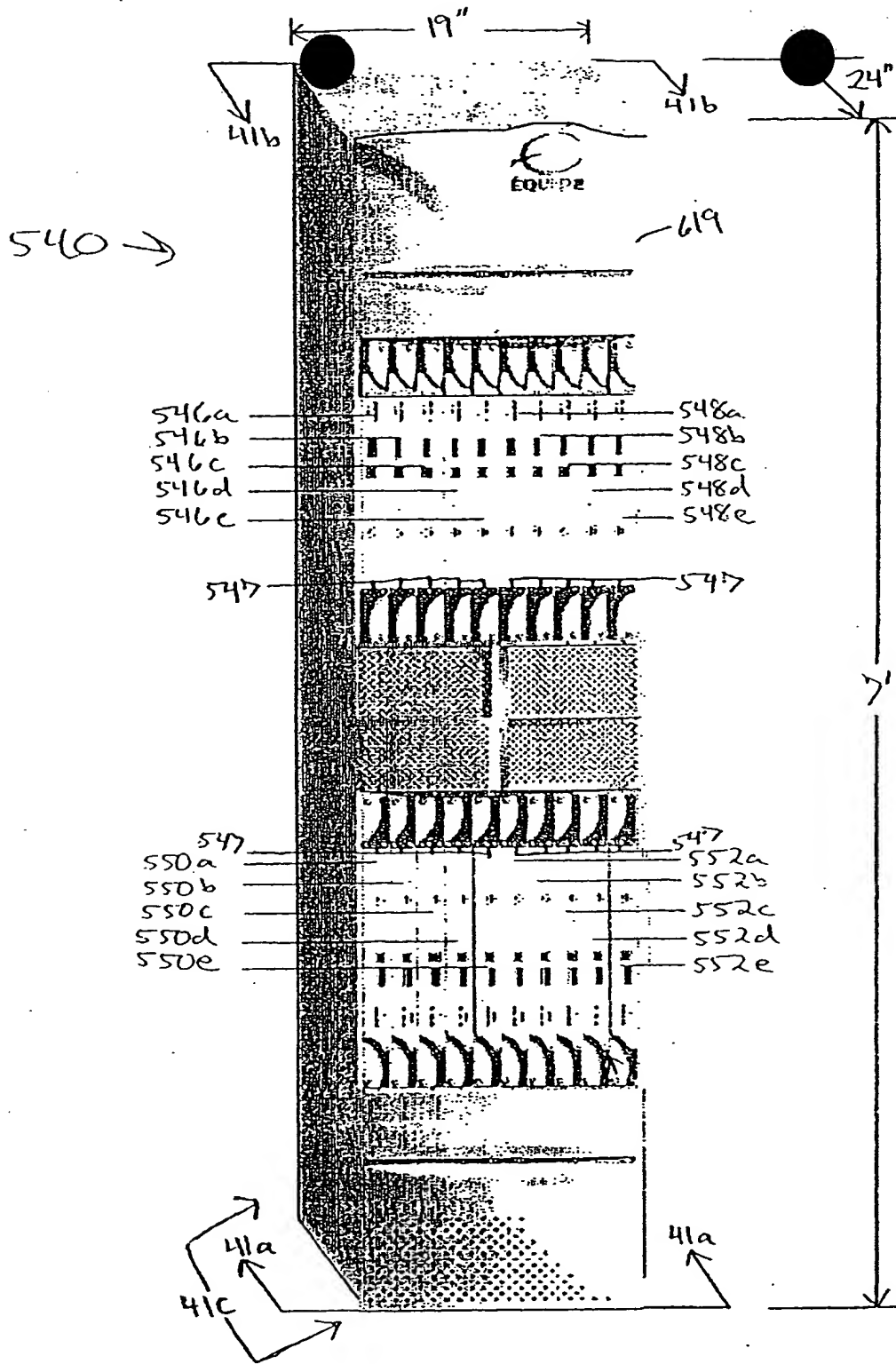


Fig. 40

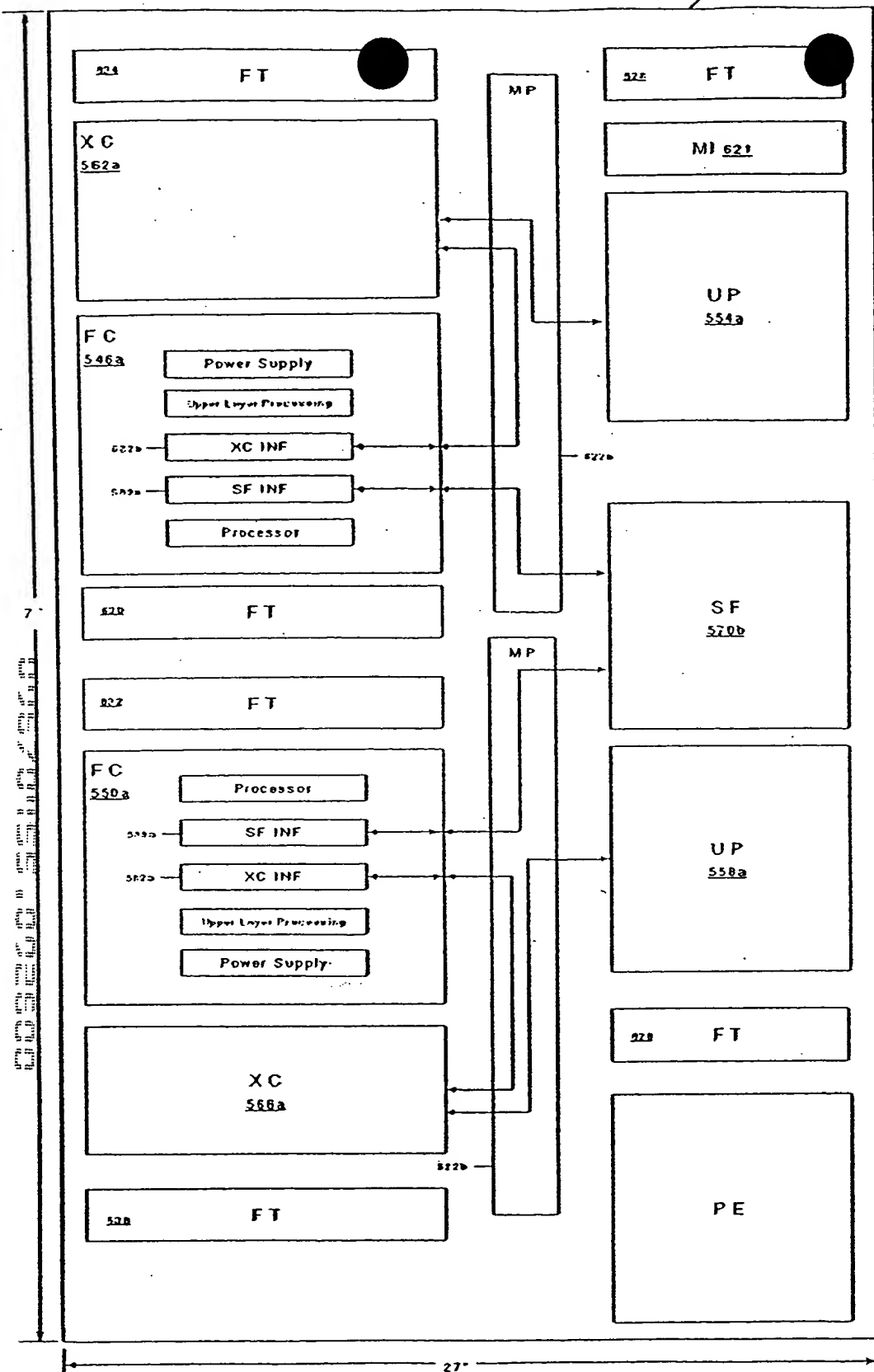


FIGURE 41C

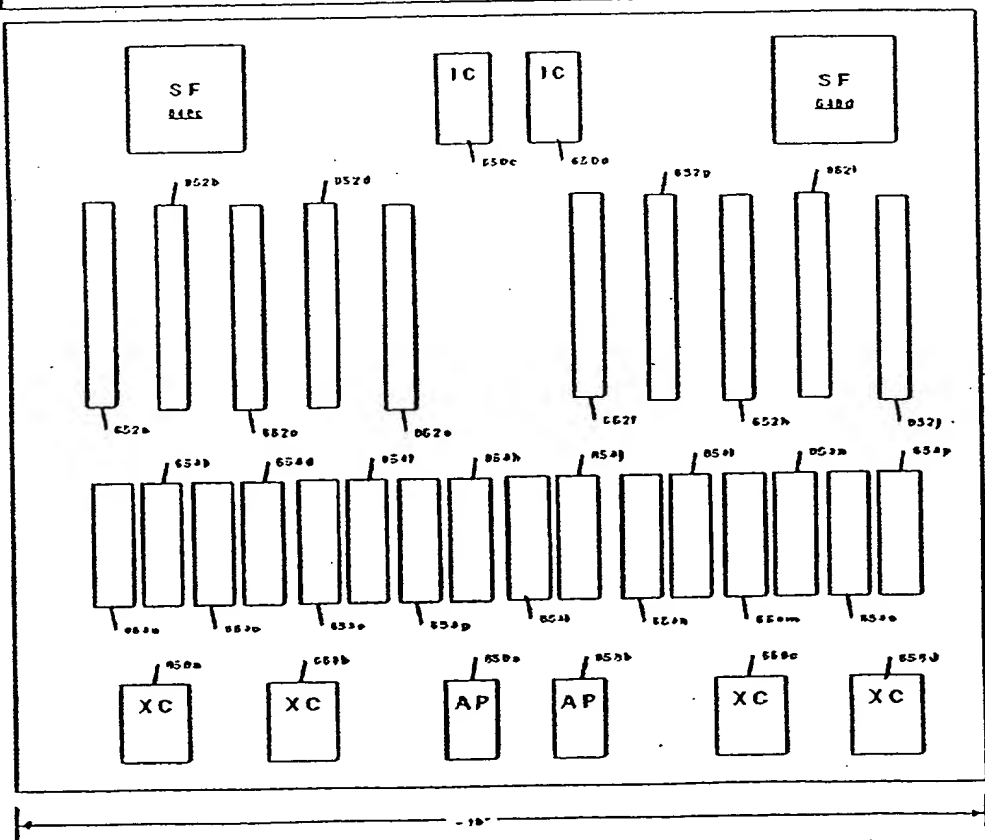
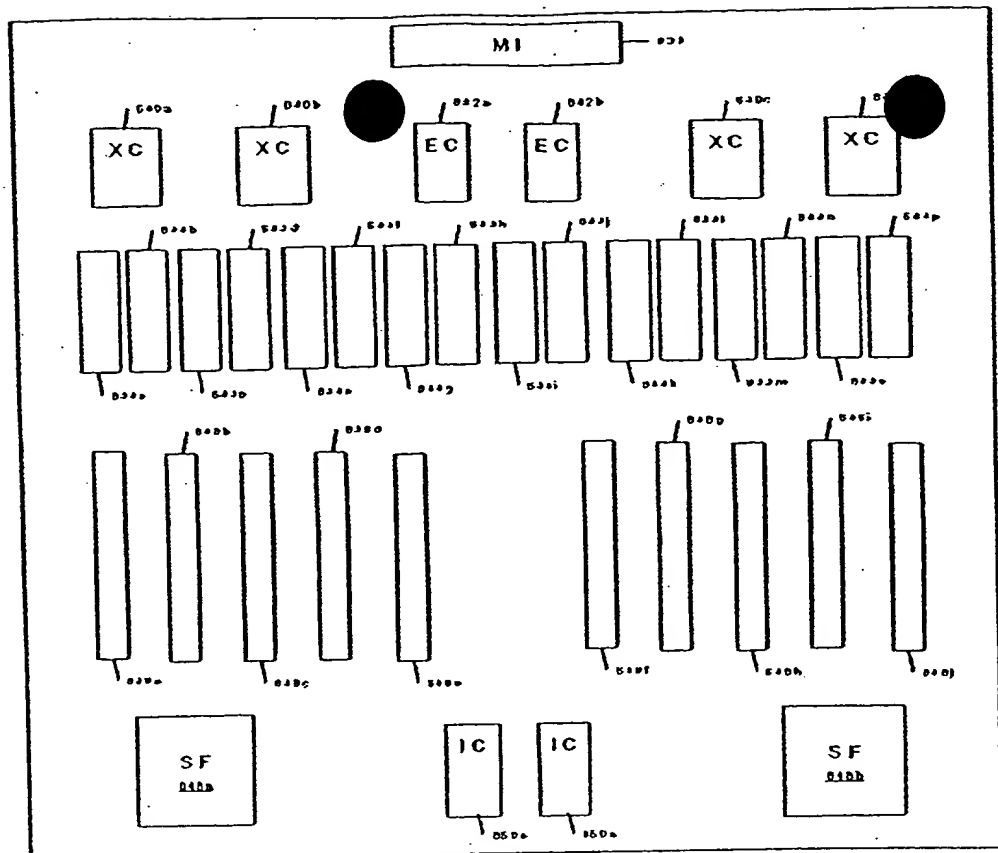
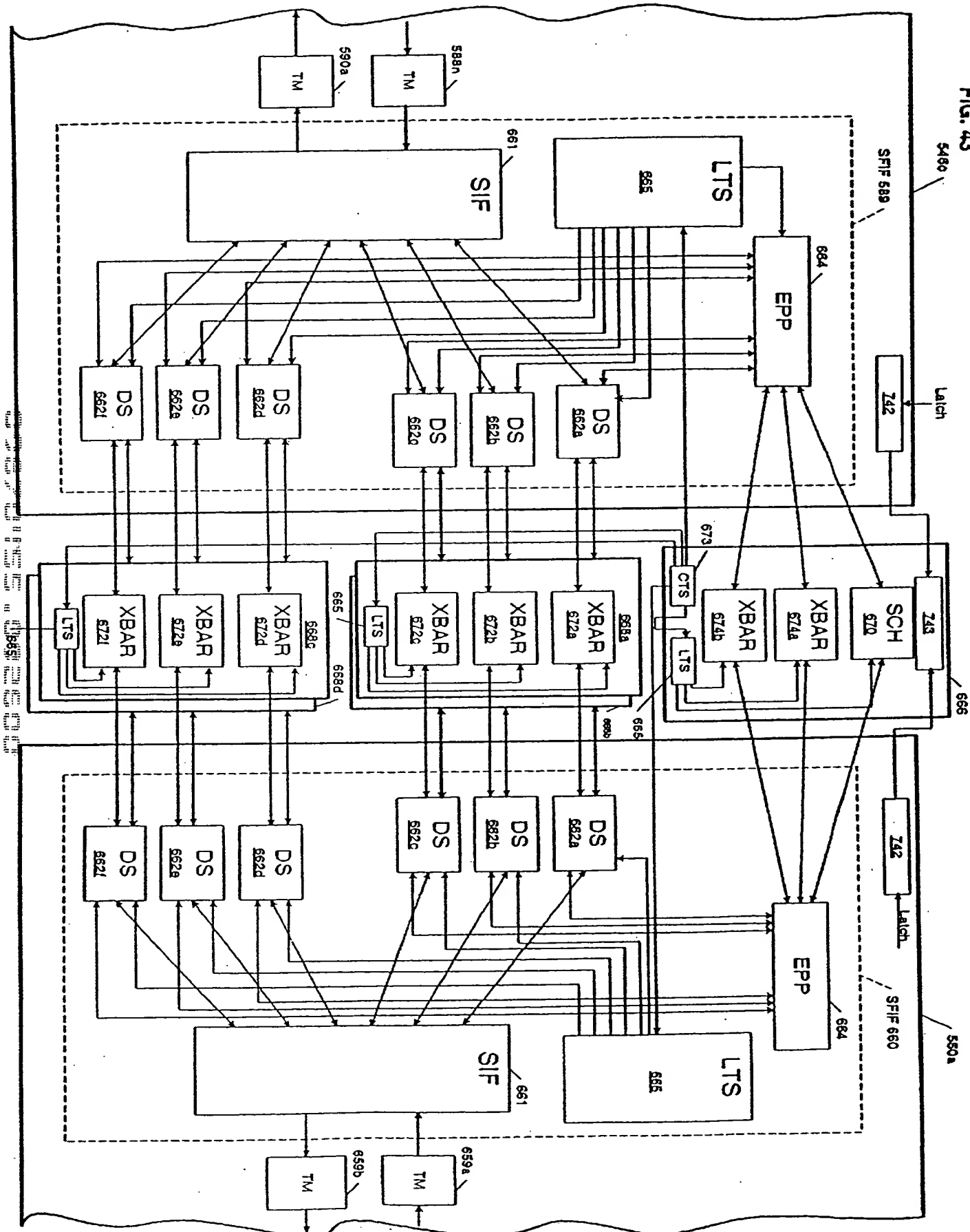


FIGURE 42

**FIG. 43**





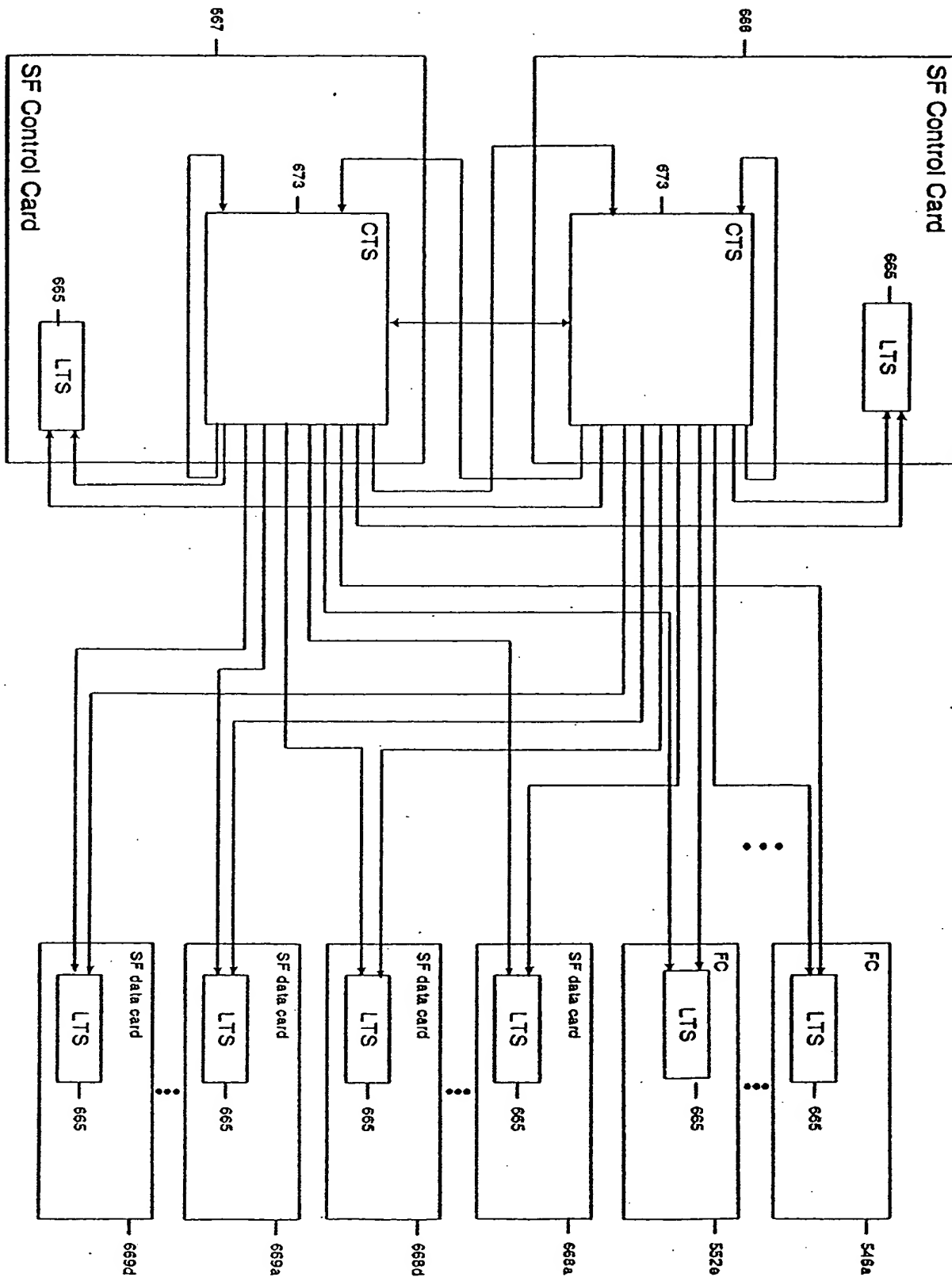


FIG. 44

540

FIG. 45

CTS

673

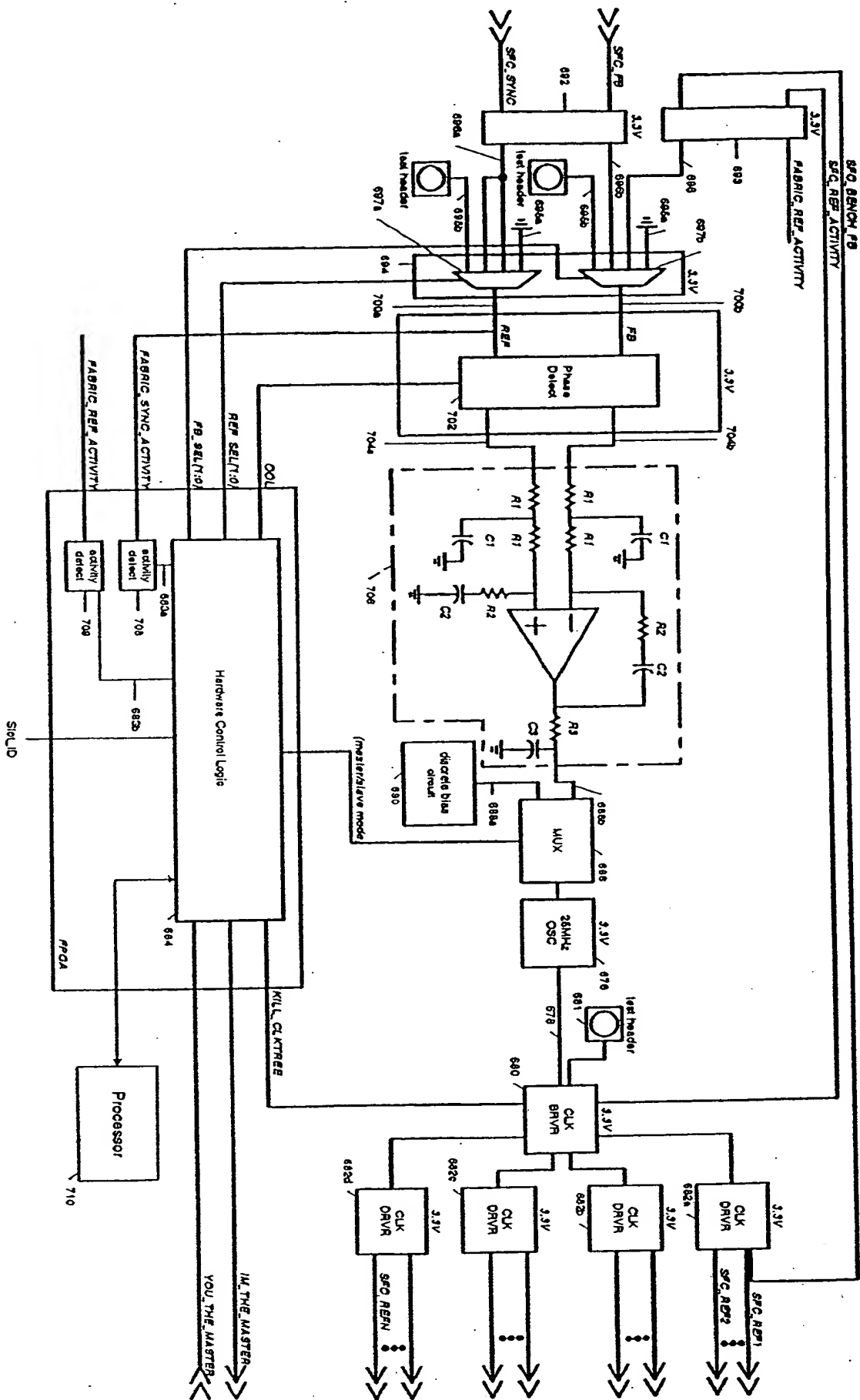


FIG. 45 is a block diagram of a hardware control logic system. The system includes a hardware control logic block (684) which is connected to a processor (710) via a serial link (704). The hardware control logic block (684) is also connected to a clock system (680) which includes a 28MHz oscillator (676) and a clock driver (682a, 682b, 682c). The clock system is connected to a multiplexer (688) and a diode bias circuit (690). The hardware control logic block (684) is also connected to a fabric sync activity block (682) and a fabric ref activity block (683). The fabric sync activity block (682) is connected to a fabric sel block (685) which is connected to a fabric sel driver (708). The fabric ref activity block (683) is connected to a fabric ref driver (708). The hardware control logic block (684) is also connected to a kill clktrres block (686) and an i/o the master block (686). The kill clktrres block (686) is connected to a kill clktrres driver (708). The i/o the master block (686) is connected to a processor (710). The hardware control logic block (684) is also connected to a phase detect block (702) and a test header block (691). The phase detect block (702) is connected to a phase detect driver (708). The test header block (691) is connected to a test header driver (708). The hardware control logic block (684) is also connected to a fabric sync activity block (682) and a fabric ref activity block (683). The fabric sync activity block (682) is connected to a fabric sel block (685) which is connected to a fabric sel driver (708). The fabric ref activity block (683) is connected to a fabric ref driver (708). The hardware control logic block (684) is also connected to a kill clktrres block (686) and an i/o the master block (686). The kill clktrres block (686) is connected to a kill clktrres driver (708). The i/o the master block (686) is connected to a processor (710). The hardware control logic block (684) is also connected to a phase detect block (702) and a test header block (691). The phase detect block (702) is connected to a phase detect driver (708). The test header block (691) is connected to a test header driver (708).

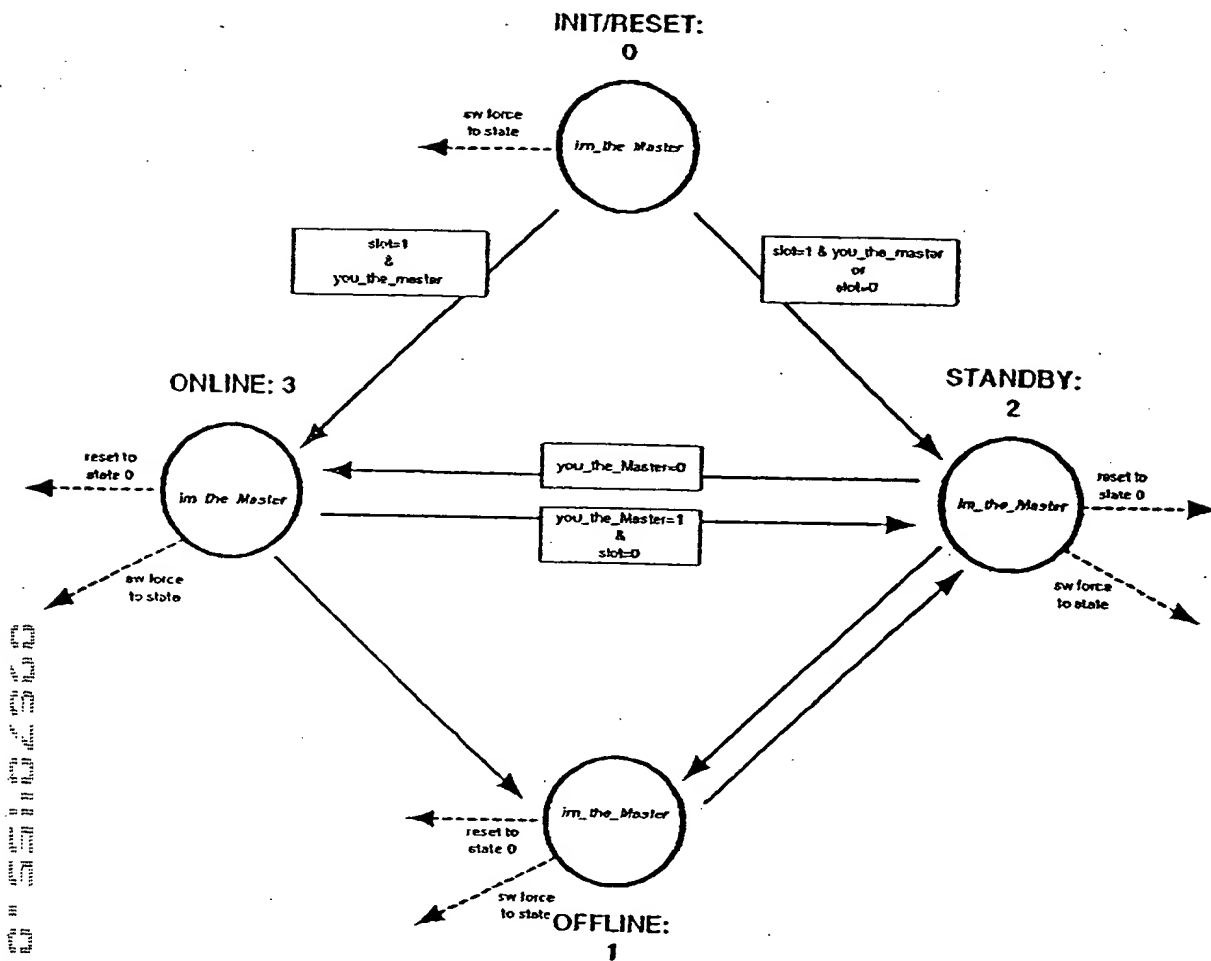


FIG. 46



**FIG. 47**

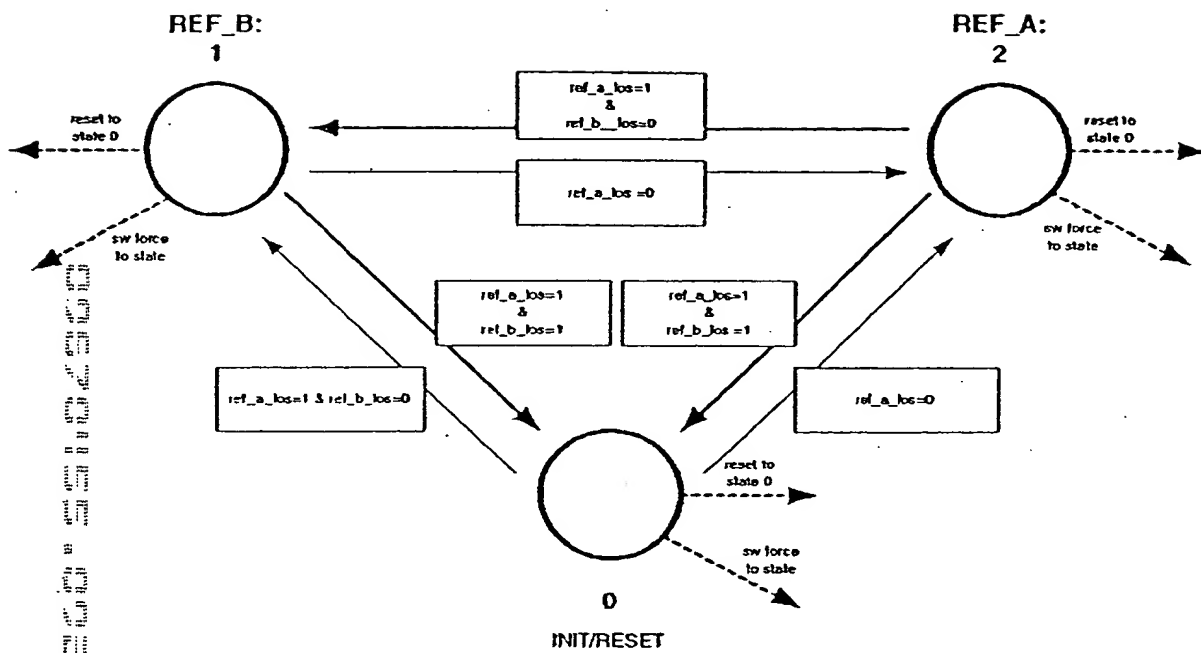


FIG. 48

540

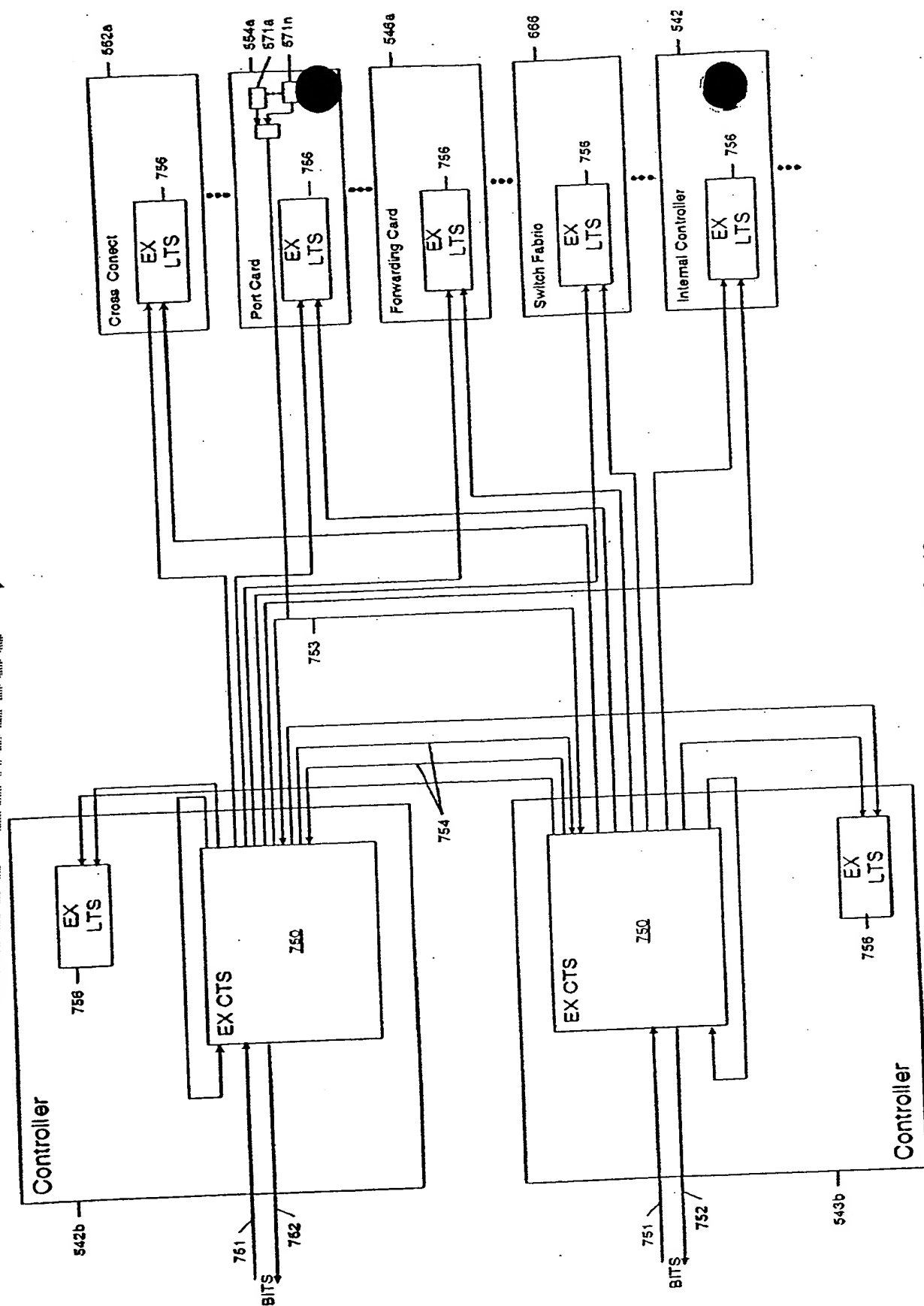
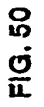


FIG. 49



**FIG. 50**

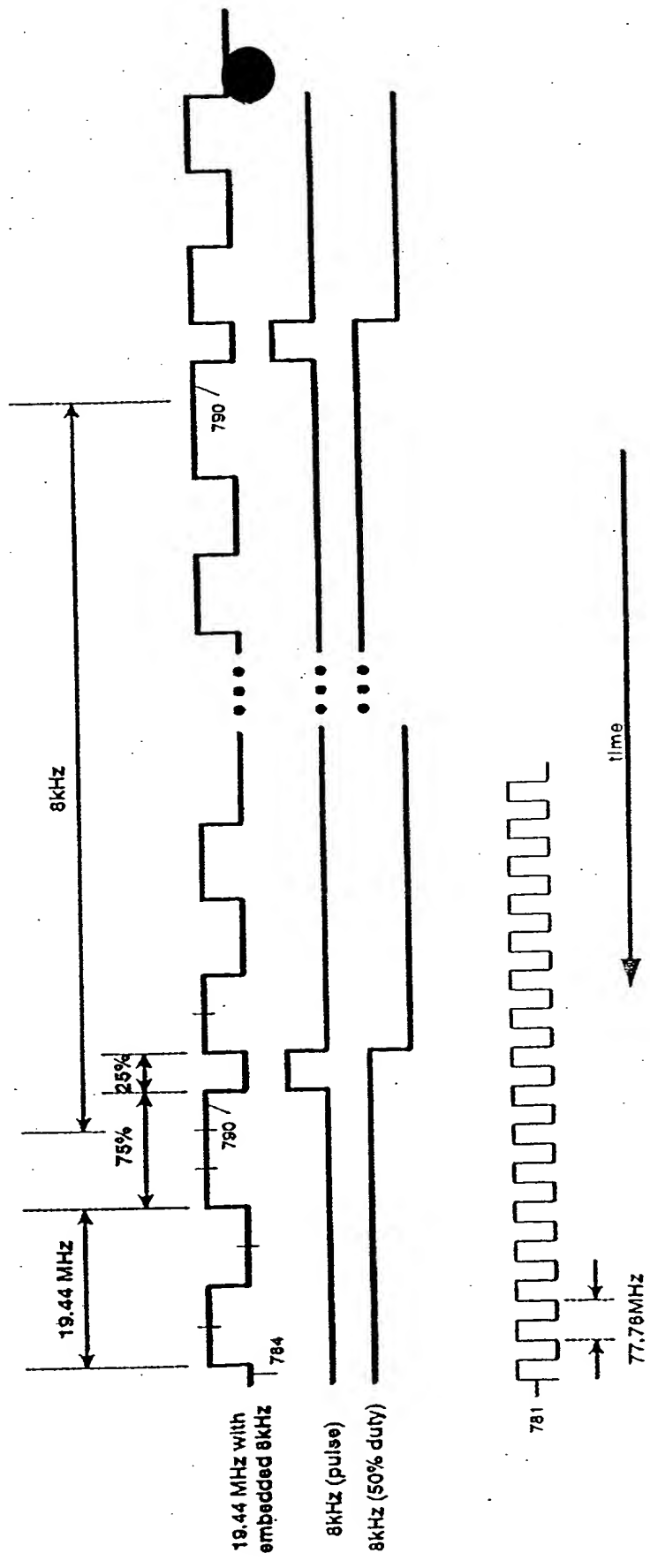


FIG. 51



792

792

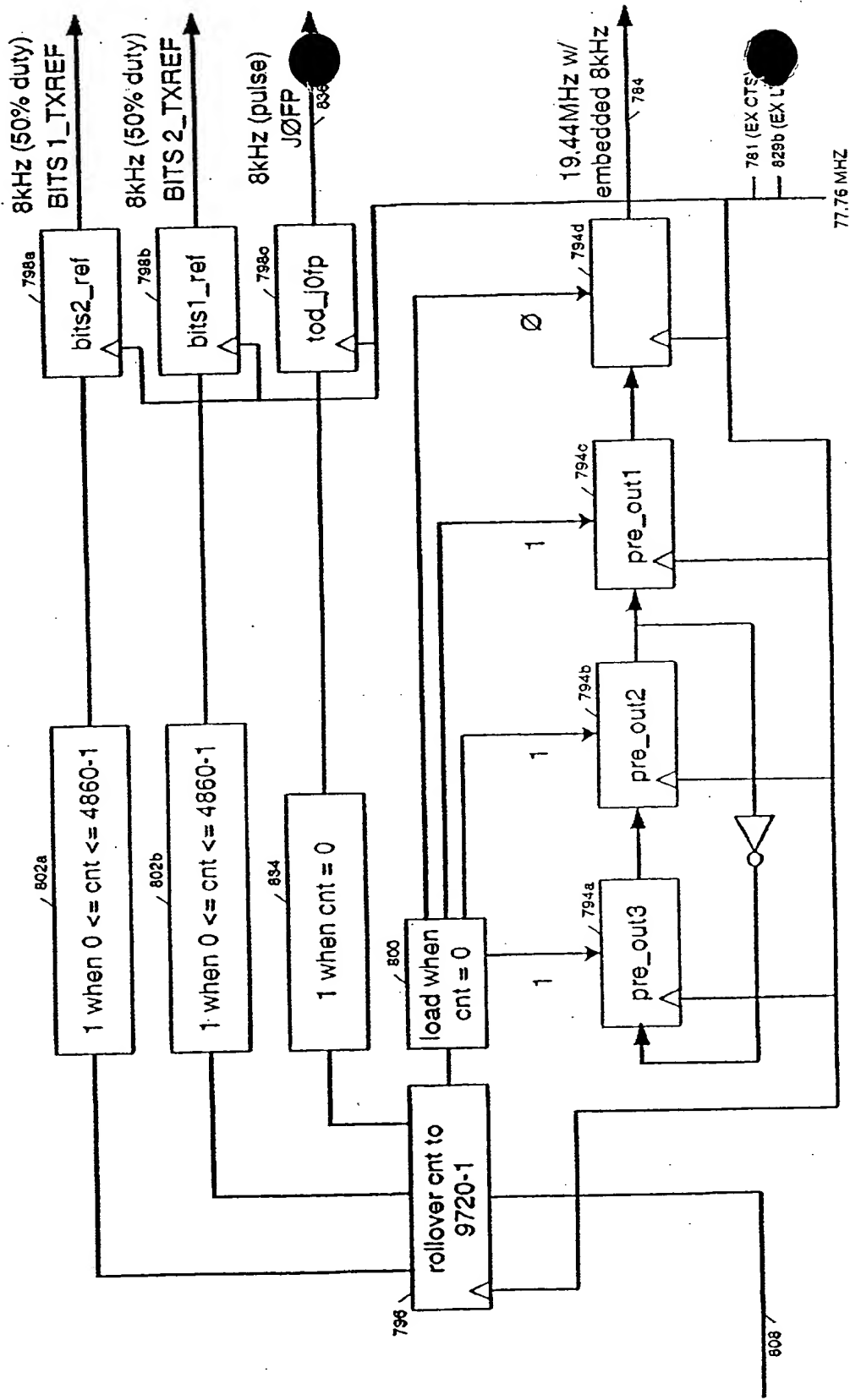
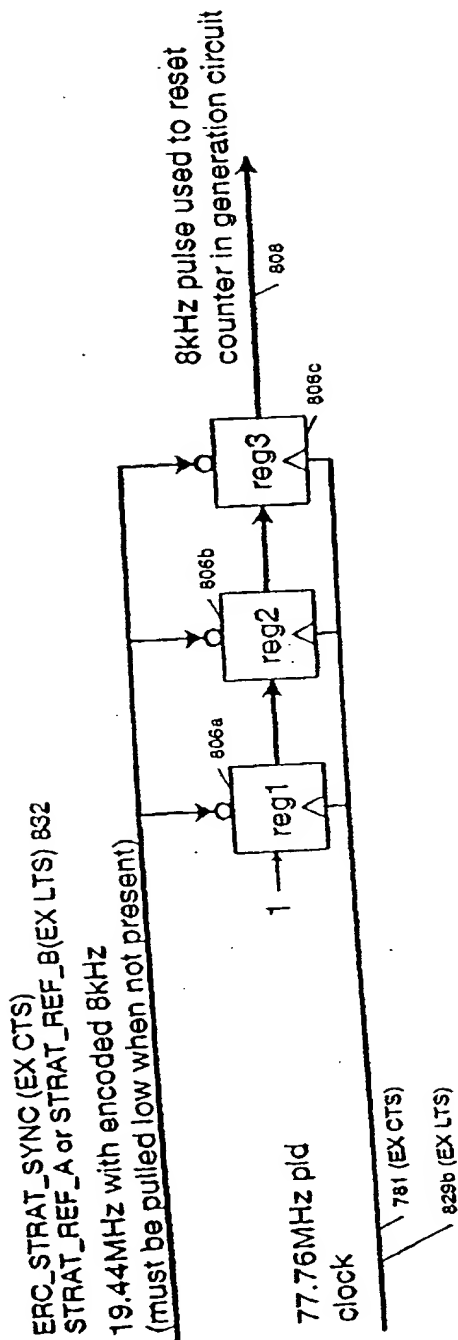


Fig. 52

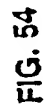
ERC\_STRAT\_SYNC (EX CTS)  
STRAT\_REF\_A or STRAT\_REF\_B (EX LTS) 832  
19.44MHz with encoded 8kHz  
(must be pulled low when not present)

804



Extractor

FIG. 53



**FIG. 54**

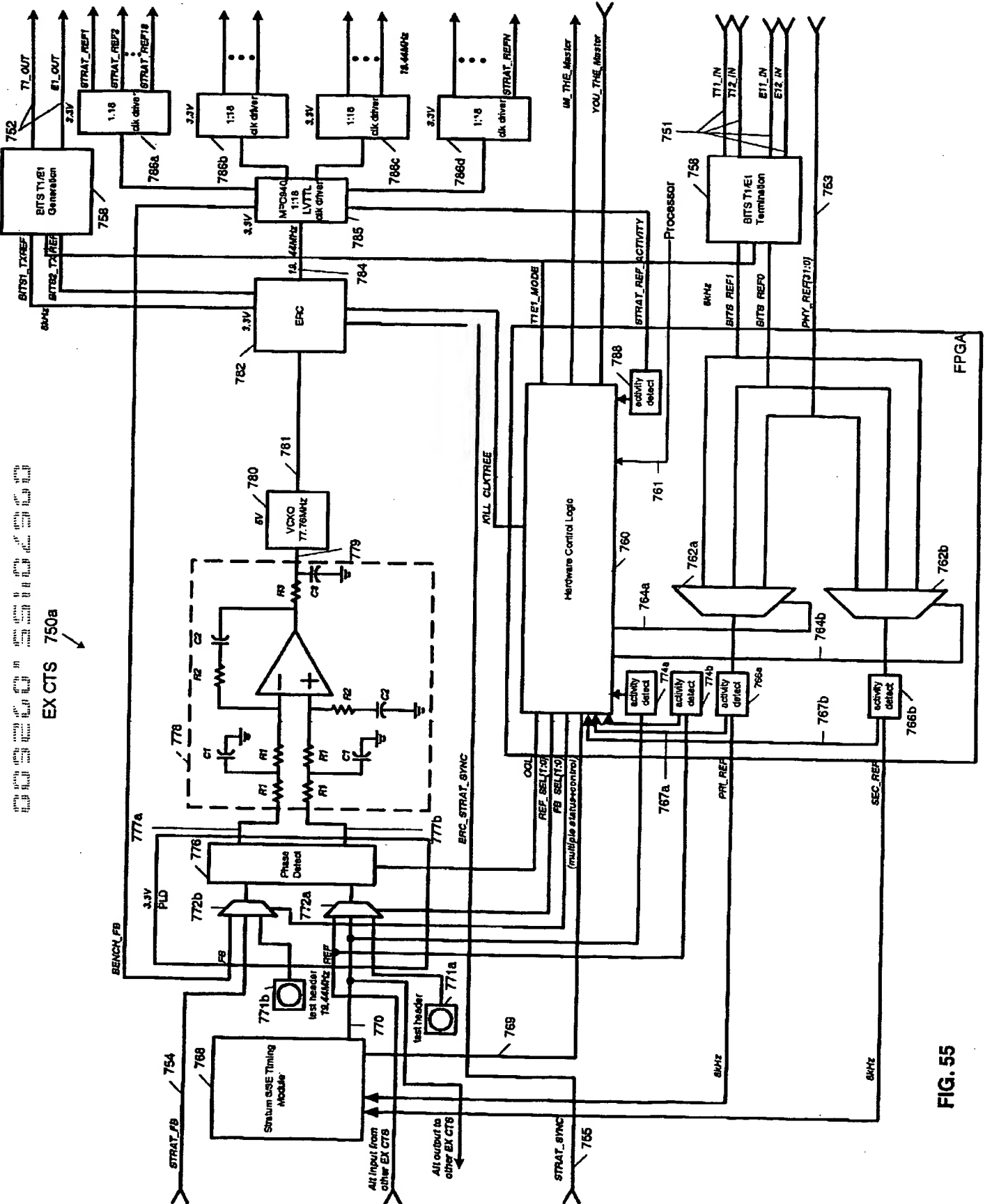


FIG. 55

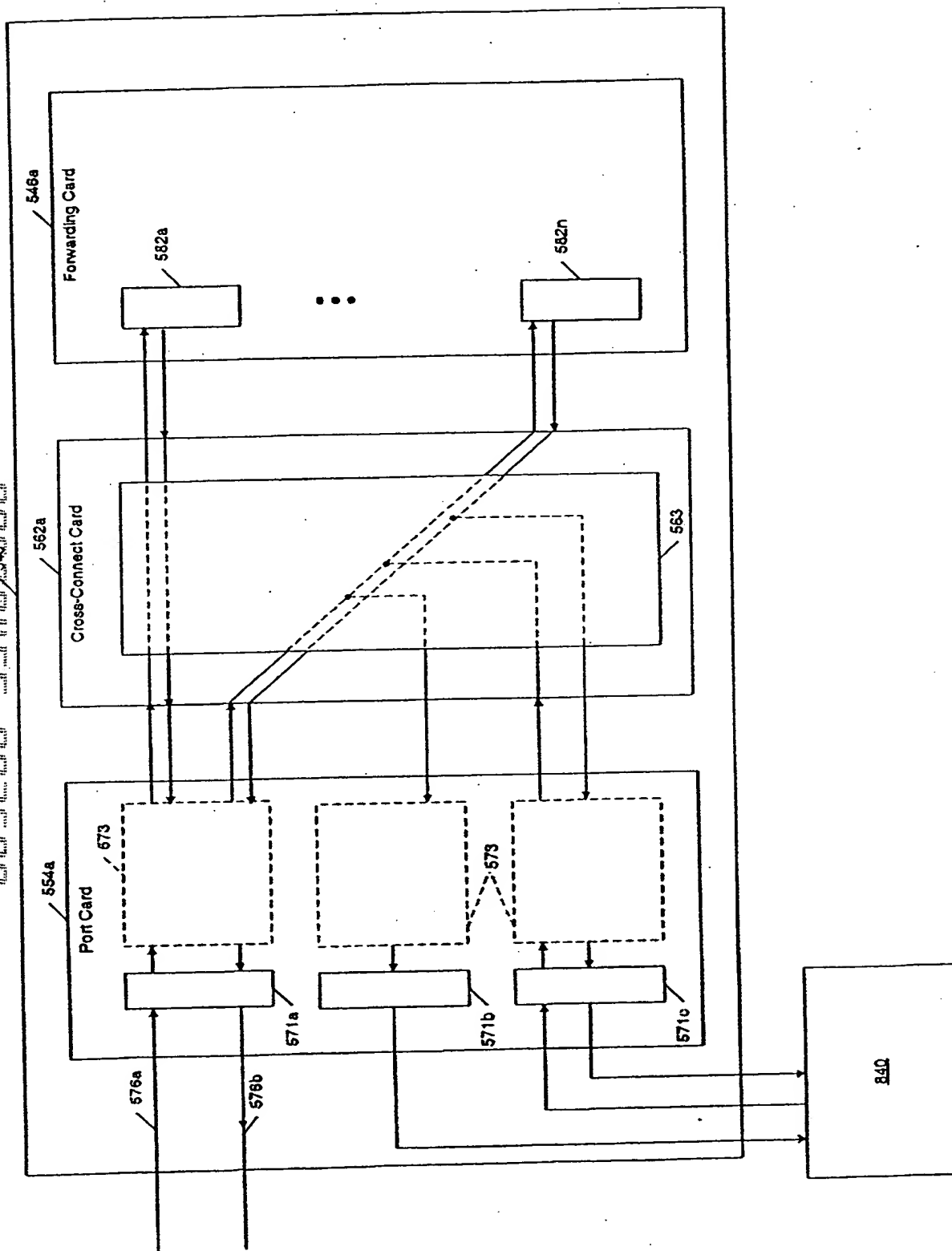


Fig. 56

FIG.57

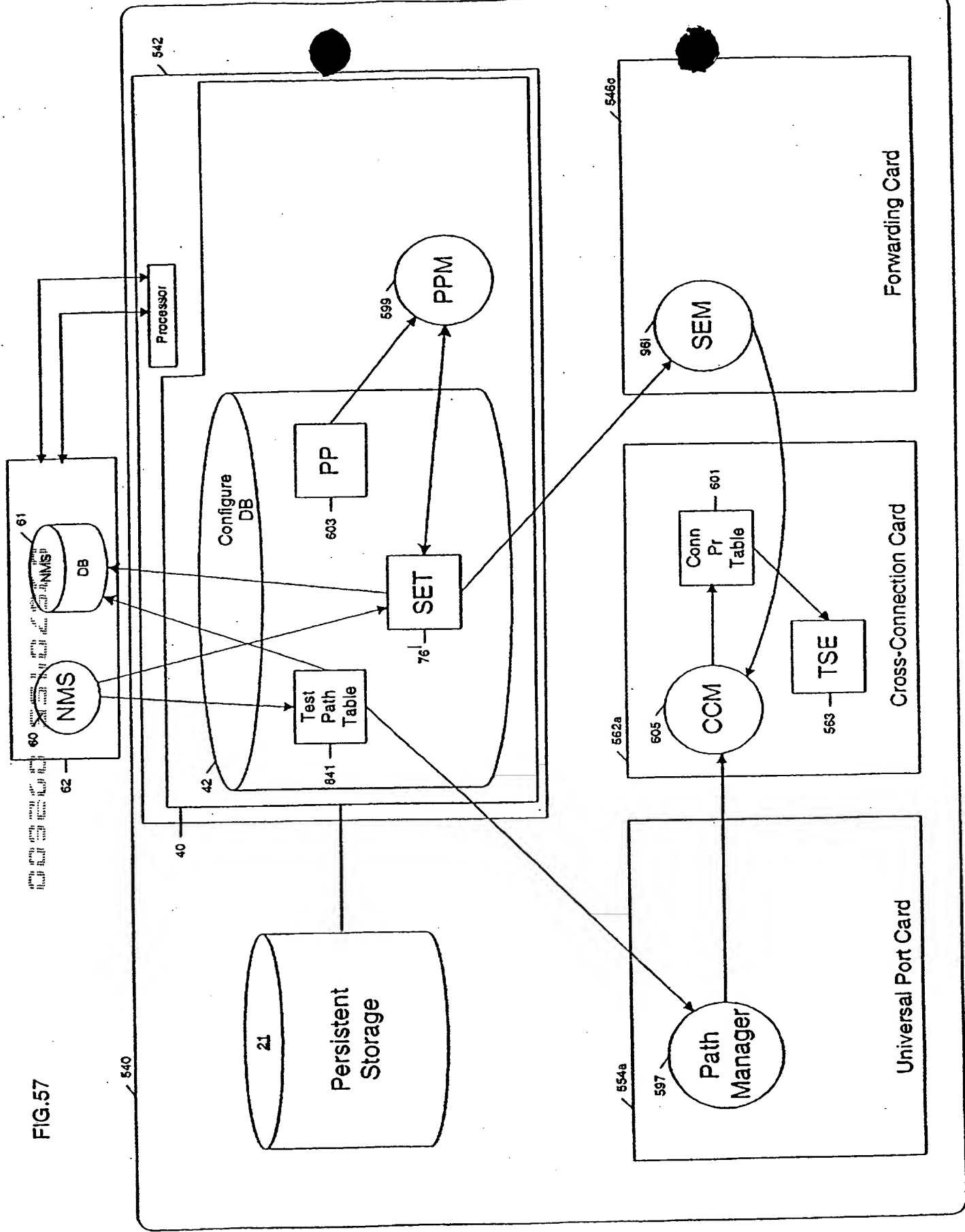


FIG. 58

Test Path Table 841

Path LID	UP Port LID	Time Slot	# of Time Slots	Monitor	Enable Port Receiver	...
842 — 1666	1232	4	3	Ingress	No	
843 — 1666	1233	4	3	Egress	No	
844 — 1666	1233	4	3	Ingress	Yes	
• •	• •	• •	• •			• •

895

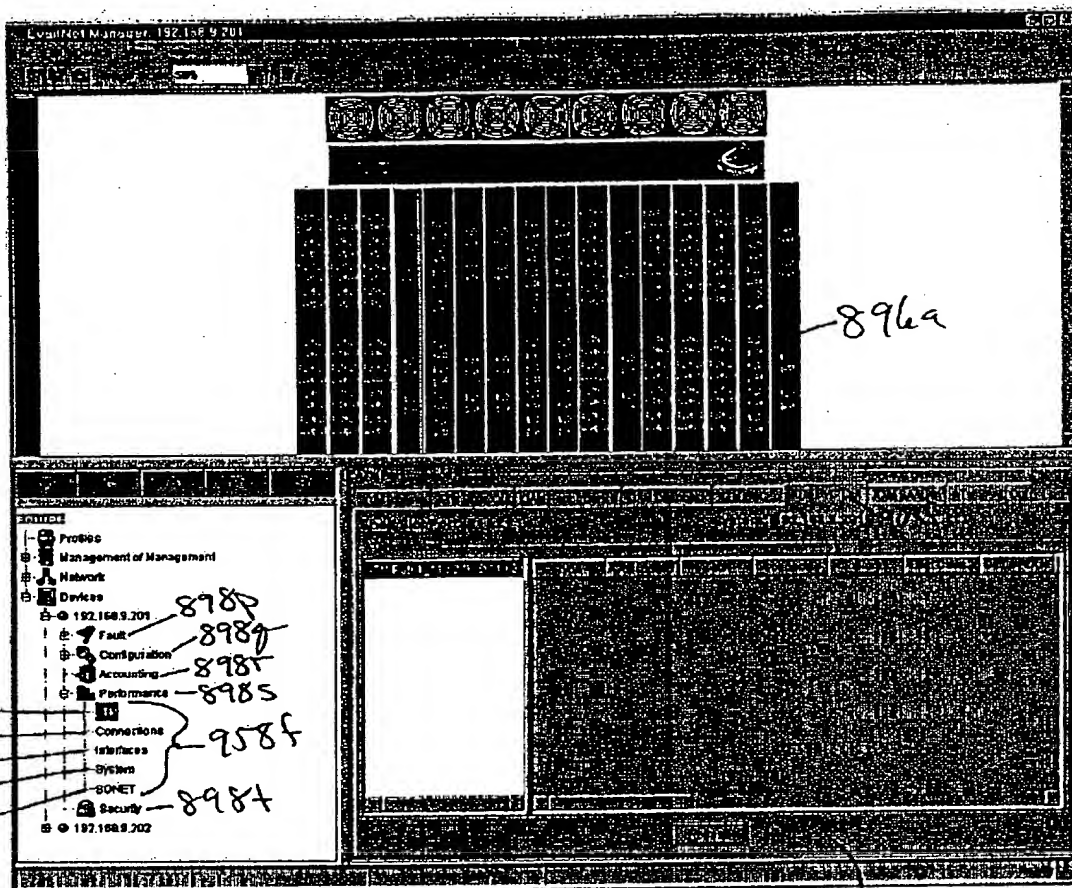


Fig. 7+